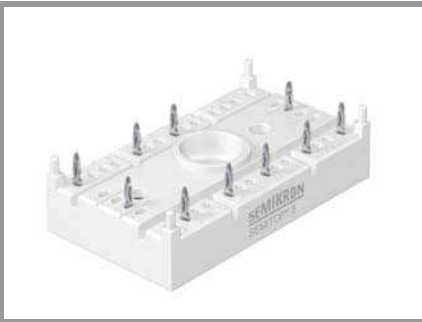


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SEMITOP® 3 Press-Fit

3-Level NPC Inverter

SK 30 MLI 066p

Features

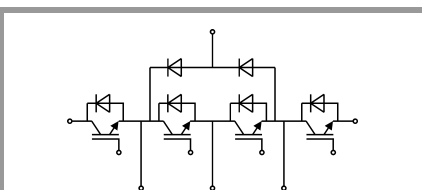
- One screw mounting module
- Solder free mounting with Press-Fit terminals
- Fully compatible with other SEMITOP® Press-Fit types
- Improved thermal performances by aluminium oxide substrate
- Trench IGBT technology
- CAL technology FWD
- Integrated NTC temperature sensor
- UL recognized, file no. E 63 532

Typical Applications*

- 3 Level Inverter
- UPS

Remarks

Dynamic measure: DUT=IGBT (gate pin1) and Neutral Clamp Diode (cathode pin16) as free-wheeling diode



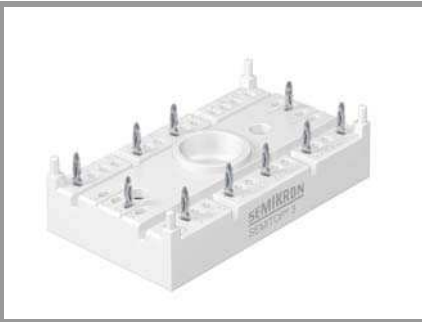
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Absolute Maximum Ratings

Symbol	Conditions	Values	Unit	
IGBT - Inverter				
V_{CES}		600	V	
I_C	$T_j = 175\text{ °C}$	$T_s = 25\text{ °C}$	37	A
		$T_s = 70\text{ °C}$	30	A
I_{Cnom}		30	A	
I_{CRM}	$I_{CRM} = 2 \times I_{Cnom}$	60	A	
V_{GES}		-20 ... 20	V	
t_{psc}	$V_{CC} = 360\text{ V}$ $V_{GE} \leq 15\text{ V}$ $V_{CES} \leq 600\text{ V}$	$T_j = 150\text{ °C}$	6	μs
T_j		-40 ... 175	$^{\circ}\text{C}$	
Inverse diode				
I_F	$T_j = 175\text{ °C}$	$T_s = 25\text{ °C}$	34	A
		$T_s = 70\text{ °C}$	27	A
I_{Fnom}		30	A	
I_{FRM}	$I_{FRM} = 2 \times I_{Fnom}$	60	A	
I_{FSM}	$t_p = 10\text{ ms, sin } 180^{\circ}, T_j = 25\text{ °C}$	185	A	
T_j		-40 ... 175	$^{\circ}\text{C}$	
Clamping diode				
I_F	$T_j = 175\text{ °C}$	$T_s = 25\text{ °C}$	34	A
		$T_s = 70\text{ °C}$	27	A
I_{Fnom}		30	A	
I_{FRM}	$I_{FRM} = 2 \times I_{Fnom}$	60	A	
I_{FSM}	$10\text{ ms, sin } 180^{\circ}, T_j = 25\text{ °C}$	185	A	
T_j		-40 ... 175	$^{\circ}\text{C}$	
Module				
$I_{t(RMS)}$	$T_s = 60\text{ °C}$	40	A	
T_{stg}		-40 ... 125	$^{\circ}\text{C}$	
V_{isol}	AC, sinusoidal, $t = 1\text{ min}$	2500	V	

Characteristics

Symbol	Conditions	min.	typ.	max.	Unit
IGBT					
$V_{CE(sat)}$	$I_C = 30\text{ A}$ $V_{GE} = 15\text{ V}$ chipelevel	$T_j = 25\text{ °C}$	1.45	1.90	V
		$T_j = 150\text{ °C}$	1.65	2.10	V
V_{CE0}	chipelevel	$T_j = 25\text{ °C}$	0.9	1	V
		$T_j = 150\text{ °C}$	0.85	0.9	V
r_{CE}	$V_{GE} = 15\text{ V}$	$T_j = 25\text{ °C}$	18.3	30	$\text{m}\Omega$
		$T_j = 150\text{ °C}$	26.7	40	$\text{m}\Omega$
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_C = 0.43\text{ mA}$	5	5.8	6.5	V
I_{CES}	$V_{GE} = 0\text{ V}$ $V_{CE} = 600\text{ V}$	$T_j = 25\text{ °C}$		0.05	mA
					mA
C_{ies}	$V_{CE} = 25\text{ V}$		1.63		nF
C_{oes}	$V_{GE} = 0\text{ V}$		0.108		nF
C_{res}			0.05		nF
Q_G	$V_{GE} = -7\text{V...}+15\text{V}$		275		nC
R_{Gint}	$T_j = 25\text{ °C}$		0		Ω



SEMITOP® 3 Press-Fit

3-Level NPC Inverter

SK 30 MLI 066p

Features

- One screw mounting module
- Solder free mounting with Press-Fit terminals
- Fully compatible with other SEMITOP® Press-Fit types
- Improved thermal performances by aluminium oxide substrate
- Trench IGBT technology
- CAL technology FWD
- Integrated NTC temperature sensor
- UL recognized, file no. E 63 532

Typical Applications*

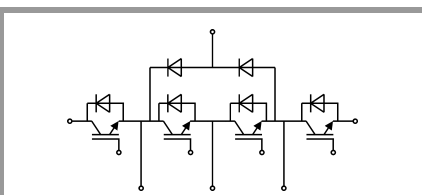
- 3 Level Inverter
- UPS

Remarks

Dynamic measure: DUT=IGBT (gate pin1) and Neutral Clamp Diode (cathode pin16) as free-wheeling diode

Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
$t_{d(on)}$	$V_{CC} = 300\text{ V}$	$T_j = 150\text{ °C}$		24		ns
t_r	$I_C = 30\text{ A}$	$T_j = 150\text{ °C}$		27		ns
E_{on}	$V_{GE} = +15/-7\text{ V}$	$T_j = 150\text{ °C}$		0.97		mJ
$t_{d(off)}$	$di/dt_{on} = 2335\text{ A}/\mu\text{s}$	$T_j = 150\text{ °C}$		328		ns
t_f	$R_{G\ on} = 25\ \Omega$	$T_j = 150\text{ °C}$		54		ns
E_{off}	$R_{G\ off} = 25\ \Omega$	$T_j = 150\text{ °C}$		1.77		mJ
$R_{th(j-s)}$	per IGBT			1.65		K/W

Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Inverse diode						
$V_F = V_{EC}$	$I_F = 30\text{ A}$	$T_j = 25\text{ °C}$		1.50	1.65	V
	chiplevel	$T_j = 150\text{ °C}$		1.50	1.65	V
V_{F0}		$T_j = 25\text{ °C}$		1	1.1	V
		$T_j = 150\text{ °C}$		0.8	0.9	V
r_F		$T_j = 25\text{ °C}$		16.7	20	m Ω
		$T_j = 150\text{ °C}$		20	23.3	m Ω
I_{RRM}	$I_F = 30\text{ A}$	$T_j = 150\text{ °C}$		30		A
Q_{rr}	$di/dt_{off} = 2335\text{ A}/\mu\text{s}$	$T_j = 150\text{ °C}$		1.6		μC
E_{rr}	$V_{GE} = -7\text{ V}$	$T_j = 150\text{ °C}$		0.26		mJ
	$V_R = 300\text{ V}$					
$R_{th(j-s)}$	per Diode			2.3		K/W
Clamping diode						
$V_F = V_{EC}$	$I_F = 30\text{ A}$	$T_j = 25\text{ °C}$		1.50	1.65	V
	chiplevel	$T_j = 150\text{ °C}$		1.50	1.65	V
V_{F0}		$T_j = 25\text{ °C}$		1	1.1	V
		$T_j = 150\text{ °C}$		0.8	0.9	V
r_F		$T_j = 25\text{ °C}$		16.7	20	m Ω
		$T_j = 150\text{ °C}$		20	23.3	m Ω
I_{RRM}	$I_F = 30\text{ A}$	$T_j = 150\text{ °C}$		30		A
Q_{rr}	$di/dt_{off} = 2335\text{ A}/\mu\text{s}$	$T_j = 150\text{ °C}$		1.6		μC
E_{rr}	$V_{GE} = -7\text{ V}$	$T_j = 150\text{ °C}$		0.26		mJ
	$V_R = 300\text{ V}$					
$R_{th(j-s)}$	per Diode			2.3		K/W
Module						
M_s	to heatsink		2.25		2.5	Nm
w				30		g
Temperature Sensor						
R_{100}						Ω
$B_{100/125}$						K



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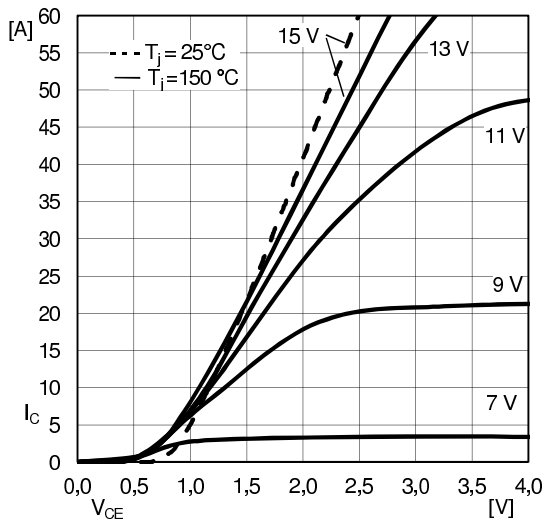


Fig. 1: Typ. IGBT 1 output characteristic, inclusive $R_{CC'+EE'}$

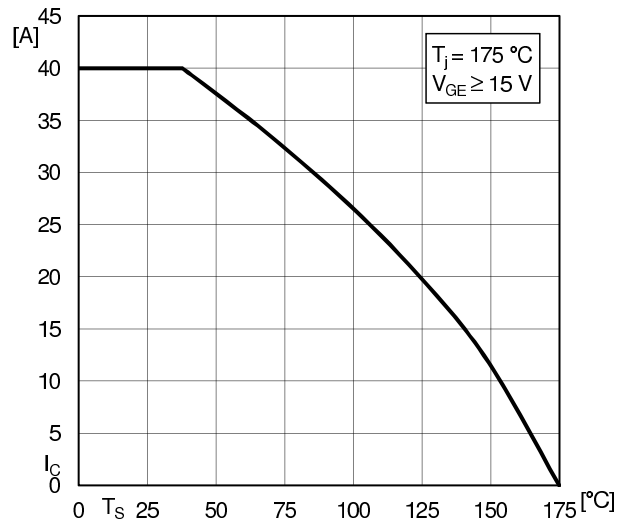


Fig. 2: Rated current vs. temperature $I_C = f(T_S)$

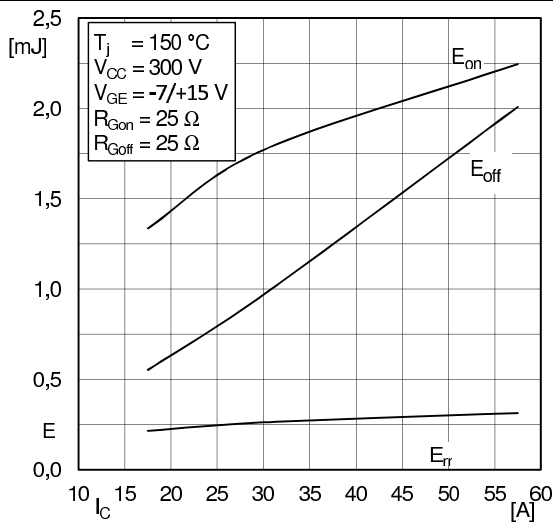


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

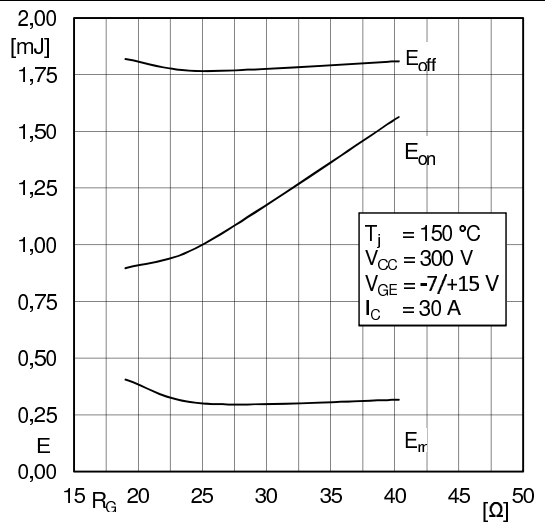


Fig. 4: Typ. turn-on /-off energy = $f(R_G)$

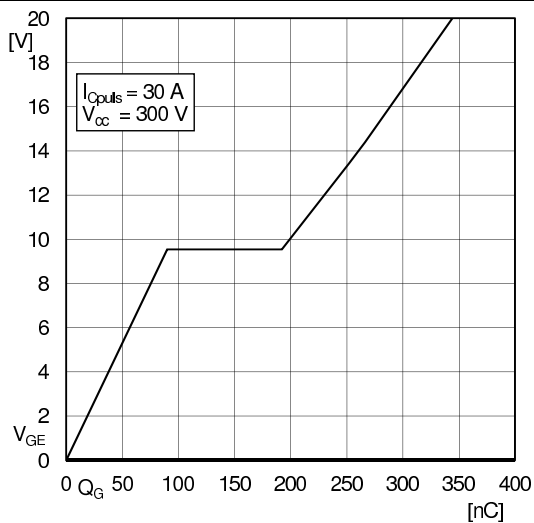


Fig. 6: Typ. gate charge characteristic

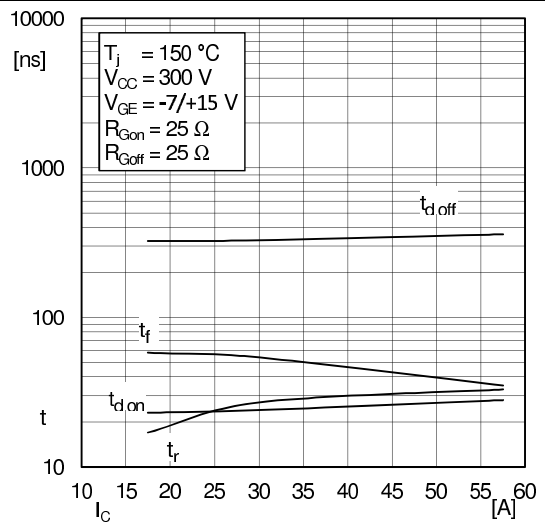


Fig. 7: Typ. switching times vs. I_C

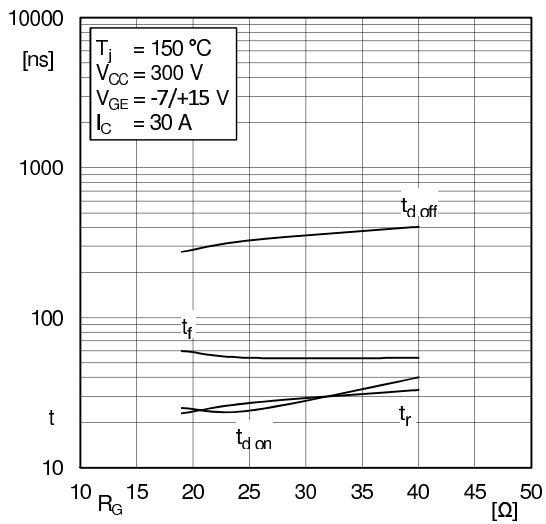


Fig. 8: Typ. switching times vs. gate resistor R_G

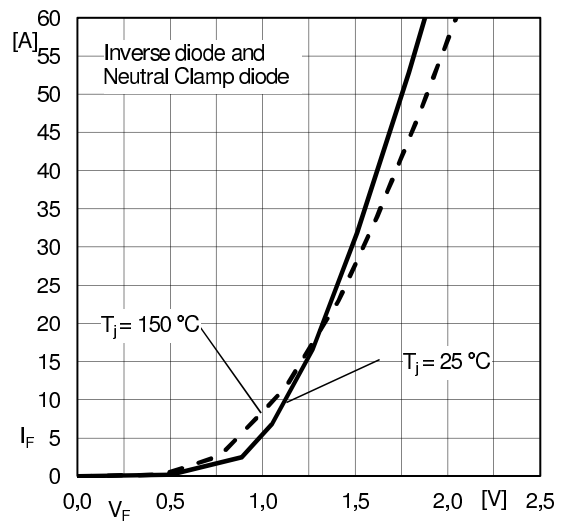
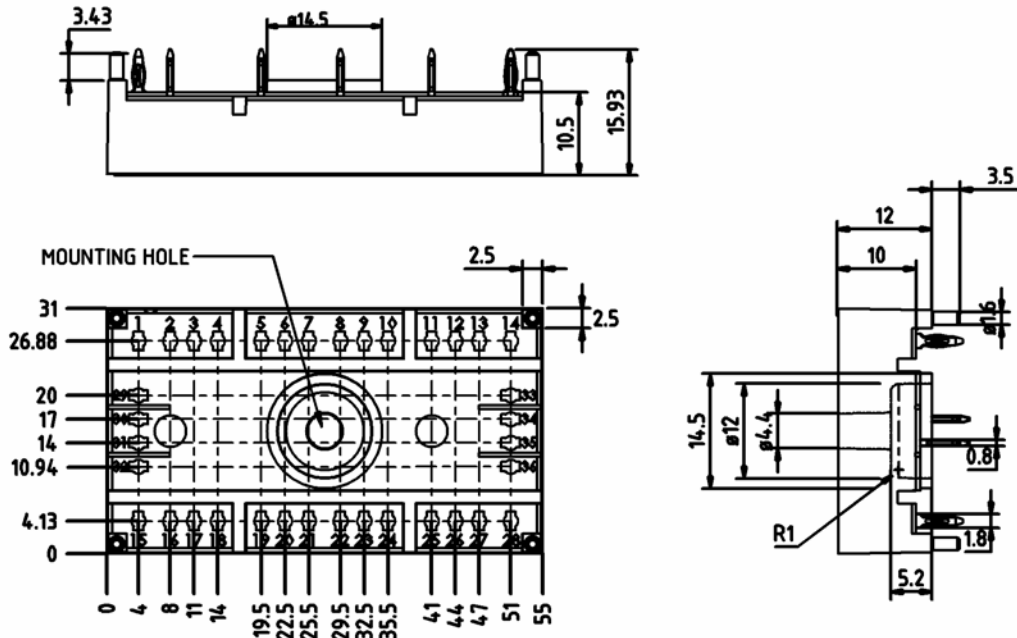


Fig. 10: Typ. Antiparallel and neutral clamp CAL diode forward charact., incl. $R_{CC'+EE'}$

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dimensions in mm
tolerance system: ISO 2768-m



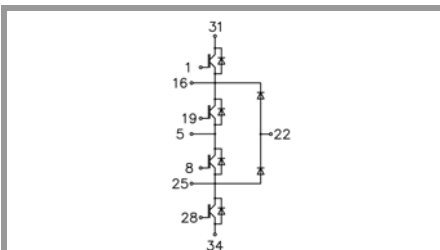
Suggested drilled hole diameter for terminal pins in the circuit board:

- minimum: 1,575mm
- typical: 1,6mm
- maximum: 1,625mm

Suggested hole diameter for the mounting pins in the circuit board: 2mm

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SEMITOP 3 Press-Fit



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This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX

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