# SK52B THRU SK5AB

# SCHOTTKY BARRIER RECTIFIER Reverse Voltage - 20 to 100 V Forward Current - 5 A

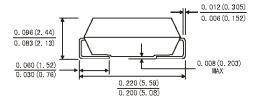
#### **Features**

- Plastic package has Underwriters Laboratory
   Flammability Classification 94V-0
- Metal silicon junction, majority carrier conduction
- · For surface mount applications
- · Low power loss, high efficieny
- · High current capability, low forward voltage drop
- · Low profile package
- · Built-in strain relief, ideal for automated placement
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications

#### **Mechanical Data**

- · Case: JEDEC SMB (DO-214AA) molded plastic body
- Terminals: solder plated, solderable per MIL-STD-750, method 2026
- · Polarity: color band denotes cathode end

# SMB(DO-214AA) 0. 086 (2. 2) 0. 075 (1. 91) 0. 187 (4. 76) 0. 160 (4. 06)



Dimensions in inches and (millimeters)

### **Maximum Ratings and Electrical Characteristics**

Ratings at 25 °C ambient temperature unless otherwise specified. Single phase, half wave, resistive or inductive load. For capacitive load, derate by 20%.

Parameter	Symbols	SK52B	SK53B	SK54B	SK55B	SK56B	SK58B	SK5AB	Units
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	20	30	40	50	60	80	100	V
Maximum RMS Voltage	$V_{RMS}$	14	21	28	35	42	57	71	V
Maximum DC Blocking Voltage	$V_{DC}$	20	30	40	50	60	80	100	V
Maximum Average Forward Rectified Current 0.375"(9.5mm) Lead Length	I <sub>F(AV)</sub>	5							Α
Peak Forward Surge Current, 8.3 ms Single Half Sine Wave Superimposed on Rated Load (JEDEC method at Rated T <sub>L</sub> )	I <sub>FSM</sub>	150							Α
Maximum Forward Voltage at 5 A 1)	$V_{F}$	0.55			0.	75	0.8	0.85	V
Maximum DC Reverse Current $T_a = 25 ^{\circ}\text{C}$ at Rated DC Blocking Voltage $T_a = 100 ^{\circ}\text{C}$	I <sub>R</sub>	0.5						- mA	
Typical Junction Capacitance 3)	CJ	500			400				pF
Typical Thermal Resistance <sup>2)</sup>	$R_{ heta JA} \ R_{ heta JL}$	55 17							°C/W
Operating Junction Temperature Range	T <sub>J</sub>	- 65 to + 125							°C
Storage Temperature Range	Ts	- 65 to + 150							°C

<sup>&</sup>lt;sup>1)</sup> Pulse test: 300 µs pulse width, 1% duty cycle

<sup>&</sup>lt;sup>3)</sup> Measured at 1 MHz and applied reverse voltage of 4 V



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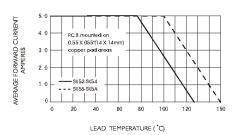




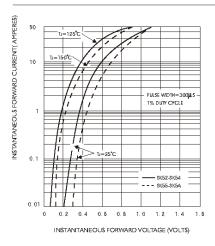
Dated: 13/10/2007

<sup>&</sup>lt;sup>2)</sup> P.C.B mounted 0.55 X 0.55" (14X14mm) copper pad areas

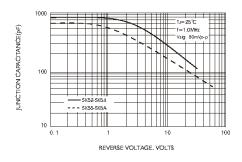
#### FIG. I-FORWARD CURRENT DERATING CURVE



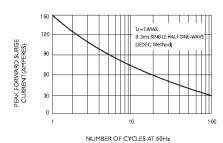
#### FIG.3-TYPICAL INSTANTANEOUS FORWARD **CHARACTERISTICS**



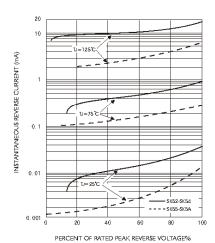
## FIG.5-TYPICAL JUNCTION CAPACITANCE



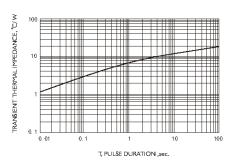
#### FIG.2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT



#### FIG.4-TYPICAL REVERSE CHARACTERISTICS



## FIG.6-TYPICAL TRANSIENT THERMAL IMPEDANCE





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