

Features

- Skysilicon Super_Junction Gen1 technology
- Much lower Ron*A performance for On-state efficiency
- Much lower FOM for fast switching efficiency

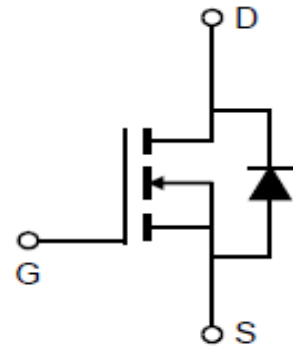
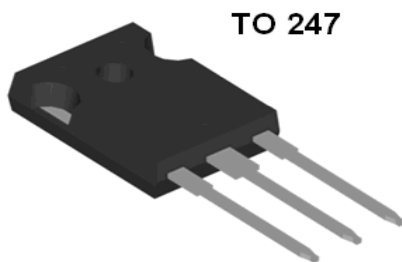
Applications

- LED/LCD/PDP TV and monitor Lighting
- Solar/Renewable/UPS-Micro Inverter System
- Charger
- Power Supply

Product Summary

V _{DS}	650V
R _{DS(on)_typ}	63.5mΩ
I _D	43A

100% Avalanche Tested


Package Marking and Ordering Information

Part #	Marking	Package	Packing	Reel Size	Tape Width	Qty
SKJQ80N65	-	TO-247	Tube	N/A	N/A	50pcs

Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-source voltage	V _{DS}	650	V
Continuous drain current T _C = 25°C T _C = 100°C	I _D	43 33.2	A
Pulsed drain current (T _C = 25°C, t _p limited by T _{jmax})	I _{D pulse}	172	A
Avalanche energy, single pulse (L=60mH, R _g =30Ω)	E _{AS}	1055	mJ
Gate-Source voltage	V _{GS}	±30	V
Power dissipation (T _C = 25°C)	P _{tot}	442	W
Operating junction and storage temperature	T _j , T _{stg}	-55...+150	°C

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal resistance, junction – case. Max	R_{thJC}	0.28	°C/W
Thermal resistance, junction – ambient. Max	R_{thJA}	37	

Electrical Characteristic (at $T_j = 25\text{ }^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		

Static Characteristic

Drain-source breakdown voltage	BV_{DSS}	650	-	-	V	$V_{GS}=0V, I_D=250\mu A$
Gate threshold voltage	$V_{GS(th)}$	3.2	3.7	4.2	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Zero gate voltage drain current	I_{DSS}	-	0.001	1	μA	$V_{DS}=650V, V_{GS}=0V$ $T_C=25^\circ C$ $T_C=150^\circ C$
Gate-source leakage current	I_{GSS}	-	1.1	80	nA	$V_{GS}=\pm 30V, V_{DS}=0V$
Drain-source on-state resistance	$R_{DS(on)}$	-	63.5	80.00	mΩ	$V_{GS}=10V, I_D=21.5A,$ $T_C=25^\circ C$ $T_C=150^\circ C$
Transconductance	g_{fs}	-	28	-	S	$V_{DS}=20V, I_D=21.5A$

Dynamic Characteristic

Input Capacitance	C_{iss}	-	3590	-	pF	$V_{GS}=0V, V_{DS}=100V,$ $f=1MHz$
Output Capacitance	C_{oss}	-	175	-		
Reverse Transfer Capacitance	C_{rss}	-	29	-		
Gate Total Charge	Q_G	-	94	-	nC	$V_{GS}=10V, V_{DS}=480V,$ $I_D=21.5A, f=1MHz$
Gate-Source charge	Q_{gs}	-	20	-		
Gate-Drain charge	Q_{gd}	-	29	-		
Turn-on delay time	$t_{d(on)}$	-	35	-	ns	$T_j=25^\circ C, V_{GS}=10V,$ $I_D=21.5A, V_{DS}=400V,$ $R_g=25\Omega$
Rise time	t_r	-	63	-		
Turn-off delay time	$t_{d(off)}$	-	126	-		
Fall time	t_f	-	26	-		
Gate resistance	R_G	-	1.2	-	Ω	$V_{GS}=0V, V_{DS}=0V,$ $f=1MHz$

Body Diode Characteristic

Parameter	Symbol	Value			Unit	Test Condition
		min.	typ.	max.		
Body Diode Forward Voltage	V_{SD}	0.5	0.83	1	V	$V_{GS}=0V, I_{SD}=21.5A$
Body Diode Reverse Recovery Time	t_{rr}	-	416	-	ns	$I_{sd}=21.5A$ $dI/dt=100A/\mu s, V_{ds}=100V$
Body Diode Reverse Recovery Charge	Q_{rr}	-	8.26	-	uC	

Typical Performance Characteristics

Fig 1. Output Characteristics (Tj=25°C)

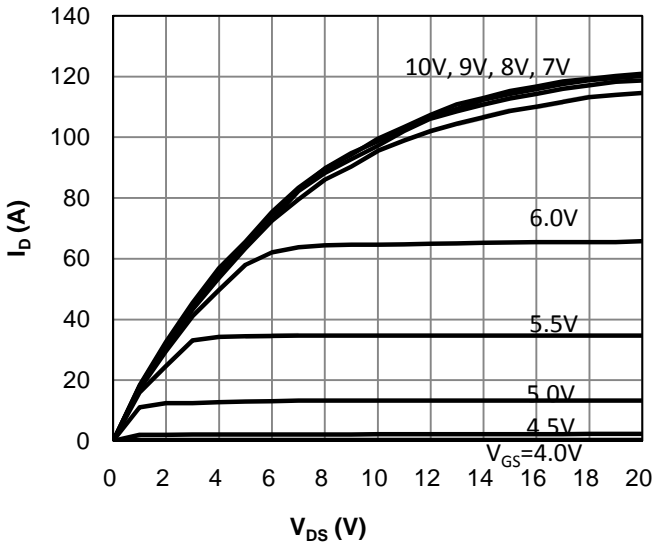


Fig 2. Output Characteristics (Tj=125°C)

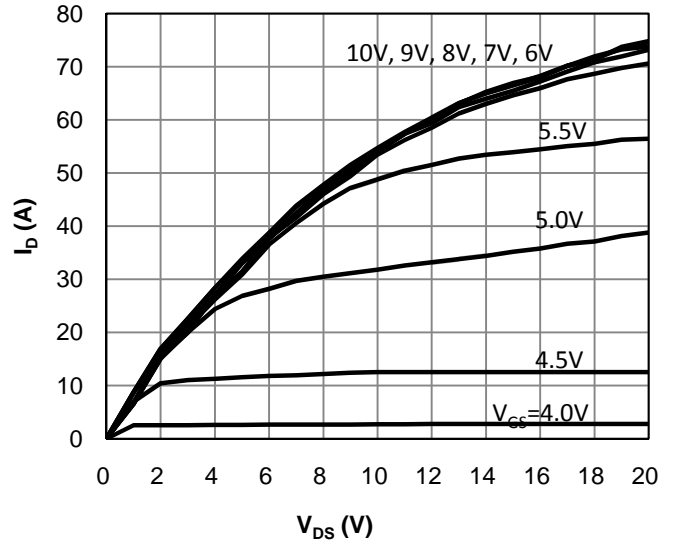


Fig 3: Transfer Characteristics

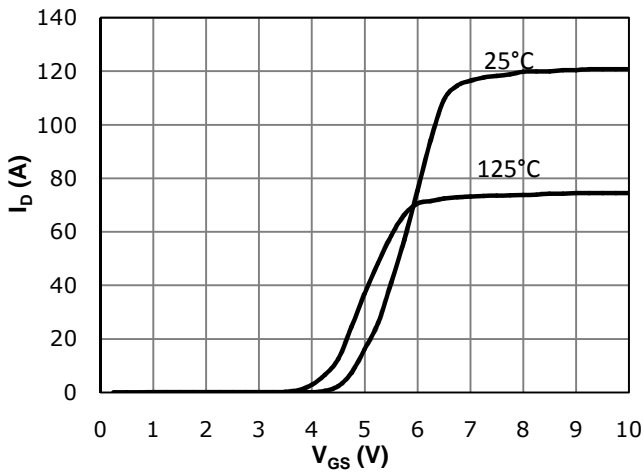


Fig 4: V_{TH} Vs Tj Temperature Characteristics

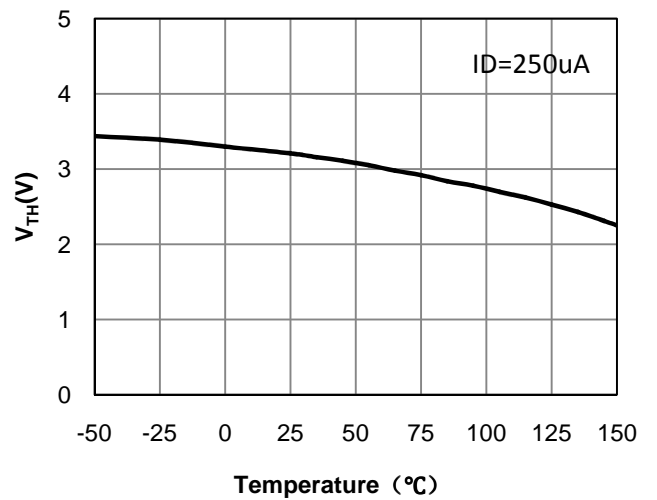


Fig 5: $R_{DS(on)}$ Vs I_{DS} Characteristics ($T_c = 25^\circ\text{C}$)

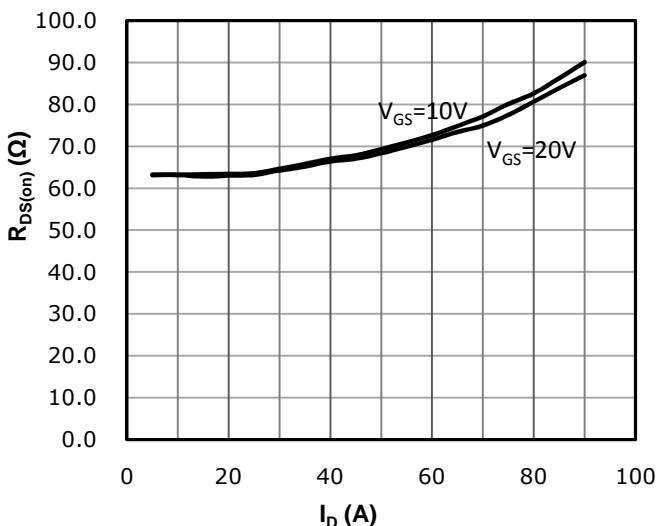


Fig 6: $R_{DS(on)}$ vs. Temperature Characteristics

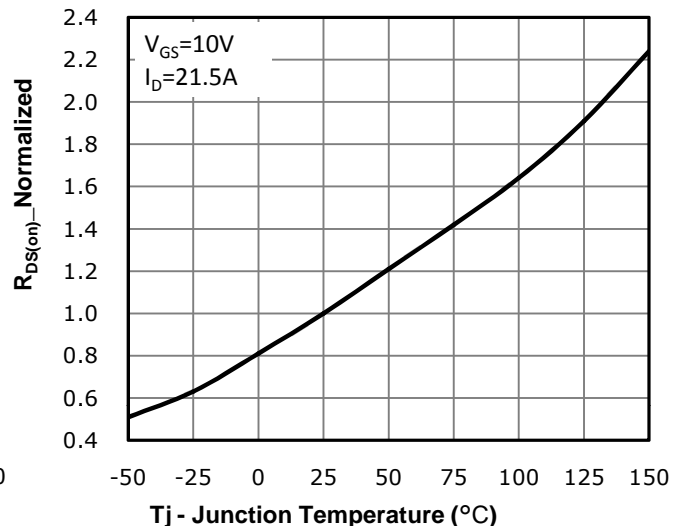


Fig 7: BVDSS vs. Temperature Characteristics

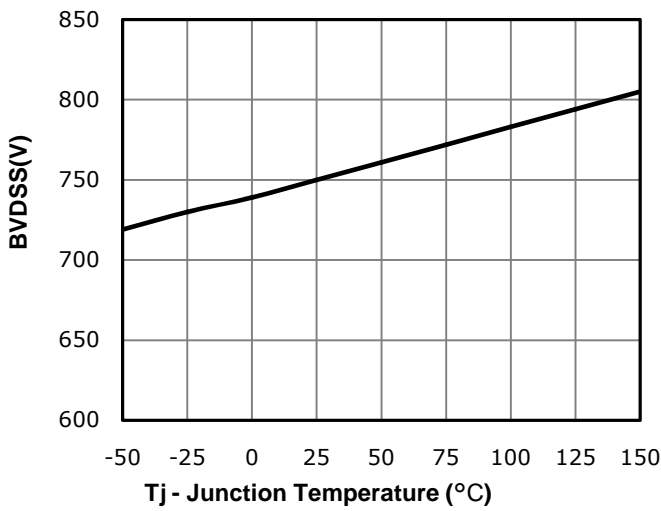


Fig 8: Rds(on) vs Gate Voltage

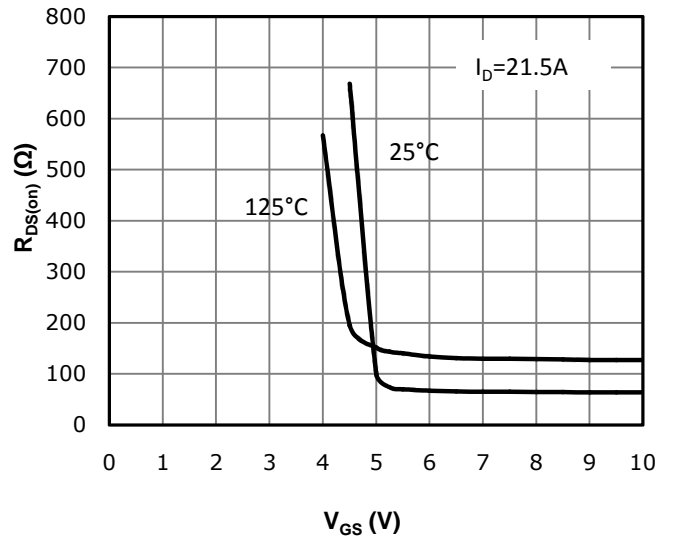


Fig 9: Body-diode Forward Characteristics

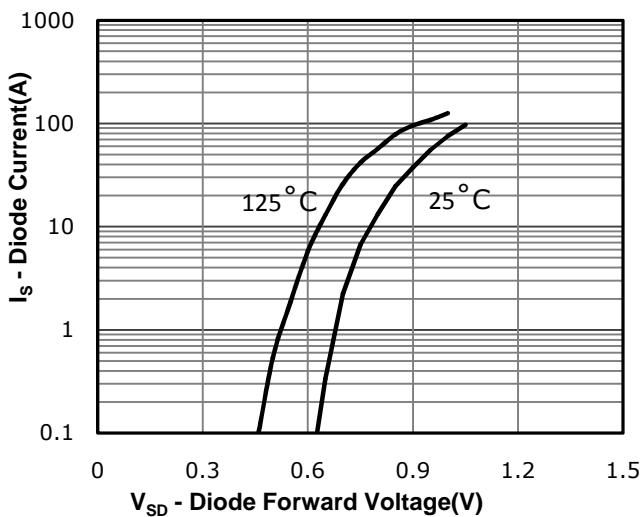


Fig 10: Gate Charge Characteristics

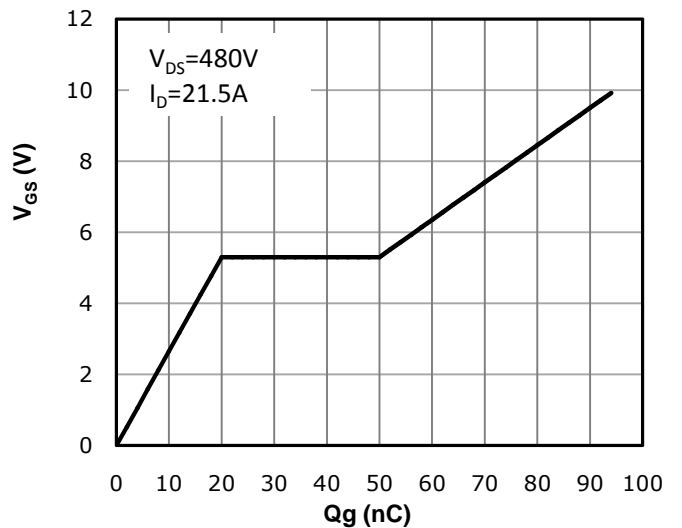


Fig 11: Capacitance Characteristics

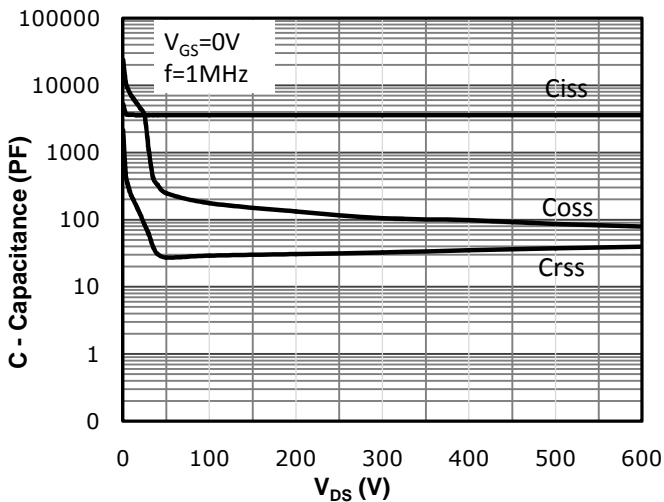


Fig 12: Safe Operating Area

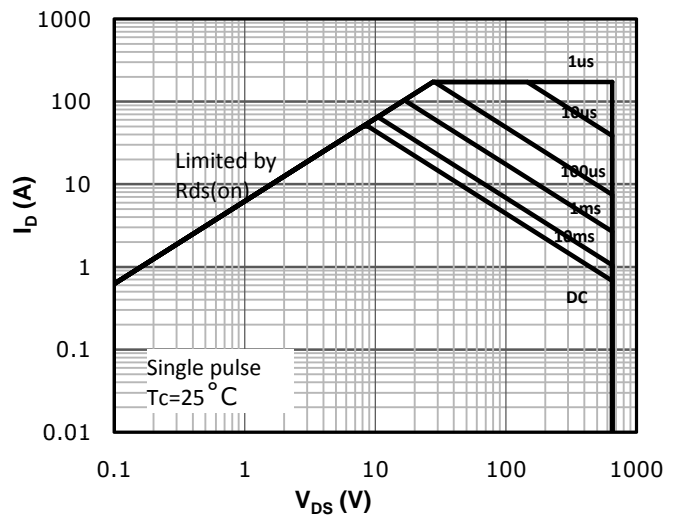
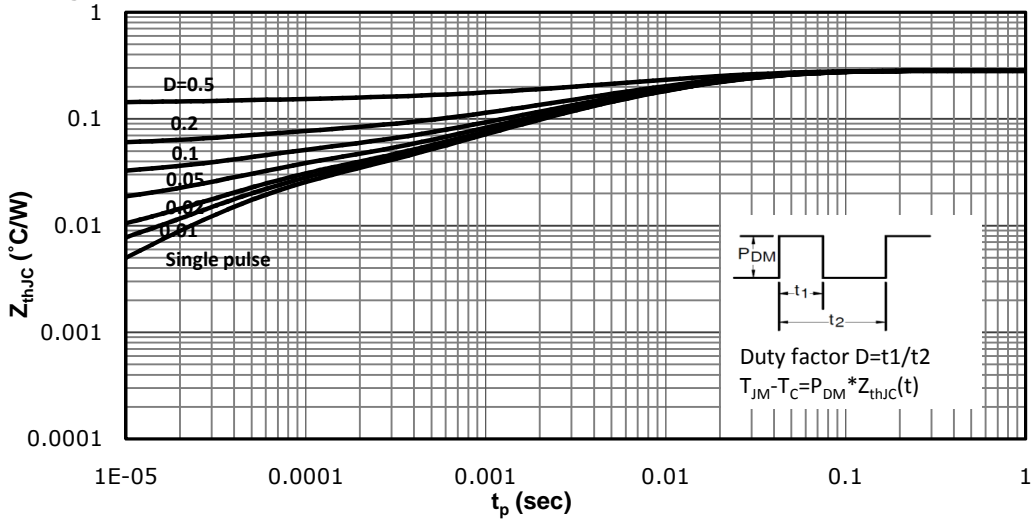
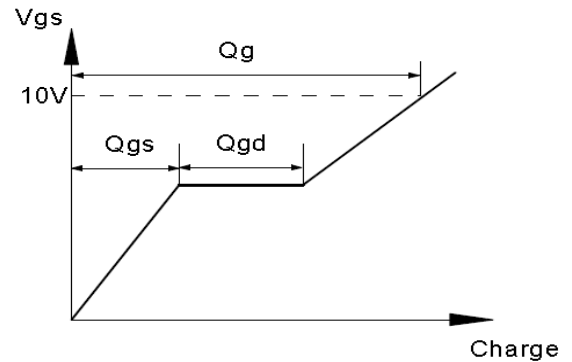
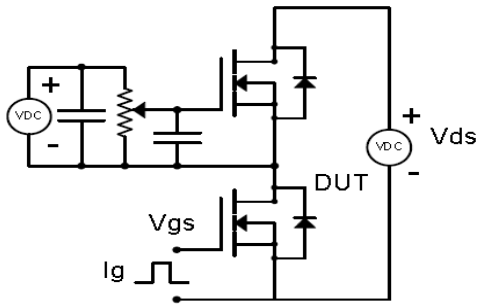


Fig 13: Max. Transient Thermal Impedance

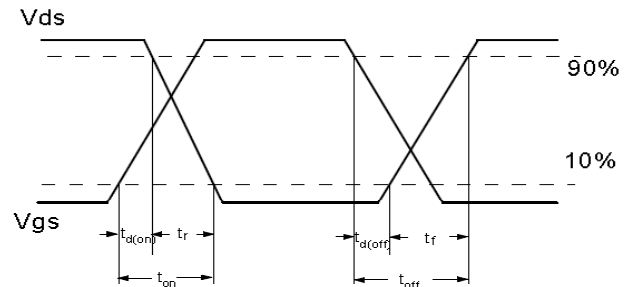
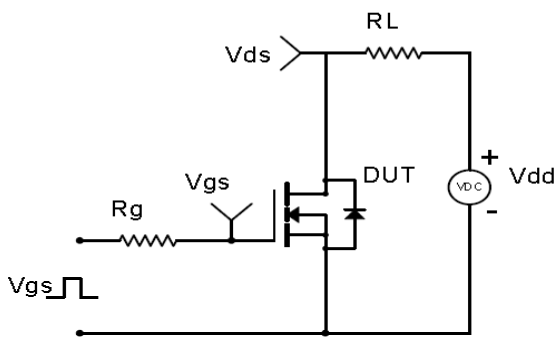


Test Circuit & Waveform

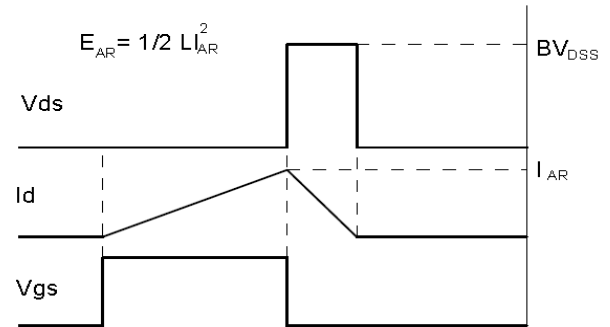
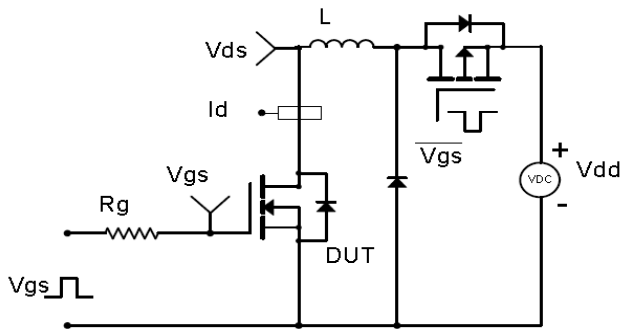
Gate Charge Test Circuit & Waveform



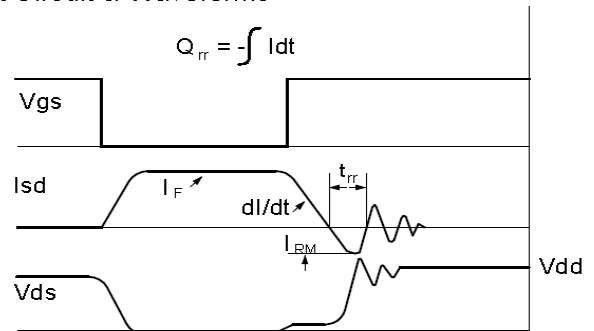
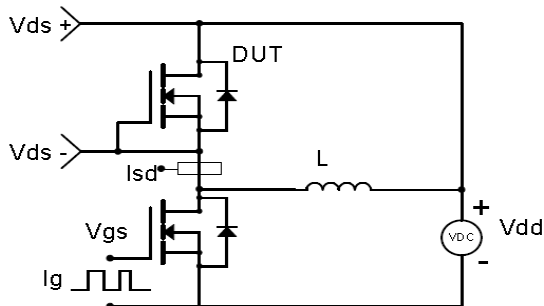
Resistive Switching Test Circuit & Waveforms



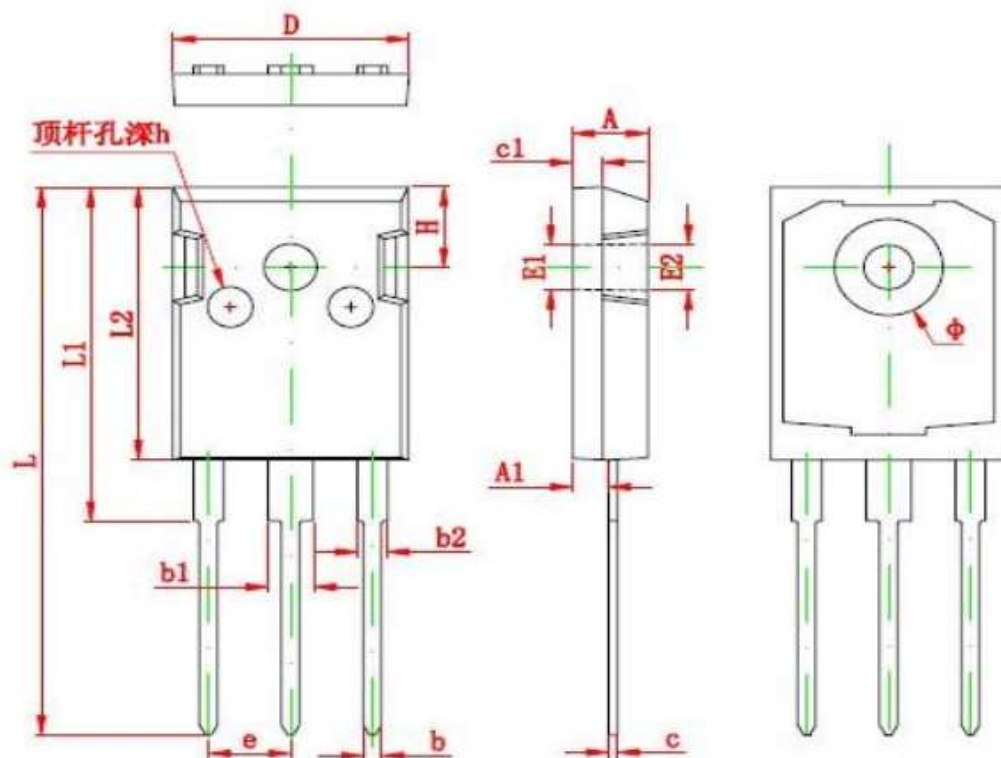
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



Package Outline: TO-247



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.850	5.150	0.191	0.200
A1	2.200	2.600	0.087	0.102
b	1.000	1.400	0.039	0.055
b1	2.800	3.200	0.110	0.126
b2	1.800	2.200	0.071	0.087
c	0.500	0.700	0.020	0.028
c1	1.900	2.100	0.075	0.083
D	15.450	15.750	0.608	0.620
E1	3.500 REF		0.138 REF	
E2	3.600 REF		0.142 REF	
L	40.900	41.300	1.610	1.626
L1	24.800	25.100	0.976	0.988
L2	20.300	20.600	0.799	0.811
Φ	7.100	7.300	0.280	0.287
e	5.450 TYP		0.215 TYP	
H	5.980 REF		0.235 REF	
h	0.000	0.300	0.000	0.012

Revision History

Revision	Date	Major changes
1.0	2016-12-13	Release of formal version

Disclaimer

Unless otherwise specified in the datasheet, the product is designed and qualified as a standard commercial product and is not intended for use in applications that require extraordinary levels of quality and reliability, such as automotive, aviation/aerospace and life-support devices or systems.

Any and all semiconductor products have certain probability to fail or malfunction, which may result in personal injury, death or property damage. Customer are solely responsible for providing adequate safe measures when design their systems.

SkySilicon reserves the right to improve product design, function and reliability without notice.