

SKL22C THRU SKL220C

2A Surface Mount Schottky Barrier Rectifiers

■ Features

- Electrostatic discharge (ESD) test under IEC6100-4-2 standard >16KV(SKL22C~SKL26C).
 standard >10KV(SKL210C~SKL220C).
- Low profile surface mounted application in order to optimize board space.
- Low power loss, high efficiency.
- High current capability, low forward voltage drop.
- Ultra high-speed switching.
- Silicon epitaxial planar chip, metal silicon junction.
- Suffix "G" indicates Halogen-free part, ex.SKL22CG.
- Lead-free parts meet environmental standards of MIL-STD-19500 /228

■ Mechanical data

• Epoxy:UL94-V0 rated flame retardant

• Case : Molded plastic, DO-214AB / SMC

• Terminals : Solder plated, solderable per

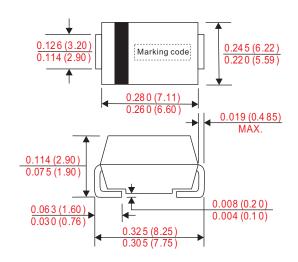
MIL-STD-750, Method 2026

• Polarity : Indicated by cathode band

• Weight: 0.007 ounce, 0.226 gram

Outline

SMC(DO-214AB)



Dimensions in inches and (millimeters)

■ Maximum ratings and electrical characteristics

Rating at 25° C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter	Conditions	Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current	See Fig.1	Io			2.0	Α
Forward surge current	8.3ms single half sine-wave superimposed on rate load (JEDEC method)	I _{FSM}			70	А
_	$V_R = V_{RRM} T_A = 25^{\circ}C$				0.5	
Reverse current	$V_R = V_{RRM} T_A = 100^{\circ}C$	I _R			20	mA
Diode junction capacitance	f=1MHz and applied 4V DC reverse voltage	C,		30		pF
Thermal resistance	Junction to ambient	R _{eJA}		50		°C/W
Storage temperature		T _{STG}	-55		+175	°C

Symbol	Marking code	Max. repetitive peak reverse voltage V _{RRM} (V)	Max. RMS voltage V _{RMS} (V)	Max. DC blocking voltage V _R (V)	Max. forward voltage $@2A, T_A = 25^{\circ}C$ $V_F(V)$	Operating temperature T _J (°C)	
SKL22C	KL22	20	14	20	0.40	-50 ~ +150	
SKL24C	KL24	40	28	40	0.45		
SKL26C	KL26	60	42	60	0.55		
SKL210C	KL210	100	70	100	0.75		
SKL215C	KL215	150	105	150	0.82	-50 ~ +175	
SKL220C	KL220	200	140	200	0.85		

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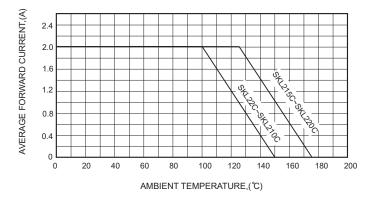


FIG.3-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

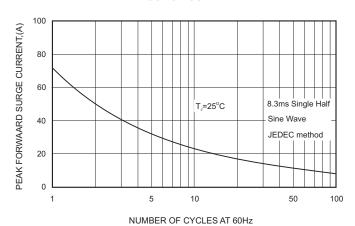


FIG.4-TYPICAL JUNCTION CAPACITANCE

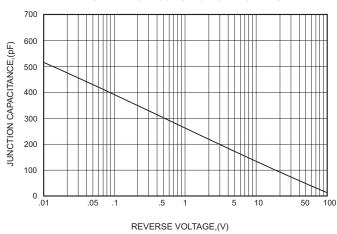


FIG.2-TYPICAL FORWARD

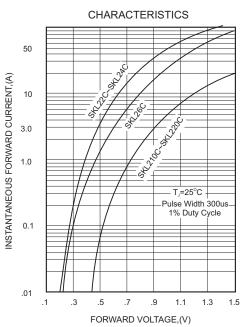
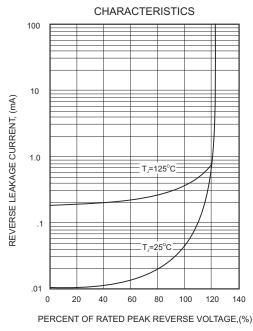


FIG.5 - TYPICAL REVERSE



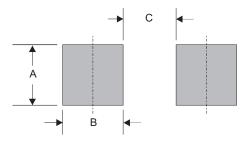
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■ SMC foot print



Α	В	С	
0.132 (3.30)	0.098 (2.50)	0.176 (4.40)	

Dimensions in inches and (millimeters)

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