

High Speed IGBT4 Modules

SKM100GB12F4

Features*

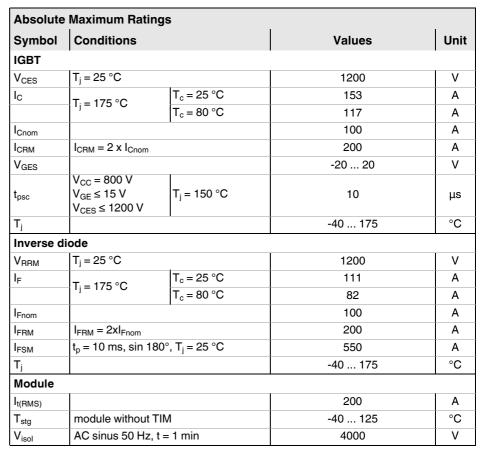
- · High speed trench and field-stop IGBT
- CAL4 ultra-fast = soft switching 4. generation CAL-diode
- Insulated copper baseplate using DBC technology (Direct Bonded Copper)
- · Increased power cycling capability
- For higher switching frequencies above 15kHz
- UL recognized, file no. E63532

Typical Applications

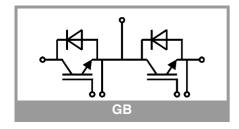
- UPS
- Electronic welders
- Inductive heating
- · Switched mode power supplies

Remarks

- Case temperature limited to T_c = 125°C max.
- Recommended T_{op} = -40 ... +150°C
- Product reliability results valid for T_i = 150°C



Characte	eristics					
Symbol	Conditions	min.	typ.	max.	Unit	
IGBT	•		•			
V _{CE(sat)}	$I_{\rm C} = 100 {\rm A}$	T _j = 25 °C		2.05	2.38	٧
	V _{GE} = 15 V chiplevel	T _j = 150 °C		2.55	2.93	V
V _{CE0}	chiplevel	T _j = 25 °C		1.10	1.28	V
		T _j = 150 °C		0.95	1.13	V
r _{CE}	V _{GE} = 15 V chiplevel	T _j = 25 °C		9.5	11	mΩ
		T _j = 150 °C		16	18	mΩ
$V_{GE(th)}$	$V_{GE}=V_{CE}$, $I_{C}=3.8$ mA		5.1	5.8	6.4	V
I _{CES}	V _{GE} = 0 V, V _{CE} = 1200 V, T _j = 25 °C				1	mA
C _{ies}	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		6.2		nF
C _{oes}		f = 1 MHz		0.41		nF
C _{res}		f = 1 MHz		0.35		nF
Q _G	V _{GE} = - 8 V+ 15 V			567		nC
R _{Gint}	T _j = 25 °C			0		Ω
t _{d(on)}	$\begin{array}{c} V_{CC} = 600 \ V \\ I_{C} = 100 \ A \\ V_{GE} = +15/-15 \ V \\ R_{G \ on} = 3.9 \ \Omega \\ R_{G \ off} = 3.9 \ \Omega \\ di/dt_{on} = 5000 \ A/\mu s \\ di/dt_{off} = 1300 \ A/\mu s \\ dv/dt = 4300 \ V/\mu s \\ L_{s} = 26 \ nH \end{array}$	T _j = 150 °C		12		ns
t _r		T _j = 150 °C		20		ns
E _{on}		T _j = 150 °C		6.6		mJ
t _{d(off)}		T _j = 150 °C		315		ns
t _f		T _j = 150 °C		65		ns
E _{off}		T _j = 150 °C		8		mJ
R _{th(j-c)}	per IGBT			0.238	K/W	
R _{th(c-s)}	per IGBT (λ _{grease} =0		0.122		K/W	





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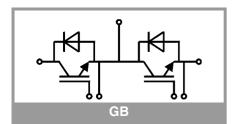
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- Recommended $T_{op} = -40 \dots +150$ °C
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Characteristics									
Symbol	Conditions		min.	typ.	max.	Unit			
Inverse d	iode								
$V_F = V_{EC}$	I _F = 100 A	T _j = 25 °C		2.55	2.93	V			
	V _{GE} = 0 V chiplevel	T _j = 150 °C		2.46	2.80	V			
V _{F0}	chiplevel	T _j = 25 °C		1.51	1.75	V			
		T _j = 150 °C		1.16	1.40	V			
r _F	chiplevel	T _j = 25 °C		10	12	mΩ			
		T _j = 150 °C		13	14	mΩ			
I _{RRM}	I _F = 100 A	T _j = 150 °C		200		Α			
Q _{rr}	di/dt _{off} = 5000 A/μs V _{GF} = -15 V	T _j = 150 °C		16.5		μC			
E _{rr}	$V_{GE} = -15 \text{ V}$ $V_{CC} = 600 \text{ V}$	T _j = 150 °C		6.3		mJ			
R _{th(j-c)}	per diode			0.483	K/W				
R _{th(c-s)}	per diode (λ _{grease} =0.81 W/(m*K))			0.134		K/W			
Module									
L _{CE}				30		nΗ			
R _{CC'+EE'}	measured per switch	T _C = 25 °C		0.65		mΩ			
		T _C = 125 °C		1.09		mΩ			
R _{th(c-s)1}	calculated without thermal coupling			0.0319		K/W			
R _{th(c-s)2}	including thermal coupling, T _s underneath module (λ _{grease} =0.81 W/(m*K))			0.050		K/W			
Ms	to heat sink M6		3		5	Nm			
M_t		to terminals M5	2.5		5	Nm			
				-		Nm			
W					160	g			



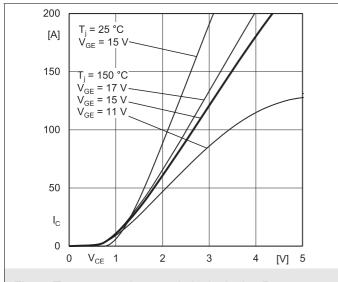


Fig. 1: Typ. output characteristic, inclusive $R_{\text{CC}'\text{+ EE'}}$

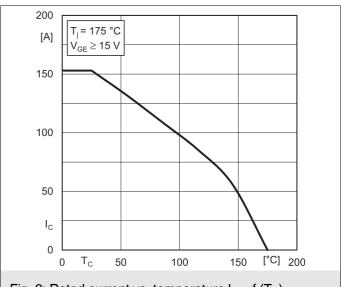


Fig. 2: Rated current vs. temperature $I_C = f(T_C)$

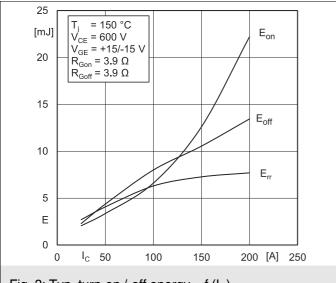


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

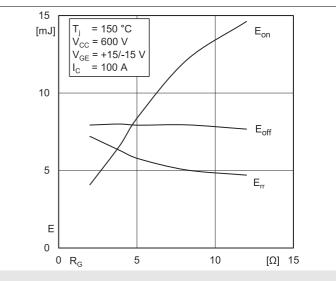


Fig. 4: Typ. turn-on /-off energy = f (R_G)

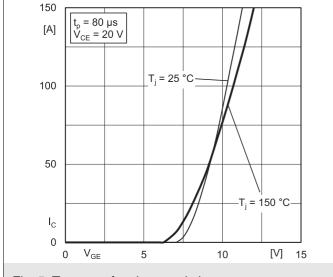


Fig. 5: Typ. transfer characteristic

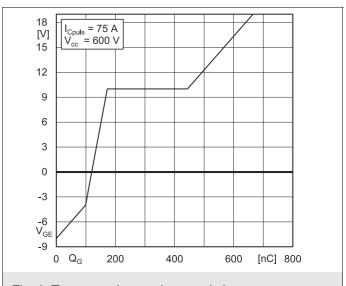
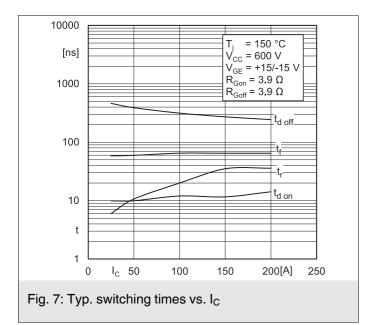
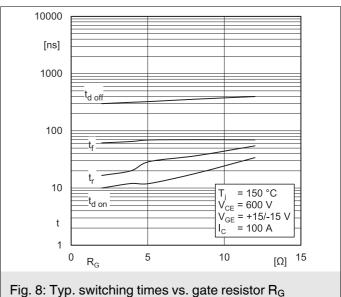
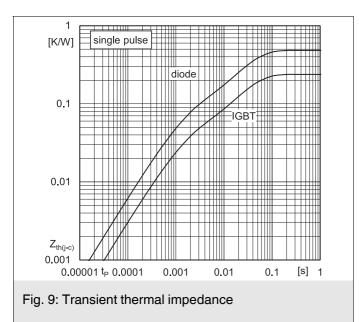
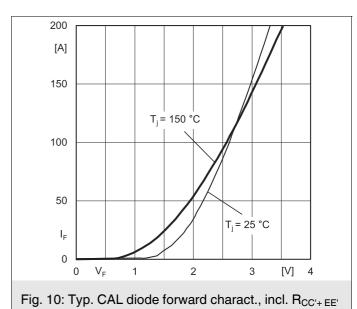


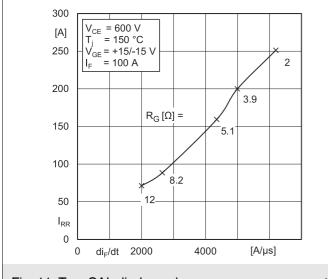
Fig. 6: Typ. gate charge characteristic











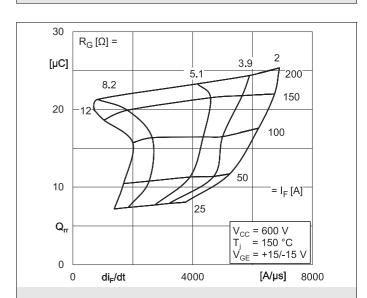
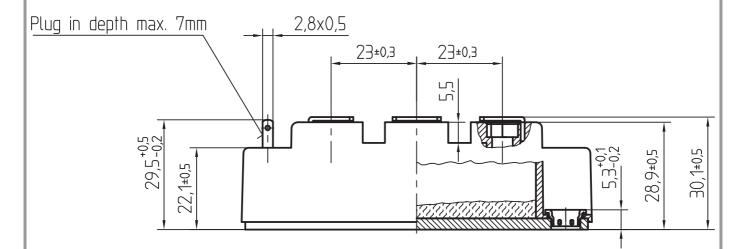
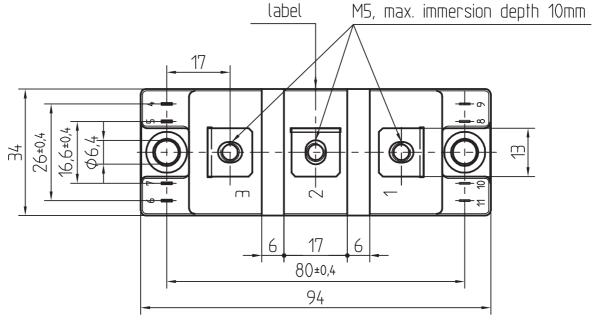


Fig. 11: Typ. CAL diode peak reverse recovery current

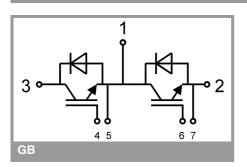






General tolerance +/- 0,5 mm

SEMITRANS 2



This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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