

SEMITRANS[®] 3

IGBT4 Modules

SKM150GB17E4GH16

Features

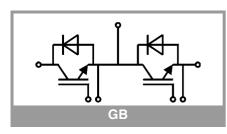
- H16: IGBT-chip with improved robustness against moisture
- IGBT4 = 4. generation medium fast trench IGBT (Infineon)
- CAL4 = Soft switching 4. Generation CAL-Diode
- Insulated copper baseplate using DBC Technology (Direct Copper Bonding)
- With integrated Gate resistor
- For switching frequencies up to 8kHzUL recognized, file no. E63532

Typical Applications*

• Medium voltage inverter market

Remarks

- Case temperature limited to T_c = 125°C max.
- Recommended $T_{op} = -40 \dots +150^{\circ}C$
- Product reliability results valid for T_j = 150°C



Absolute	Maximum Ratin	igs			
Symbol	Conditions		Values	Unit	
IGBT					
V _{CES}	T _j = 25 °C		1700	V	
lc	T _j = 175 °C	T _c = 25 °C	255	А	
		T _c = 80 °C	194	A	
I _{Cnom}			150	А	
I _{CRM}	$I_{CRM} = 3 x I_{Cnom}$		450	A	
V _{GES}			-20 20	V	
t _{psc}	$V_{CC} = 1000 V$ $V_{GE} \le 15 V$ $V_{CES} \le 1700 V$	T _j = 150 °C	10	μs	
Tj			-40 175	°C	
Inverse d	iode				
V _{RRM}	T _j = 25 °C		1700	V	
l _F	T _j = 175 °C	T _c = 25 °C	163	A	
		T _c = 80 °C	121	А	
I _{Fnom}			150	A	
I _{FRM}	I _{FRM} = 2xI _{Fnom}		300	A	
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 25 °C		918	А	
Tj			-40 175	°C	
Module	·				
I _{t(RMS)}			500	А	
T _{stg}			-40 125	°C	
V _{isol}	AC sinus 50 Hz, t = 1 min		4000	V	

Characteristics

Symbol	Conditions		min.	typ.	max.	Unit
IGBT						
V _{CE(sat)}	I _C = 150 A V _{GE} = 15 V chiplevel	T _j = 25 °C		1.96	2.27	V
		T _j = 150 °C		2.29	2.54	V
V _{CE0}	chiplevel	T _j = 25 °C		1.10	1.20	V
		T _j = 150 °C		1.00	1.10	V
r _{CE}	V _{GE} = 15 V	T _j = 25 °C		5.7	7.1	mΩ
	chiplevel	T _j = 150 °C		8.6	9.6	mΩ
V _{GE(th)}	$V_{GE}=V_{CE}$, $I_C = 5.6$ mA		5.2	5.8	6.4	V
I _{CES}	$V_{GE} = 0 V, V_{CE} = 17$	00 V, T _j = 25 °C			2.0	mA
Cies	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		11.0		nF
Coes		f = 1 MHz		0.46		nF
C _{res}		f = 1 MHz		0.36		nF
Q _G	V _{GE} = - 8 V+ 15 V			1200		nC
R _{Gint}	T _j = 25 °C			8.0		Ω
t _{d(on)}	$\begin{aligned} &H_{G \text{ on}} = 1 \Omega \\ &R_{G \text{ off}} = 1 \Omega \\ &di/dt_{on} = 4530 A/\mu s \\ &di/dt_{off} = 880 A/\mu s \end{aligned}$	T _j = 150 °C		290		ns
t _r		T _j = 150 °C		38		ns
Eon		T _j = 150 °C		69		mJ
t _{d(off)}		T _j = 150 °C		690		ns
t _f		T _j = 150 °C		155		ns
E _{off}		T _j = 150 °C		59		mJ
R _{th(j-c)}	per IGBT	1			0.161	K/W
R _{th(c-s)}	per IGBT (λ _{grease} =0.81 W/(m*K))			0.064		K/W



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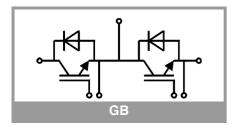
Typical Applications*

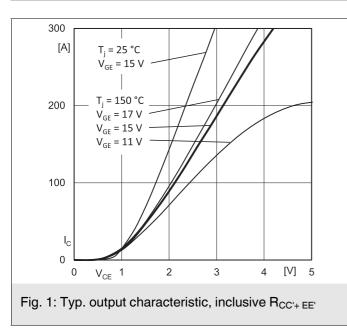
• Medium voltage inverter market

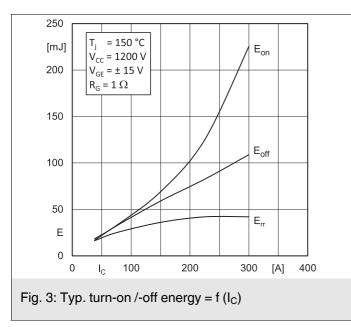
Remarks

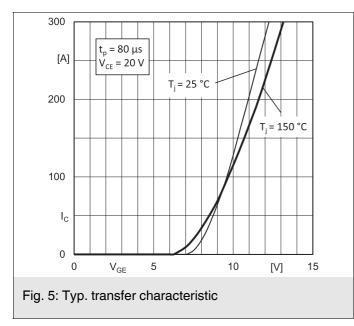
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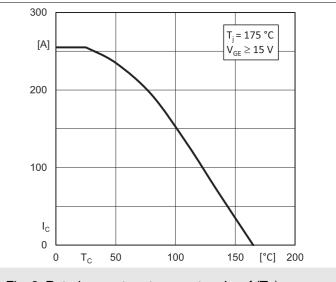
Characte	ristics					
Symbol	Conditions		min.	typ.	max.	Unit
Inverse d	iode					
$V_F = V_{EC}$	$\frac{V_{F} = V_{EC}}{V_{GE} = 0 V}$ Chiplevel	T _j = 25 °C	1	2.00	2.40	V
		T _j = 150 °C		2.14	2.56	V
V _{F0}	chiplevel	T _j = 25 °C		1.32	1.56	V
		T _j = 150 °C		1.08	1.22	V
ŕ _F	chiplevel	T _j = 25 °C		4.5	5.6	mΩ
		T _j = 150 °C		7.1	9.0	mΩ
I _{RRM}	$I_{F} = 150 \text{ A} \\ di/dt_{off} = 4100 \text{ A/}\mu\text{s} \\ V_{GE} = \pm 15 \text{ V} \\ V_{CC} = 1200 \text{ V} $	T _j = 150 °C		185		Α
Q _{rr}		T _j = 150 °C	1	49		μC
E _{rr}		T _j = 150 °C		36		mJ
R _{th(j-c)}	per diode				0.356	K/W
R _{th(c-s)}	per diode (λ_{grease} =0.81 W/(m*K))			0.072		K/W
Module						
L _{CE}			1	15		nH
R _{CC'+EE'}	measured per switch	T _C = 25 °C		0.55		mΩ
		T _C = 125 °C		0.85		mΩ
R _{th(c-s)1}	calculated without thermal coupling (λ _{grease} =0.81 W/(m*K))			0.017		K/W
R _{th(c-s)2}	including thermal coupling, Ts underneath module (\lambda_grease=0.81 W/(m*K))			0.027		K/W
Ms	to heat sink M6		3		5	Nm
Mt		to terminals M6	2.5		5	Nm
	1					Nm
w		1	1		325	g

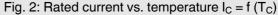


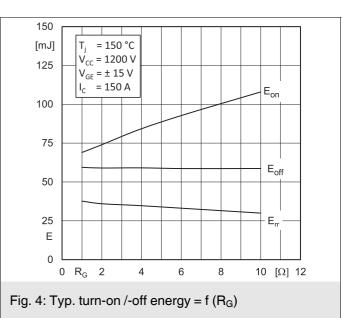


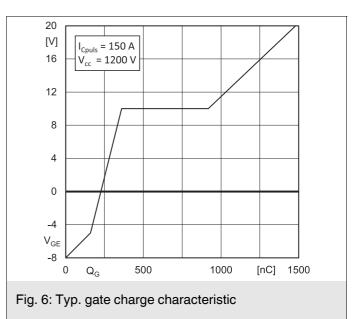




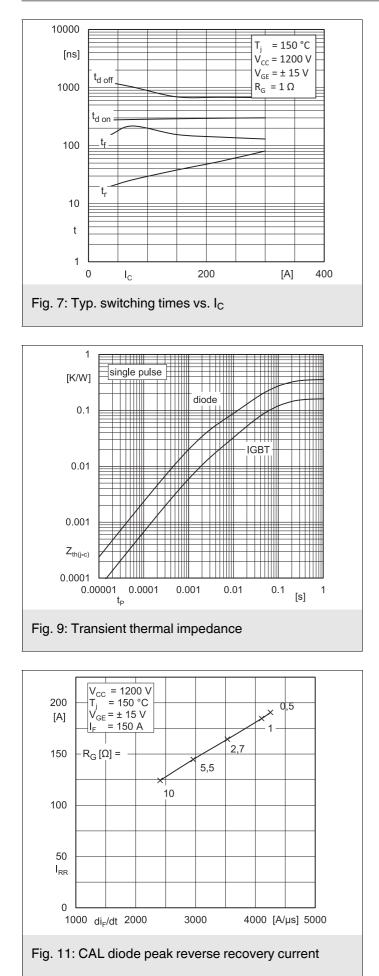


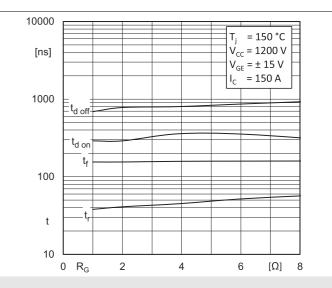


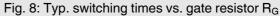


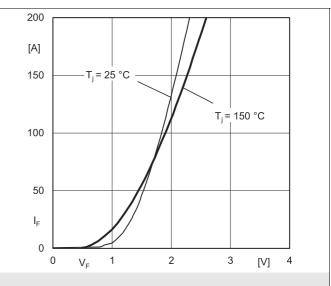


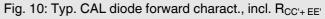
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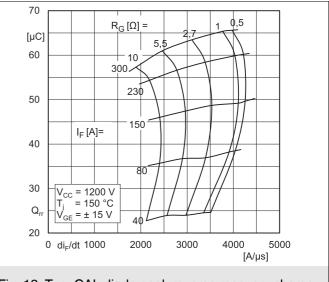
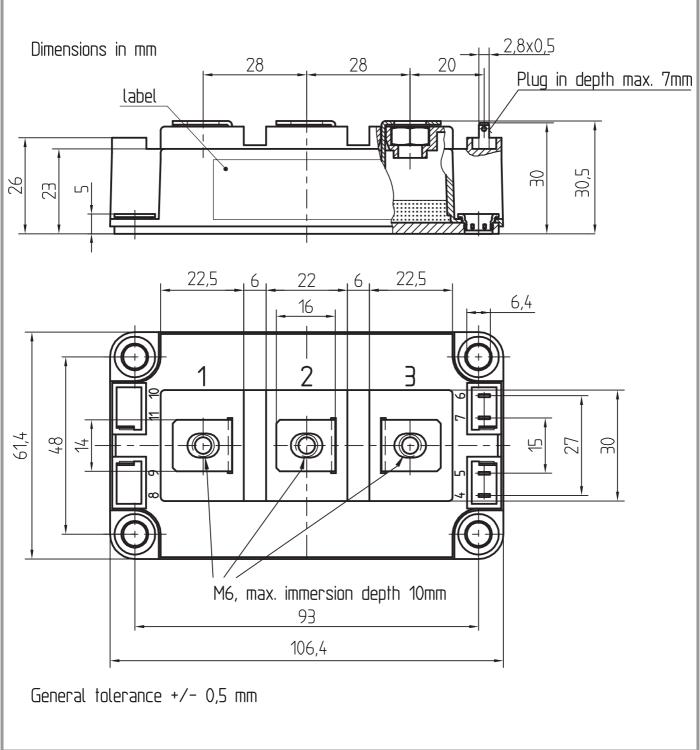
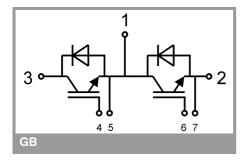


Fig. 12: Typ. CAL diode peak reverse recovery charge







This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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