

SEMITRANS® 3

IGBT4 Modules

SKM300GB17E4

Features*

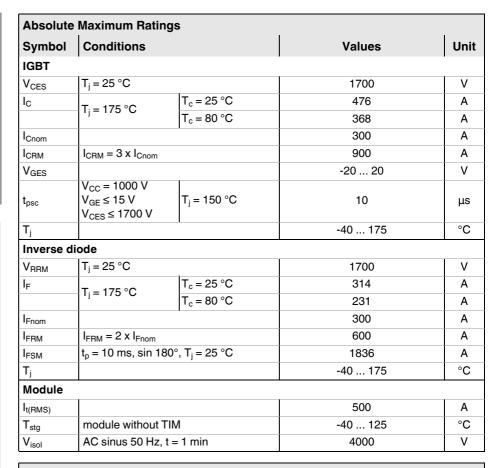
- IGBT4 = 4th generation medium fast trench IGBT (Infineon)
- CAL4 = Soft switching 4th generation **CAL-Diode**
- · Insulated copper baseplate using DBC Technology (Direct Copper Bonding)
- With integrated Gate resistor
- For switching frequencies up to 8kHz
- UL recognized, file no. E63532

Typical Applications

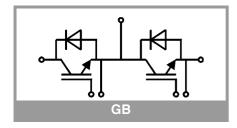
- · AC inverter drives
- UPS
- · Electronic welders
- · Wind power
- · Public transport

Remarks

- · Case temperature limited to $T_c = 125$ °C max.
- Recommended $T_{op} = -40 \dots +150$ °C
- Product reliability results valid for $T_i = 150$ °C



Characteristics									
Symbol	Conditions	min.	typ.	max.	Unit				
IGBT	•					•			
V _{CE(sat)}	$I_C = 300 \text{ A}$ $V_{GE} = 15 \text{ V}$ chiplevel	T _j = 25 °C		1.91	2.20	V			
		T _j = 150 °C		2.29	2.60	V			
V _{CE0}	chiplevel	T _j = 25 °C		0.80	0.90	V			
		T _j = 150 °C		0.70	0.80	V			
r _{CE}	V _{GE} = 15 V chiplevel	T _j = 25 °C		3.7	4.3	mΩ			
		T _j = 150 °C		5.3	6.0	mΩ			
$V_{GE(th)}$	$V_{GE}=V_{CE}$, $I_{C}=12$ mA		5.2	5.8	6.4	V			
I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 1700 \text{ V}, T_j = 25 ^{\circ}\text{C}$				4.0	mA			
C _{ies}		f = 1 MHz		27.2		nF			
Coes	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		1.06		nF			
C _{res}		f = 1 MHz		0.88		nF			
Q _G	V _{GE} = - 8 V+ 15 V			2400		nC			
R _{Gint}	T _j = 25 °C			2.1		Ω			
t _{d(on)}	a on	T _j = 150 °C		207		ns			
t _r		T _j = 150 °C		37.5		ns			
E _{on}		T _j = 150 °C		88		mJ			
t _{d(off)}		T _j = 150 °C		756		ns			
tf		T _j = 150 °C		154		ns			
E _{off}		T _j = 150 °C		121		mJ			
R _{th(j-c)}	per IGBT				0.083	K/W			
R _{th(c-s)}	per IGBT (λ _{grease} =0.81 W/(m*K))			0.038		K/W			
R _{th(c-s)}	per IGBT, pre-appli material		0.023		K/W				





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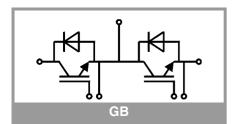
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• Case temperature limited to $T_c = 125$ °C max.

• Recommended T_{op} = -40 ... +150°C

· Product reliability results valid for $T_i = 150$ °C

Characteristics										
Symbol	Conditions	min.	typ.	max.	Unit					
Inverse diode										
$V_F = V_{EC}$	I _F = 300 A V _{GE} = 0 V chiplevel	T _j = 25 °C		2.00	2.40	V				
		T _j = 150 °C		2.13	2.56	V				
V _{F0} chi	chiplevel	T _j = 25 °C		1.32	1.56	V				
	Chipievei	T _j = 150 °C		1.08	1.22	V				
r _F	chiplevel	T _j = 25 °C		2.3	2.8	$m\Omega$				
	Chipievei	T _j = 150 °C		3.5	4.5	mΩ				
I _{RRM}	$I_F = 300 \text{ A}$ di/dt _{off} = 8600 A/µs $V_{GE} = -15 \text{ V}$ $V_{CC} = 1200 \text{ V}$	T _j = 150 °C		489		Α				
Q_{rr}		T _j = 150 °C		102		μC				
E _{rr}		T _j = 150 °C		77		mJ				
R _{th(j-c)}	per diode			0.19	K/W					
$R_{\text{th(c-s)}}$	per diode (λ_{grease} =0		0.045		K/W					
R _{th(c-s)}	per diode, pre-applied phase change material			0.040		K/W				
Module										
L _{CE}				15		nΗ				
R _{CC'+EE'}	measured per	T _C = 25 °C		0.55		mΩ				
switch	switch	T _C = 125 °C		0.85		$m\Omega$				
$R_{\text{th(c-s)1}}$	calculated without thermal coupling			0.0103		K/W				
R _{th(c-s)2}	including thermal coupling, T_s underneath module $(\lambda_{grease} = 0.81 \text{ W/(m*K)})$			0.017		K/W				
$R_{\text{th(c-s)2}}$	including thermal coupling, T _s underneath module, pre-applied phase change material			0.012		K/W				
M_s	to heat sink M6		3		5	Nm				
M_{t}		to terminals M6	2.5		5	Nm				
						Nm				
W					325	g				



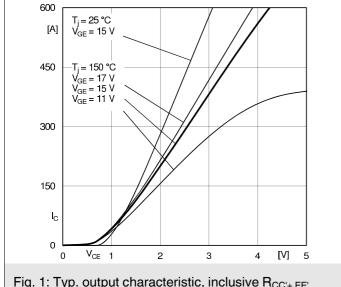


Fig. 1: Typ. output characteristic, inclusive R_{CC'+ EE'}

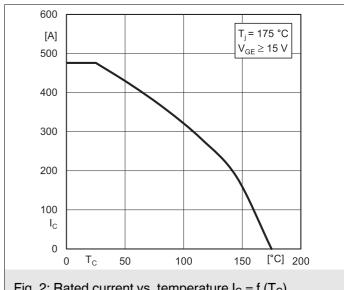
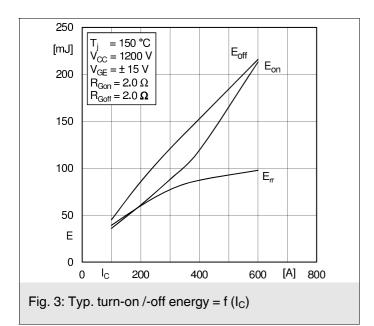


Fig. 2: Rated current vs. temperature $I_C = f(T_C)$



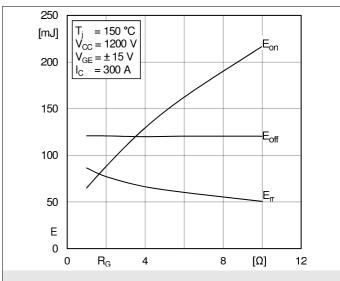


Fig. 4: Typ. turn-on /-off energy = $f(R_G)$

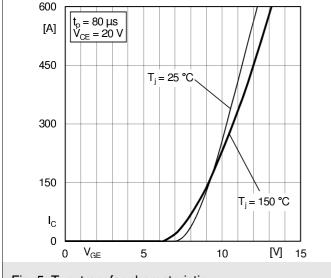


Fig. 5: Typ. transfer characteristic

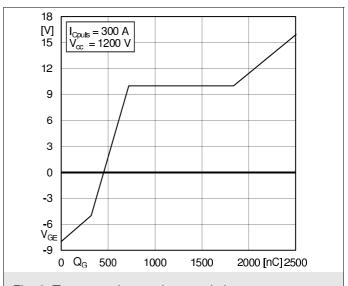
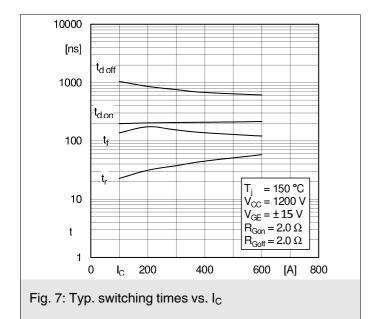
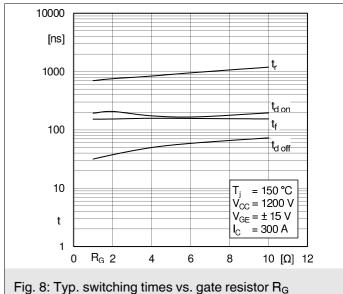
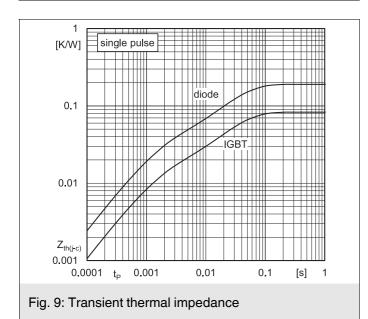
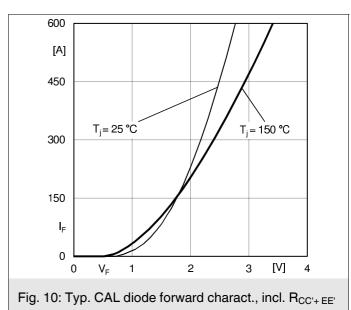


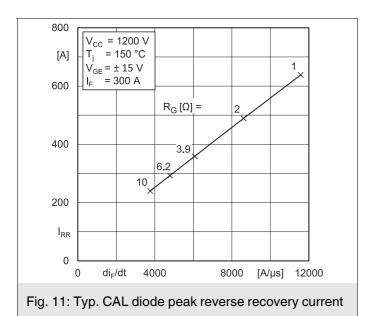
Fig. 6: Typ. gate charge characteristic











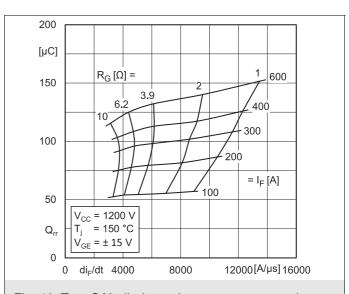
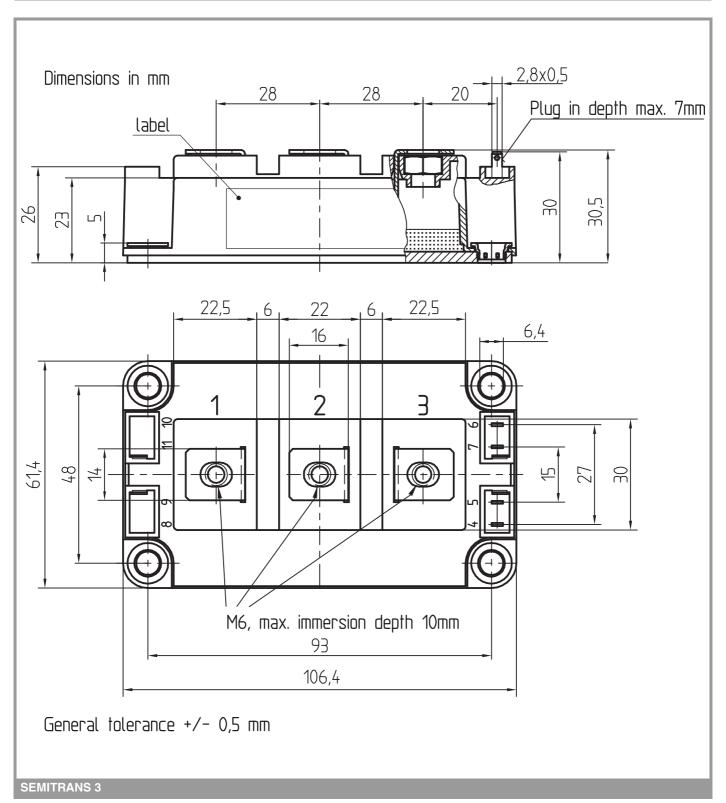
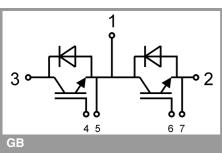


Fig. 12: Typ. CAL diode peak reverse recovery charge





This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

*IMPORTANT INFORMATION AND WARNINGS

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