

# SEMITRANS® 3

#### **IGBT4** Modules

#### SKM600GB12E4

#### Features\*

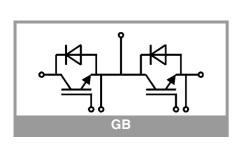
- IGBT4 = 4th generation medium fast trench IGBT (Infineon)
- CAL4 = Soft switching 4th generation CAL-diode
- Insulated copper baseplate using DBC technology (Direct Bonded Copper)
- Increased power cycling capability
- With integrated gate resistor
- For higher switching frequencies up to 12kHz
- UL recognized, file no. E63532

### **Typical Applications**

- · AC inverter drives
- UPS
- · Electronic welders

#### **Remarks**

• Case temperature limited to  $T_c = 125^{\circ}C$  max, recomm.  $T_{op} = -40 \dots +150$ °C, product rel. results valid for  $T_i = 150$ °C



Absolute Maximum Ratings							
Symbol	Conditions		Values	Unit			
IGBT							
V <sub>CES</sub>	T <sub>j</sub> = 25 °C		1200	V			
$T_j = 1$	T <sub>i</sub> = 175 °C	T <sub>c</sub> = 25 °C	860	Α			
	11 - 173 0	T <sub>c</sub> = 80 °C	702	Α			
I <sub>Cnom</sub>		•	600	Α			
I <sub>CRM</sub>			1800	Α			
$V_{GES}$			-20 20	V			
t <sub>psc</sub>	$V_{CC} = 800 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T <sub>j</sub> = 150 °C	10	μs			
T <sub>j</sub>		•	-40 175	°C			
Inverse di	ode						
$V_{RRM}$	T <sub>j</sub> = 25 °C		1200	V			
I <sub>F</sub>	T <sub>j</sub> = 175 °C	T <sub>c</sub> = 25 °C	623	Α			
		T <sub>c</sub> = 80 °C	466	Α			
I <sub>Fnom</sub>			500	Α			
I <sub>FRM</sub>			1200	Α			
I <sub>FSM</sub>	$t_p = 10 \text{ ms, sin } 180^{\circ}, T_j = 25 ^{\circ}\text{C}$		2736	Α			
Tj			-40 175	°C			
Module							
I <sub>t(RMS)</sub>			500	Α			
T <sub>stg</sub>	module without TIM		-40 125	°C			
V <sub>isol</sub>	AC sinus 50 Hz, t = 1 min		4000	V			

Characteristics							
Symbol	Conditions	min.	typ.	max.	Unit		
IGBT						•	
V <sub>CE(sat)</sub>	I <sub>C</sub> = 600 A	T <sub>j</sub> = 25 °C		1.80	2.05	V	
	V <sub>GE</sub> = 15 V chiplevel	T <sub>j</sub> = 150 °C		2.20	2.42	V	
V <sub>CE0</sub>	chiplevel	T <sub>j</sub> = 25 °C		0.80	0.90	V	
		T <sub>j</sub> = 150 °C		0.70	0.80	V	
	V <sub>GE</sub> = 15 V	T <sub>j</sub> = 25 °C		1.67	1.92	mΩ	
	chiplevel	T <sub>j</sub> = 150 °C		2.5	2.7	mΩ	
$V_{GE(th)}$	$V_{GE}=V_{CE}$ , $I_C=24$ m	A	5	5.8	6.5	V	
I <sub>CES</sub>	$V_{GE} = 0 \text{ V}, V_{CE} = 12$			5	mA		
C <sub>ies</sub>	V <sub>CE</sub> = 25 V V <sub>GE</sub> = 0 V	f = 1 MHz		37.2		nF	
Coes		f = 1 MHz		2.32		nF	
C <sub>res</sub>		f = 1 MHz		2.04		nF	
$Q_{G}$	V <sub>GE</sub> = - 8 V+ 15 V			3400		nC	
R <sub>Gint</sub>	T <sub>j</sub> = 25 °C			1.3		Ω	
t <sub>d(on)</sub>	$V_{CC} = 600 \text{ V}$ $I_{C} = 600 \text{ A}$ $V_{GE} = +15/-15 \text{ V}$ $R_{G \text{ on}} = 1.8 \Omega$ $R_{G \text{ off}} = 1 \Omega$	T <sub>j</sub> = 150 °C		156		ns	
t <sub>r</sub>		T <sub>j</sub> = 150 °C		68		ns	
E <sub>on</sub>		T <sub>j</sub> = 150 °C		30		mJ	
t <sub>d(off)</sub>		T <sub>j</sub> = 150 °C		522		ns	
t <sub>f</sub>	$di/dt_{on} = 9100 A/\mu s$	T <sub>j</sub> = 150 °C		138		ns	
E <sub>off</sub>	$\begin{array}{l} \text{di/dt}_{\text{off}} = 4000 \text{ A/µs} \\ \text{dv/dt} = 3500 \text{ V/µs} \\ \text{L}_{\text{s}} = 25 \text{ nH} \end{array}$	T <sub>j</sub> = 150 °C		77		mJ	
R <sub>th(j-c)</sub>	per IGBT				0.049	K/W	
R <sub>th(c-s)</sub>	per IGBT (λ <sub>grease</sub> =0.81 W/(m*K))			0.032		K/W	



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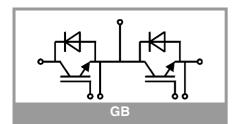
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· Electronic welders

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Characte	eristics					
Symbol	Conditions		min.	typ.	max.	Unit
Inverse d	liode					
$V_F = V_{EC} \qquad \begin{aligned} I_F &= 600 \text{ A} \\ V_{GE} &= 0 \text{ V} \\ \text{chiplevel} \end{aligned}$	•	T <sub>j</sub> = 25 °C		2.28	2.63	V
		T <sub>j</sub> = 150 °C		2.28	2.61	V
V <sub>F0</sub>	chiplevel	T <sub>j</sub> = 25 °C		1.30	1.50	V
		T <sub>j</sub> = 150 °C		0.90	1.10	V
r <sub>F</sub>	chiplevel	T <sub>j</sub> = 25 °C		1.64	1.88	mΩ
		T <sub>j</sub> = 150 °C		2.3	2.5	mΩ
I <sub>RRM</sub>	I <sub>F</sub> = 600 A	T <sub>j</sub> = 150 °C		559		Α
Q <sub>rr</sub>	di/dt <sub>off</sub> = 8500 A/μs V <sub>GE</sub> = -15 V	T <sub>j</sub> = 150 °C		98		μC
E <sub>rr</sub>	$V_{GE} = -15 \text{ V}$ $V_{CC} = 600 \text{ V}$	T <sub>j</sub> = 150 °C		39		mJ
R <sub>th(j-c)</sub>	per diode				0.095	K/W
R <sub>th(c-s)</sub>	per diode (λ <sub>grease</sub> =0.81 W/(m*K))			0.039		K/W
Module						
L <sub>CE</sub>				15		nΗ
R <sub>CC'+EE'</sub>	measured per switch	T <sub>C</sub> = 25 °C		0.55		mΩ
		T <sub>C</sub> = 125 °C		0.85		mΩ
R <sub>th(c-s)1</sub>	calculated without t (λ <sub>grease</sub> =0.81 W/(m*		0.00879		K/W	
R <sub>th(c-s)2</sub>	including thermal coupling, Ts underneath module (\(\lambda_{\text{grease}} = 0.81 \) W/(m*K))			0.014		K/W
Ms	to heat sink M6		3		5	Nm
$M_t$		to terminals M6	2.5		5	Nm
				-		Nm
W					325	g



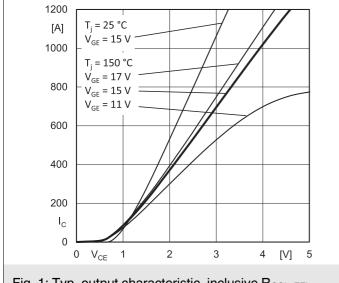


Fig. 1: Typ. output characteristic, inclusive R<sub>CC'+ EE'</sub> Fig. 2: Rated current vs. tempe

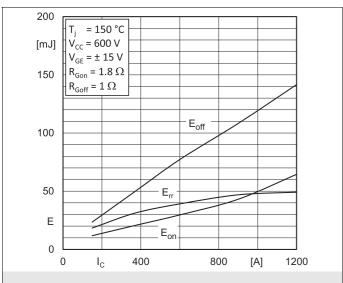
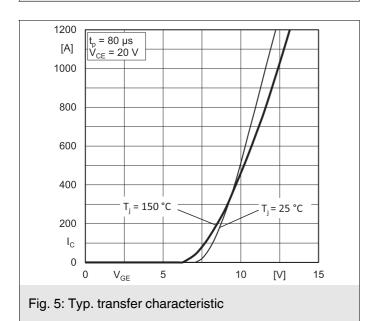
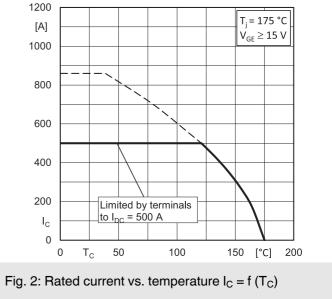
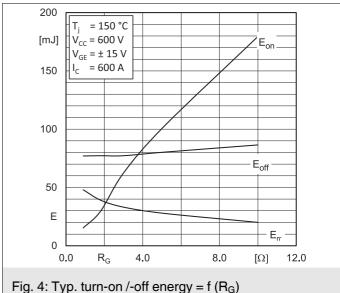
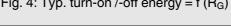


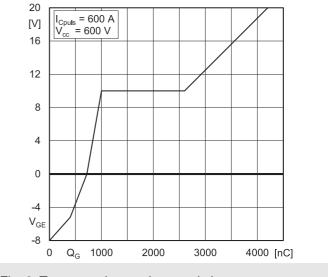
Fig. 3: Typ. turn-on /-off energy =  $f(I_C)$ 

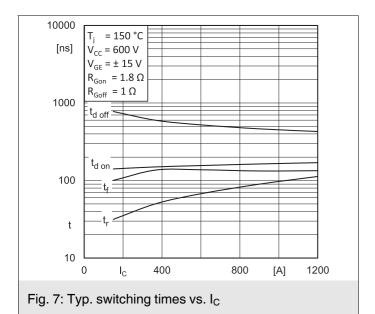


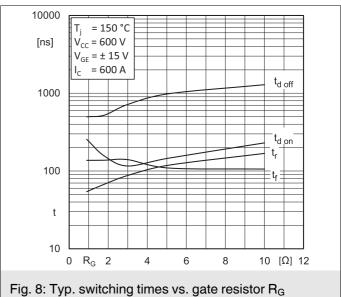


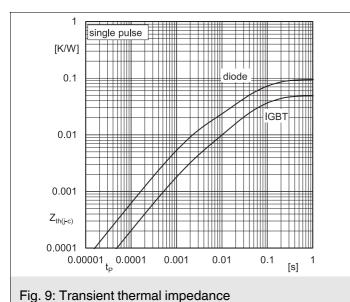


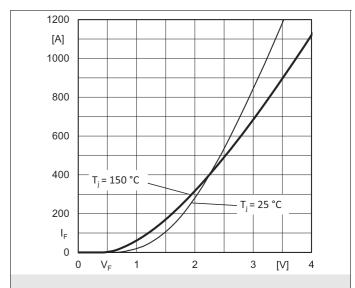




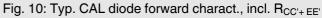


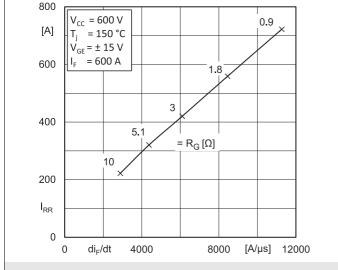












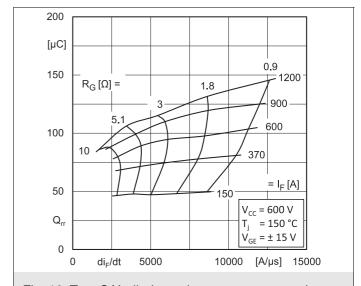
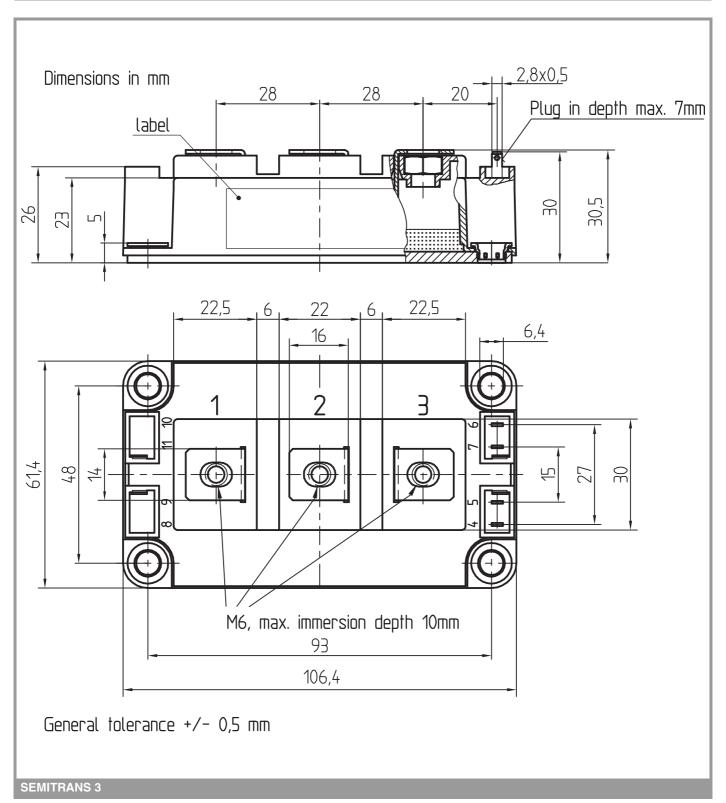
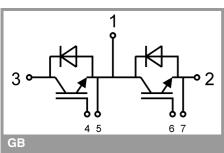


Fig. 11: Typ. CAL diode peak reverse recovery current

Fig. 12: Typ. CAL diode peak reverse recovery charge





This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

#### \*IMPORTANT INFORMATION AND WARNINGS

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