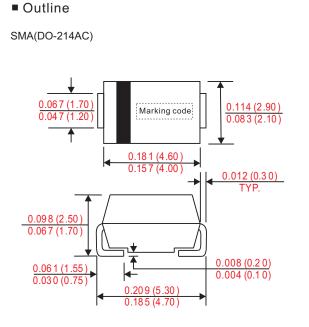
SL12A THRU SL14A

1A Surface Mount Schottky Barrier Rectifiers

Features

- Low profile surface mounted application in order to optimize board space.
- Low power loss, high efficiency.
- High current capability, low forward voltage drop.
- Ultra high-speed switching.
- Silicon epitaxial planar chip, metal silicon junction.
- Suffix "G" indicates Halogen-free part, ex.SL12AG.
- Lead-free parts meet environmental standards of MIL-STD-19500 /228



Mechanical data

- Epoxy:UL94-V0 rated flame retardant
- Case : Molded plastic, DO-214AC / SMA
- Terminals : Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity : Indicated by cathode band
- Weight : 0.002 ounce, 0.055 gram

Dimensions in inches and (millimeters)

Maximum ratings and electrical characteristics

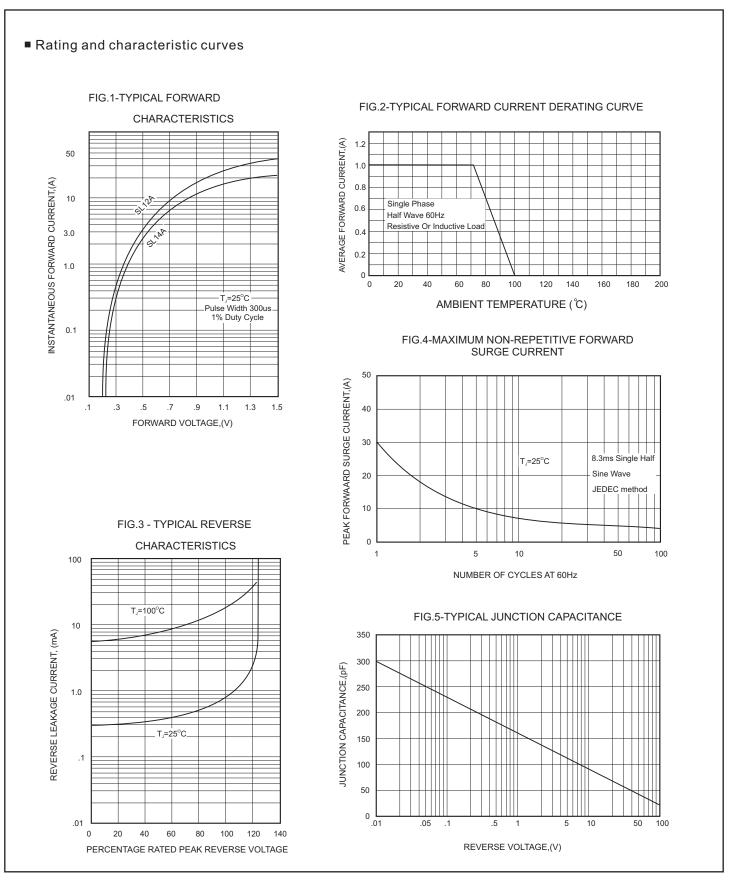
Rating at 25°C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Parameter			Conditions			Symbol	MIN.	TYP.	MAX.	UNIT
Forward rectified current						I _o			1.0	А
Forward surge current			8.3ms single half sine-wave superimposed on rate load (JEDEC method)			I _{fsm}			30	А
Reverse current			$V_{R} = V_{RRM} T_{A} = 25^{\circ}C$ $V_{R} = V_{RRM} T_{A} = 100^{\circ}C$			- I _R			1.0 20	mA
Diode junction capacitance			f=1MHz and applied 4V DC reverse voltage			C		130		pF
Thermal resistance			Junction to ambient			R _{eja}		80		°C/W
Storage temperature						T _{stg}	-55		+150	°C
	Marking code	Max. repetitive peak		Max. DC blocking voltage		forward 2 forward 2		Oper	ating tem	perature
Symbol		rovoroo voltoo		V _R (V)	$V_{F}(V)$		T, (°C)			
Symbol		reverse voltage V _{RRM} (V)	V _{RMS} (V)	V _R (V)		V _F (V)			,	
Symbol SL12A	SL12		V _{RMS} (V)	20		V _F (V)			-50 ~ +1	



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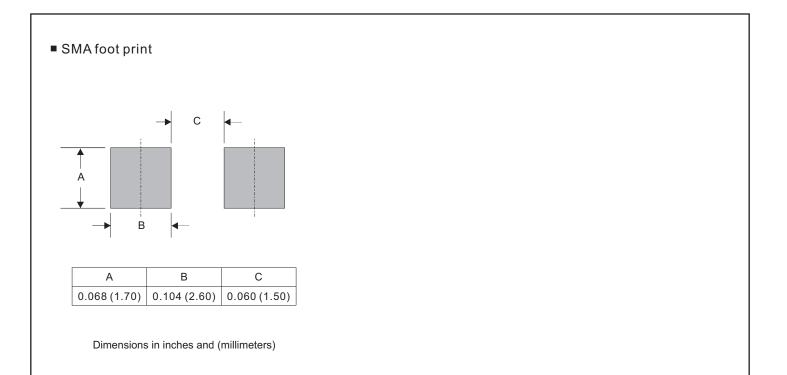
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