



# SLB120N08G3/SLP120N08G3 85V N - Channel MOSFET

#### **General Description**

This Power MOSFET is produced using Msemitek's advanced Shielding Gate MOSFET technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for low voltage applications such as DC/DC converters and high efficiency switching for power management in portable and battery operated products.

#### **Features**

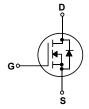
- N-Channel:85V 120A

 $R_{DS(on)Typ}$ = 5.0m $\Omega$ @ $V_{GS}$  = 10 V

- Very Low On-resistance R<sub>DS(ON)</sub>
- Low Crss
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability







# **Absolute Maximum Ratings**

T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	SLB120N08G3/SLP120N08G3	Units
$V_{DSS}$	Drain-Source Voltage	85	V
I <sub>D</sub>	Drain Current - Continuous (T <sub>C</sub> = 25°C)	120	Α
	- Continuous (T <sub>C</sub> = 100°C)	100	Α
I <sub>DM</sub>	Drain Current - Pulsed (Note 1)	480	Α
$V_{GSS}$	Gate-Source Voltage	±20	V
E <sub>AS</sub>	Single Pulsed Avalanche Energy (Note 2)	625	mJ
P <sub>D</sub>	Power Dissipation (T <sub>C</sub> = 25°C)	105	W
R <sub>eJC</sub>	Thermal Resistance, Junction to Case	0.7	%C/W
R <sub>θJA</sub>	Thermal Resistance, Junction to ambient	60	%C/W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Temperature Range	-55 to +150	သူ
TL	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	သ

<sup>\*</sup> Drain current limited by maximum junction temperature.

### **Package Marking**

Part Number	Top Marking	Package	Packing Method	MOQ	QTY
SLB120N08G3	SLB120N08G3	TO-263	Tape	800	4000
SLP120N08G3	SLP120N08G3	TO-220C	Tube	1000	5000

#### **Electrical Characteristics**

T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Тур	Max	Unit
Off Ch	aracteristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \text{ uA}$	85			V
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =85 V, V <sub>GS</sub> = 0 V			1	uA
		V <sub>DS</sub> = 85V, T <sub>C</sub> = 125°C		5	-	uA
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 20V, V <sub>DS</sub> = 0 V			100	nΑ
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -20 V, V <sub>DS</sub> = 0 V			-100	nΑ
On Ch	aracteristics					
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 uA	2.0	-	4.0	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 30A		5.0	6.2	m
gfs	Transconductance	VDS =5V,I D =30A	-	80	-	S
R <sub>G</sub>	Gate Resistance	V GS =0V,V DS =0V F=1MHz	-	1.5	-	Ω
Dynam	nic Characteristics					
Ciss	Input Capacitance			4030	-	рF
Coss	Output Capacitance	$V_{DS} = 40 \text{ V}, V_{GS} = 0 \text{ V},$ f = 1.0  MHz		545	-	pF
Crss	Reverse Transfer Capacitance	1 - 1.0 MHZ		35	-	pF
Switch	ning Characteristics		•			
t <sub>d(on)</sub>	Turn-On Delay Time			20		ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS}$ = 10 V, $V_{DS}$ =40 V, $R_L$ = 3 $\Omega$ (Note 3)		38		ns
$t_{\text{d(off)}}$	Turn-Off Delay Time			45		ns
t <sub>f</sub>	Turn-Off Fall Time			20		ns
$Q_g$	Total Gate Charge	$V_{DS} = 40 \text{ V}, I_{D} = 25\text{A},$		65		nC
$Q_{gs}$	Gate-Source Charge	V <sub>GS</sub> = 10V (Note 3)		25		n(
$Q_{qd}$	Gate-Drain Charge			14		n(

#### Notes:

 $I_{SM}$ 

 $V_{SD}$ 

 $T_{rr}$ 

 $Q_{rr}$ 

1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

Maximum Continuous Drain-Source Diode Forward Current

Drain to Source Diode Forward Voltage,  $V_{GS} = 0V$ ,  $I_{SD} = 30A$ ,  $T_{J} = 25$ °C

Maximum Pulsed Drain-Source Diode Forward Current

Reverse recovery time, I F = 20A DI F /dt = 500A/µs

Reverse recovery charge,I F =20A DI F /dt=500A/µs

- 2. EAS condition: T J =25°C, V DD =15V, V G =10V, R G =25 $\Omega$ , L=0.5mH
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%

120

480

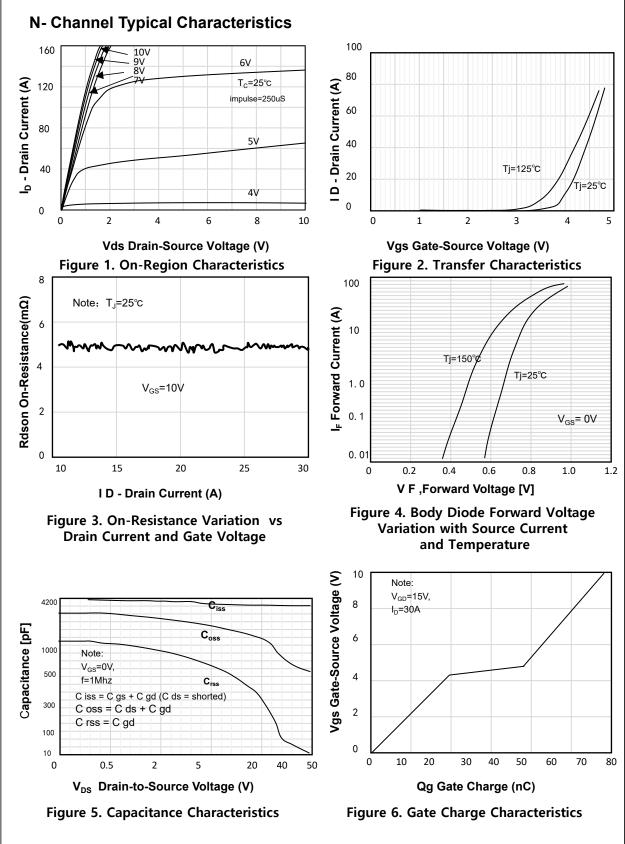
60

340

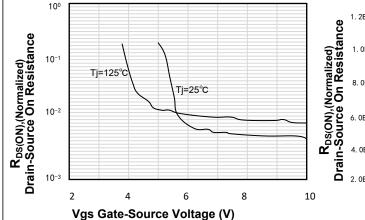
A V

ns

nC



### N- Channel Typical Characteristics (Continued)



1. 2E-02

Orango Onesistano

8. 0E-03

4. 0E-03

2. 0E-03

25 50 75 100 125 150 175

Figure 7. Breakdown Voltage Variation vs Temperature

T J , Junction Temperature [°C] Figure 8. On-Resistance Variation vs Temperature

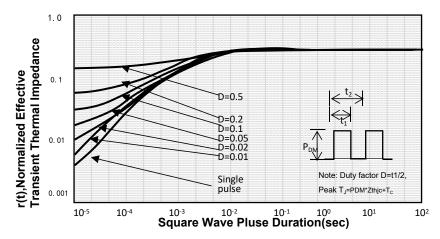


Figure 9.1 Transient Thermal Response Curve  $(R_{thJC})$ 

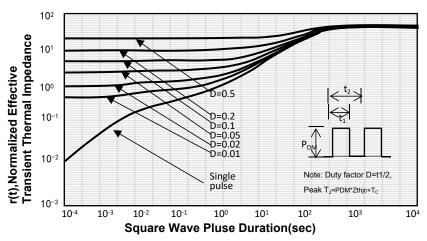
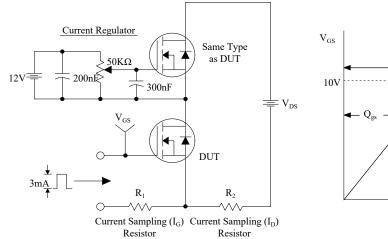
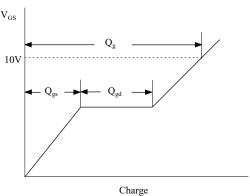


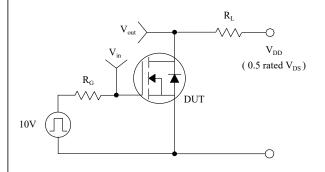
Figure 9.2. Transient Thermal Response Curve(R<sub>thJA</sub>)

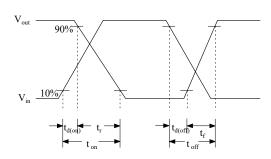
### **Gate Charge Test Circuit & Waveform**



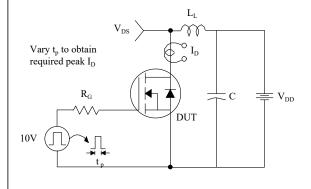


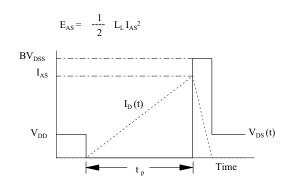
## **Resistive Switching Test Circuit & Waveforms**



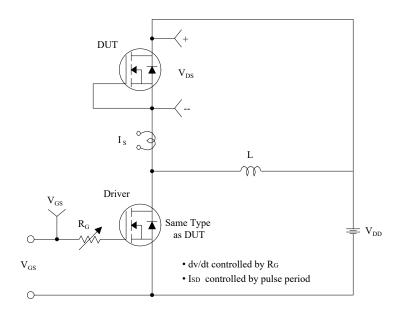


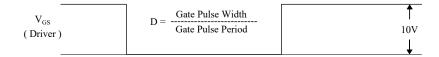
# **Unclamped Inductive Switching Test Circuit & Waveforms**

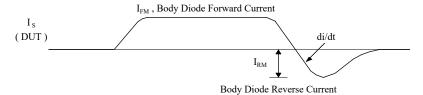


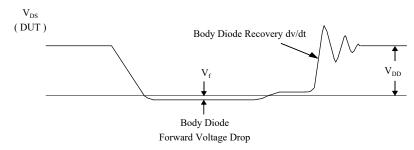


## Peak Diode Recovery dv/dt Test Circuit & Waveforms

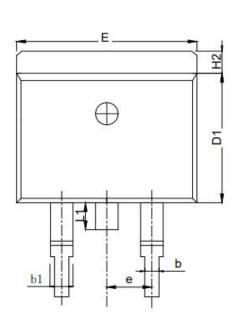


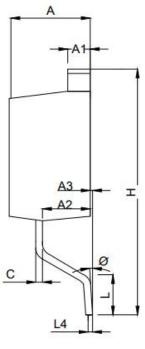


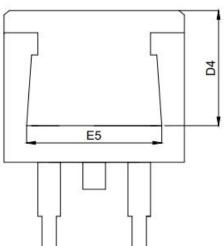




## **TO-263 OUTLINE**







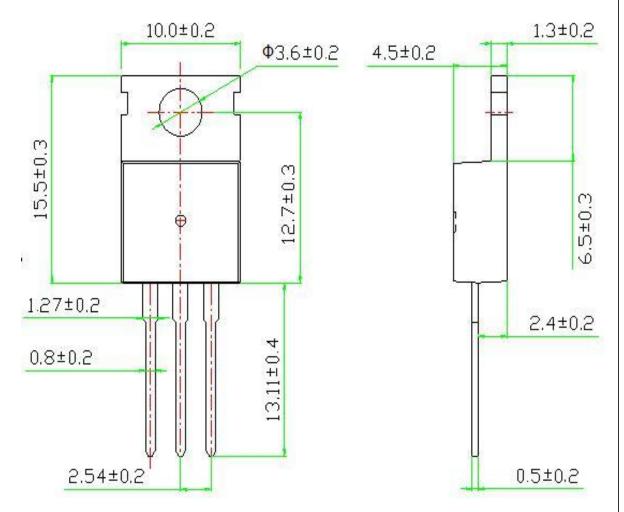
Symbol	DIMENSIONS (unit:mm)			
000	Min	Typ	Max	
A	4.37	4.57	4, 77	
A1	1.22	1.27	1.42	
A2	2.49	2.69	2.89	
A3	0	0.13	0.25	
b	0.7	0.81	0.96	
b1	1.17	1.27	1.47	
c	0.3	0.38	0.53	
D1	8. 5	8.7	8.9	
D4	6.6		-	
E	9.86	10.16	10, 36	
E5	7.06	-	-	
e		2.54 BSC		
H	14.7	15. 1	15.5	
H2	1.07	1.27	1.47	
L.	2	2.3	2.6	
L1	1.4	1.55	1.7	
L4	0. 25 BSC			
?	0°	5°	9°	

#### NOTE:

1The plastic package is not marked as smooth surfaceRa=0.1;Subglossy surfaceRa=0.8

2.Undeclared tolerance ± 0.25, Unmarked filletRmax=0.25

## **TO-220C OUTLINE**



#### NOTE:

1The plastic package is not marked as smooth surfaceRa=0.1;Subglossy surfaceRa=0.8 2.Undeclared tolerance  $\pm$  0.25,Unmarked filletRmax=0.25

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