



**CB-FET**

SLP60R380SJ / SLF60R380SJ

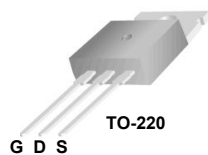
## SLP60R380SJ / SLF60R380SJ 600V N-Channel MOSFET

### General Description

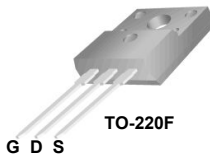
This Power MOSFET is produced using Maple semi's Advanced Super-Junction technology. This advanced technology has been especially tailored to minimize conduction loss, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for AC/DC power conversion in switching mode operation for higher efficiency.

### Features

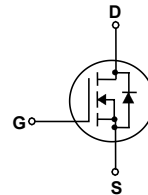
- 11A, 600V,  $R_{DS(on) typ.} = 0.34\Omega @ V_{GS} = 10V$
- Low gate charge ( typical 33nC)
- High ruggedness
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



TO-220



TO-220F



### Absolute Maximum Ratings

$T_C = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	SLP60R380SJ	SLF60R380SJ	Units
$V_{DSS}$	Drain-Source Voltage	600		V
$I_D$	Drain Current - Continuous ( $T_C = 25^\circ\text{C}$ ) - Continuous ( $T_C = 100^\circ\text{C}$ )	11	11*	A
		6.7	6.7 *	A
$I_{DM}$	Drain Current - Pulsed (Note 1)	30	30 *	A
$V_{GSS}$	Gate-Source Voltage	$\pm 30$		V
EAS	Single Pulsed Avalanche Energy (Note 2)	132		mJ
$I_{AR}$	Avalanche Current (Note 1)	2.1		A
$E_{AR}$	Repetitive Avalanche Energy (Note 1)	65		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	5.0		V/ns
$P_D$	Power Dissipation ( $T_C = 25^\circ\text{C}$ ) - Derate above $25^\circ\text{C}$	205	35	W
		1.67	0.3	W/ $^\circ\text{C}$
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150		$^\circ\text{C}$
$T_L$	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300		$^\circ\text{C}$

\* Drain current limited by maximum junction temperature.

### Thermal Characteristics

Symbol	Parameter	SLP60R380SJ	SLF60R380SJ	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.6	3.6	$^\circ\text{C}/\text{W}$
$R_{\theta JS}$	Thermal Resistance, Case-to-Sink Typ.	0.5	--	$^\circ\text{C}/\text{W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62	80	$^\circ\text{C}/\text{W}$

**Electrical Characteristics**T<sub>C</sub> = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	600	--	--	V
ΔBV <sub>DSS</sub> / ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 250 μA, Referenced to 25°C	--	0.6	--	V/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 600 V, V <sub>GS</sub> = 0 V	--	--	1	μA
		V <sub>DS</sub> = 480 V, T <sub>C</sub> = 125°C	--	--	10	μA
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 30 V, V <sub>DS</sub> = 0 V	--	--	100	nA
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	V <sub>GS</sub> = -30 V, V <sub>DS</sub> = 0 V	--	--	-100	nA
<b>On Characteristics</b>						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	2.5	--	4.5	V
R <sub>DS(on)</sub>	Static Drain-Source On-Resistance	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 5.5 A	--	0.34	0.38	Ω
g <sub>FS</sub>	Forward Transconductance	V <sub>DS</sub> = 40 V, I <sub>D</sub> = 5.5 A (Note 4)	--	16	--	S
<b>Dynamic Characteristics</b>						
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1.0 MHz	--	680	--	pF
C <sub>oss</sub>	Output Capacitance		--	140	--	pF
C <sub>riss</sub>	Reverse Transfer Capacitance		--	5	--	pF
<b>Switching Characteristics</b>						
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 400 V, I <sub>D</sub> = 5.5 A, R <sub>G</sub> = 20 Ω (Note 4, 5)	--	26	--	ns
t <sub>r</sub>	Turn-On Rise Time		--	60	--	ns
t <sub>d(off)</sub>	Turn-Off Delay Time		--	75	--	ns
t <sub>f</sub>	Turn-Off Fall Time		--	44	--	ns
Q <sub>g</sub>	Total Gate Charge	V <sub>DS</sub> = 480 V, I <sub>D</sub> = 11 A, V <sub>GS</sub> = 10 V (Note 4, 5)	--	33	--	nC
Q <sub>gs</sub>	Gate-Source Charge		--	4	--	nC
Q <sub>gd</sub>	Gate-Drain Charge		--	4.2	--	nC
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
I <sub>S</sub>	Maximum Continuous Drain-Source Diode Forward Current		--	--	11	A
I <sub>SM</sub>	Maximum Pulsed Drain-Source Diode Forward Current		--	--	30	A
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 11 A	--	--	1.5	V
t <sub>rr</sub>	Reverse Recovery Time	V <sub>GS</sub> = 0 V, I <sub>S</sub> = 11 A,	--	270	--	ns
Q <sub>rr</sub>	Reverse Recovery Charge	dI <sub>F</sub> / dt = 100 A/μs (Note 4)	--	3.3	--	μC

**Notes:**

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2. I<sub>AS</sub> = 2.1A, V<sub>DD</sub> = 50V, R<sub>G</sub> = 25Ω, Starting T<sub>J</sub> = 25°C
3. I<sub>SD</sub> ≤ 10A, di/dt ≤ 200A/μs, V<sub>DD</sub> ≤ BV<sub>DSS</sub>, Starting T<sub>J</sub> = 25°C
4. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%
5. Essentially independent of operating temperature

Typical Characteristics

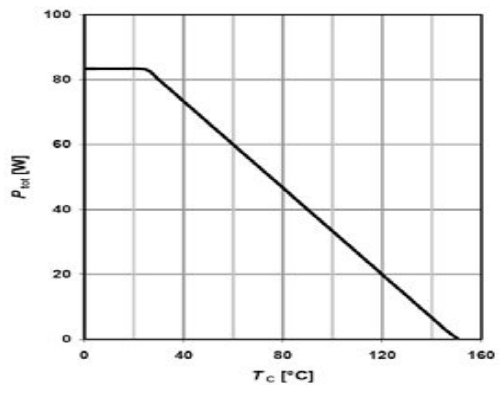


Figure 1. Power Dissipation for SLP60R380SJ

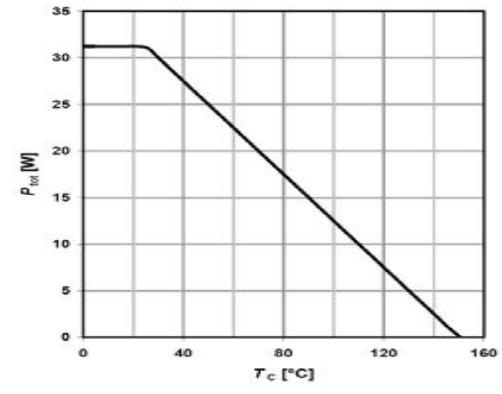


Figure 2. Power Dissipation for SLF60R380SJ

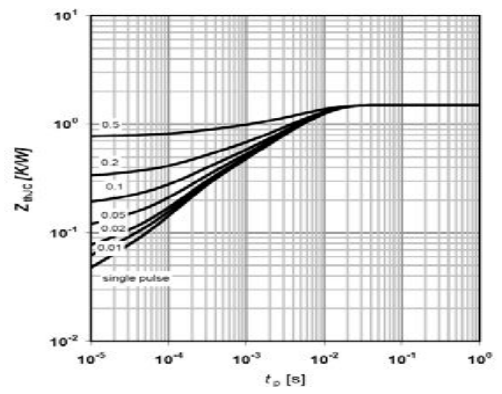


Figure 3. Transient Thermal Response Curve for SLP60R380SJ

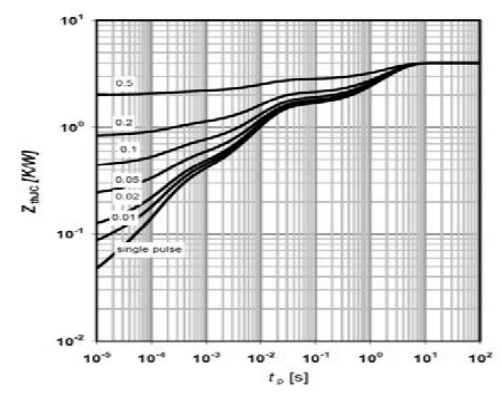


Figure 4. Transient Thermal Response Curve for SLF60R380SJ

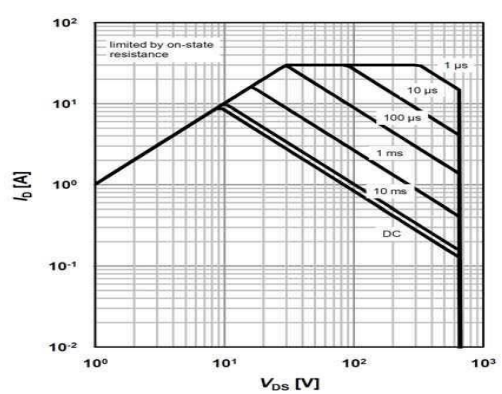


Figure 5. Maximum Safe Operating Area for SLP60R380SJ@25°C

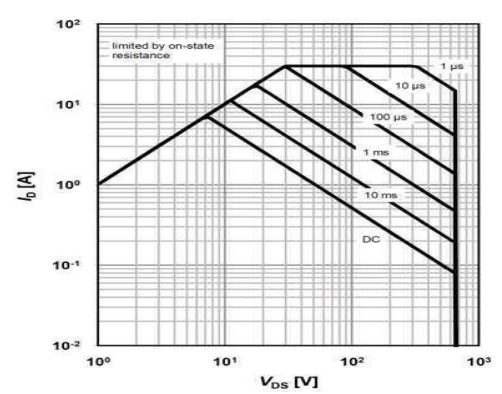


Figure 6. Maximum Safe Operating Area for SLF60R380SJ@25°C

Typical Characteristics (Continued)

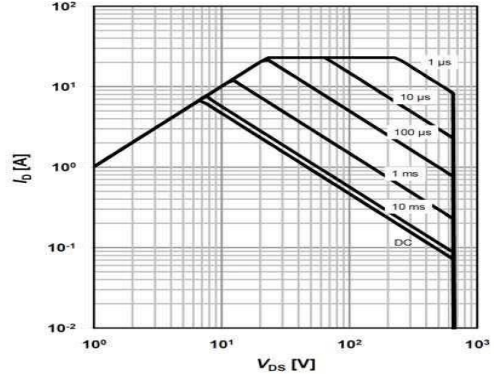


Figure 7. Maximum Safe Operating Area for SLP60R380SJ@80°C

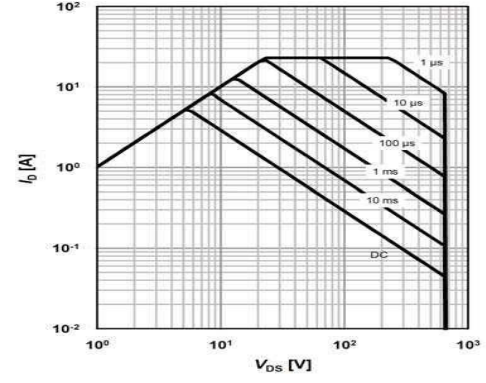


Figure 8. Maximum Safe Operating Area for SLF60R380SJ@80°C

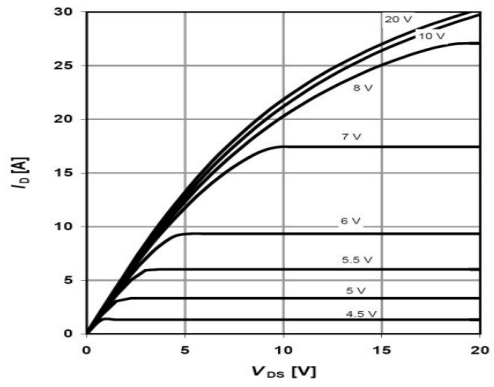


Figure 9. On-Region Characteristics@25°C

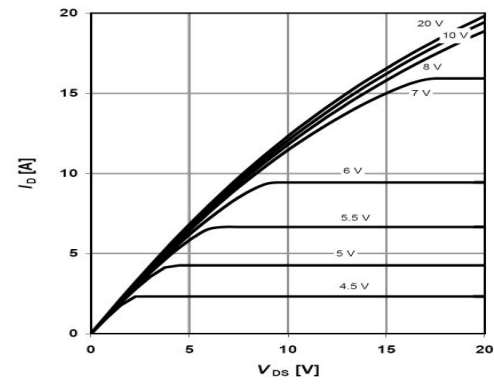


Figure 10. On-Region Characteristics@125°C

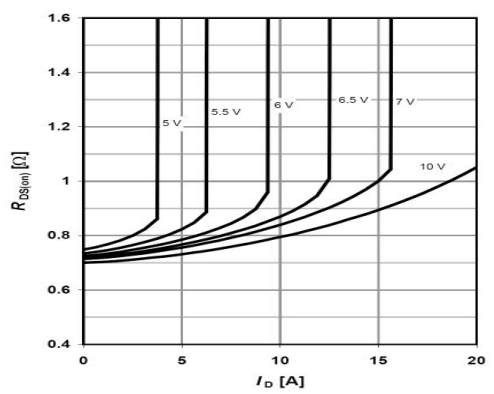


Figure 11. On-Resistance Variation vs Drain Current and Gate Voltage@125°C

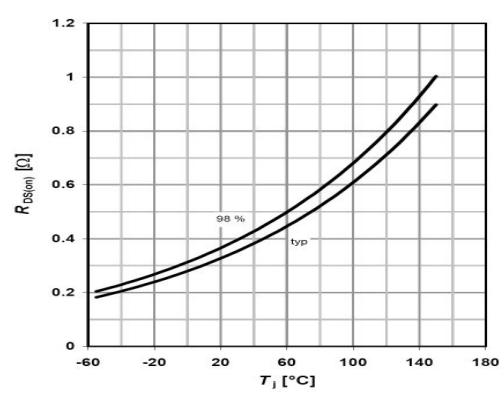
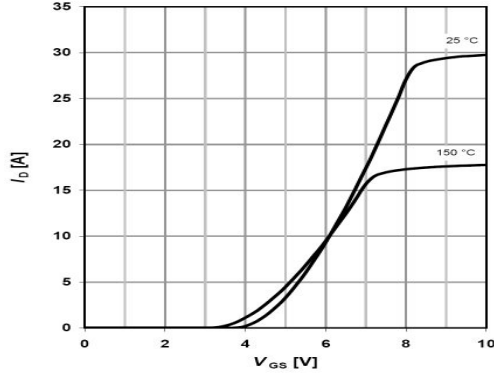
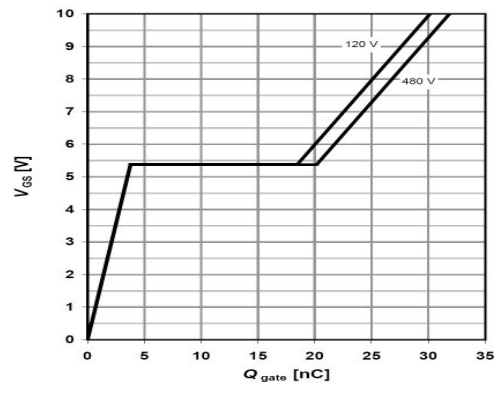


Figure 12. On-Resistance Variation vs Temperature

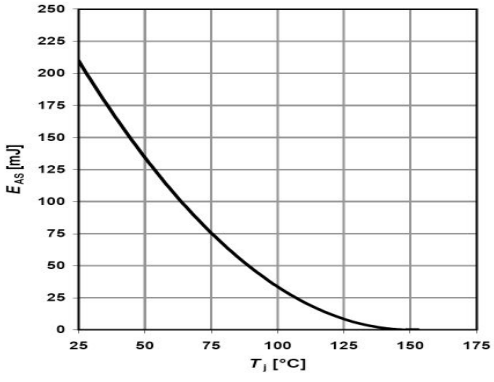
**Typical Characteristics** (Continued)



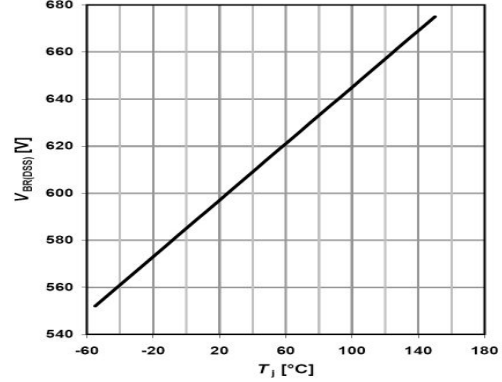
**Figure 13. Transfer Characteristics**



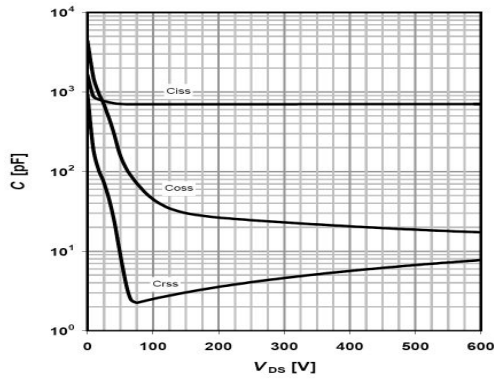
**Figure 14. Gate Charge Characteristics**



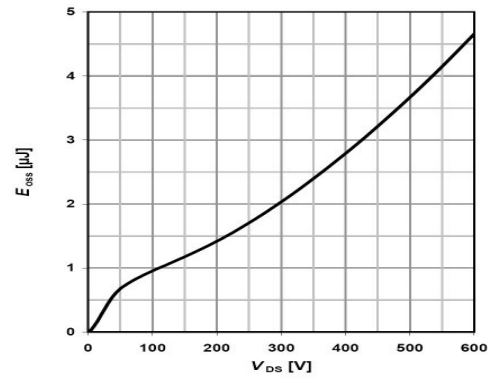
**Figure 15. Avalanche Energy Characteristics**



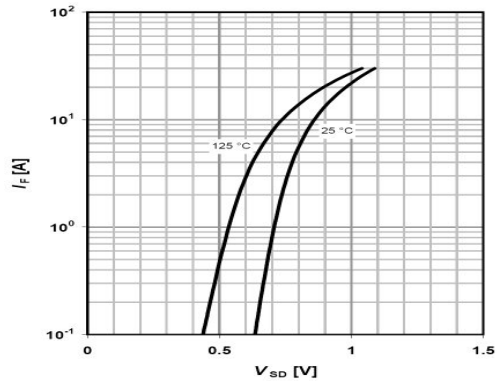
**Figure 16. Breakdown Voltage Variation vs Temperature**



**Figure 17. Capacitance Characteristics**

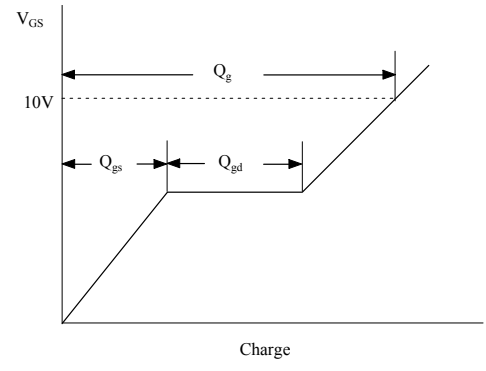
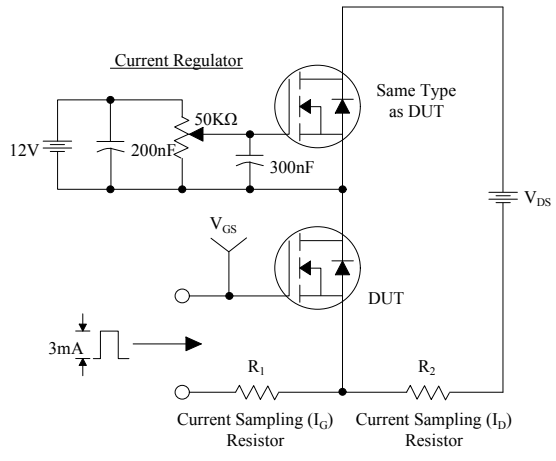


**Figure 18. On-Resistance Variation vs Temperature**

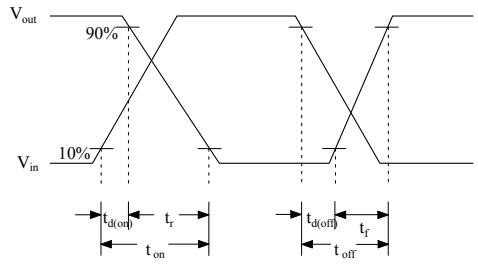
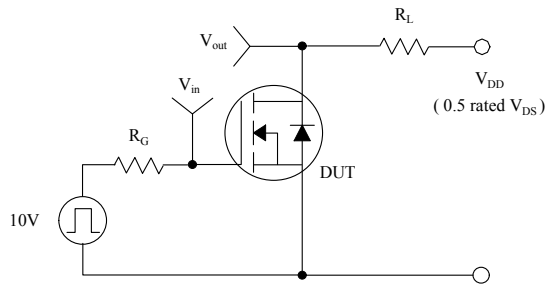
**Typical Characteristics** (Continued)

**Figure 19. Body Diode Forward Voltage Variation with Source Current and Temperature**

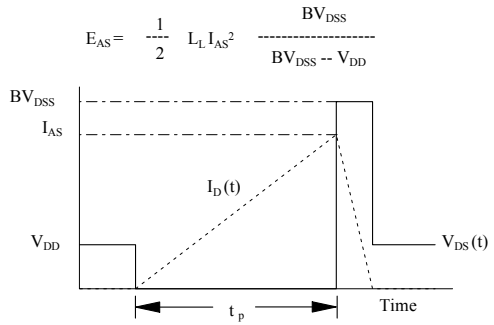
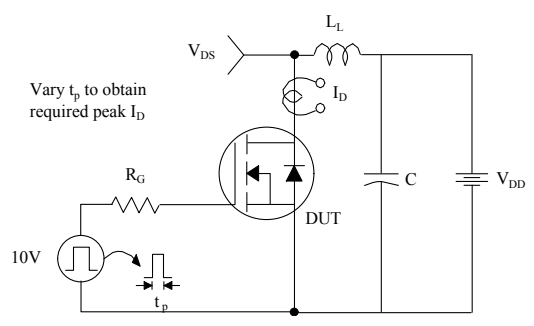
### Gate Charge Test Circuit & Waveform



### Resistive Switching Test Circuit & Waveforms



### Unclamped Inductive Switching Test Circuit & Waveforms



### Peak Diode Recovery dv/dt Test Circuit & Waveforms

