



CB-FET

SLP65R420SJ / SLF65R420SJ

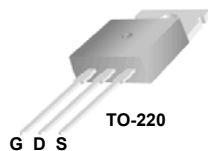
SLP65R420SJ / SLF65R420SJ 650V N-Channel MOSFET

General Description

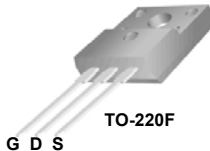
This Power MOSFET is produced using Maple semi's Advanced Super-Junction technology. This advanced technology has been especially tailored to minimize conduction loss, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for AC/DC power conversion in switching mode operation for higher efficiency.

Features

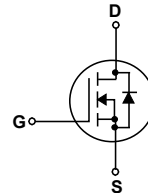
- 11A, 650V, $R_{DS(on) typ.} = 0.38 \Omega @ V_{GS} = 10 V$
- Low gate charge (typical 33nC)
- High ruggedness
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



TO-220



TO-220F



Absolute Maximum Ratings

$T_C = 25^\circ C$ unless otherwise noted

Symbol	Parameter	SLP65R420SJ	SLF65R420SJ	Units
V_{DSS}	Drain-Source Voltage	650		V
I_D	Drain Current - Continuous ($T_C = 25^\circ C$) - Continuous ($T_C = 100^\circ C$)	11	11 *	A
		6.6	6.6 *	A
I_{DM}	Drain Current - Pulsed (Note 1)	44	44 *	A
V_{GSS}	Gate-Source Voltage	± 30		V
EAS	Single Pulsed Avalanche Energy (Note 2)	132		mJ
I_{AR}	Avalanche Current (Note 1)	2.1		A
E_{AR}	Repetitive Avalanche Energy (Note 1)	65		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	5.0		V/ns
P_D	Power Dissipation ($T_C = 25^\circ C$) - Derate above $25^\circ C$	205	35	W
		1.67	0.3	W/ $^\circ C$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150		$^\circ C$
T_L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300		$^\circ C$

* Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	SLP65R420SJ	SLF65R420SJ	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	0.6	3.6	$^\circ C/W$
$R_{\theta JS}$	Thermal Resistance, Case-to-Sink Typ.	0.5	--	$^\circ C/W$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62	80	$^\circ C/W$

Electrical CharacteristicsT_c = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	650	--	--	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	--	0.6	--	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V	--	--	1	μA
		V _{DS} = 480 V, T _C = 125°C	--	--	10	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V	--	--	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V	--	--	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	2.5	--	4.5	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 5.5 A	--	0.38	0.42	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 5.5 A (Note 4)	--	16	--	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz	--	680	--	pF
C _{oss}	Output Capacitance		--	140	--	pF
C _{rss}	Reverse Transfer Capacitance		--	5	--	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 400 V, I _D = 5.5 A, R _G = 20 Ω (Note 4, 5)	--	26	--	ns
t _r	Turn-On Rise Time		--	60	--	ns
t _{d(off)}	Turn-Off Delay Time		--	75	--	ns
t _f	Turn-Off Fall Time		--	44	--	ns
Q _g	Total Gate Charge	V _{DS} = 480 V, I _D = 11 A, V _{GS} = 10 V (Note 4, 5)	--	33	--	nC
Q _{gs}	Gate-Source Charge		--	4	--	nC
Q _{gd}	Gate-Drain Charge		--	4.2	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	11	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	44	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 11 A	--	--	1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 11 A,	--	270	--	ns
Q _{rr}	Reverse Recovery Charge	dI _F / dt = 100 A/μs (Note 4)	--	3.3	--	μC

Notes:

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2. I_{AS} = 2.1A, V_{DD} = 150V, R_G = 25 Ω, Starting T_J = 25°C
3. I_{SD} ≤ 10A, di/dt ≤ 200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C
4. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%
5. Essentially independent of operating temperature

Typical Characteristics

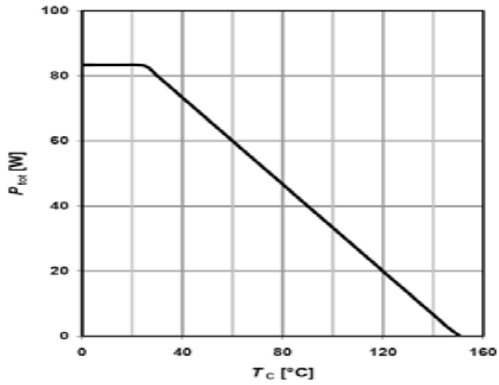


Figure 1. Power Dissipation for SLP65R420SJ

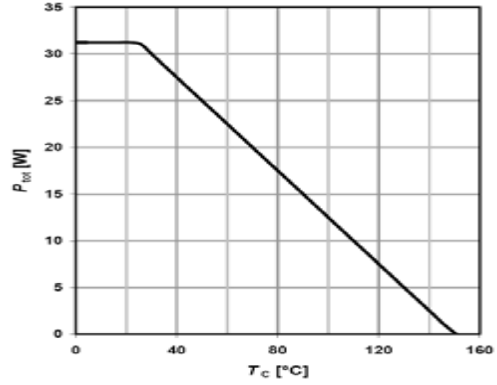


Figure 2. Power Dissipation for SLF65R420SJ

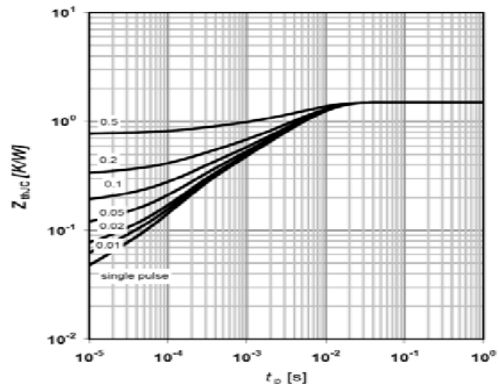


Figure 3. Transient Thermal Response Curve for SLP65R420SJ

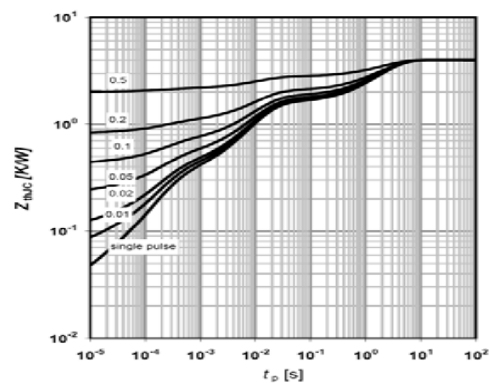


Figure 4. Transient Thermal Response Curve for SLF65R420SJ

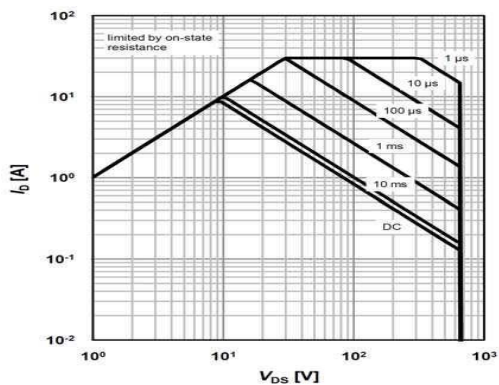


Figure 5. Maximum Safe Operating Area for SLP65R420SJ@25°C

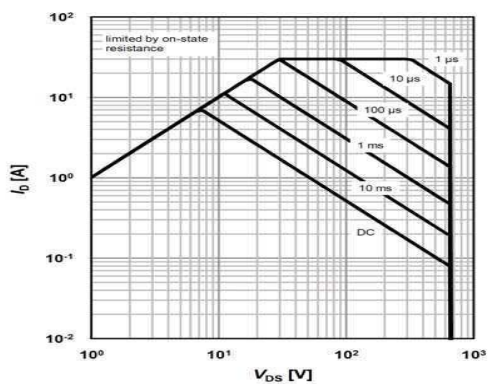


Figure 6. Maximum Safe Operating Area for SLF65R420SJ@25°C

Typical Characteristics (Continued)

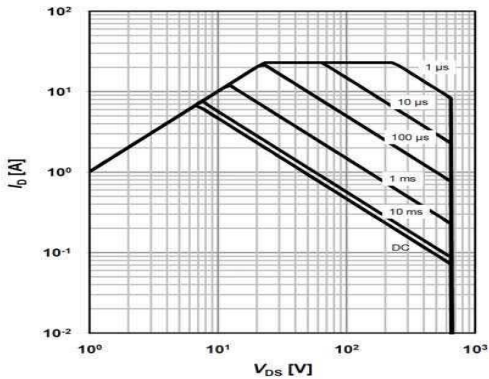


Figure 7. Maximum Safe Operating Area for SLP65R420SJ@80°C

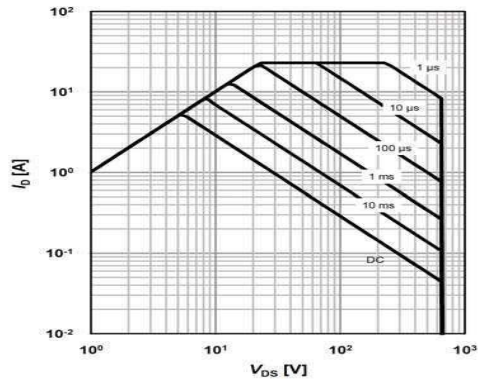


Figure 8. Maximum Safe Operating Area for SLP65R420SJ@80°C

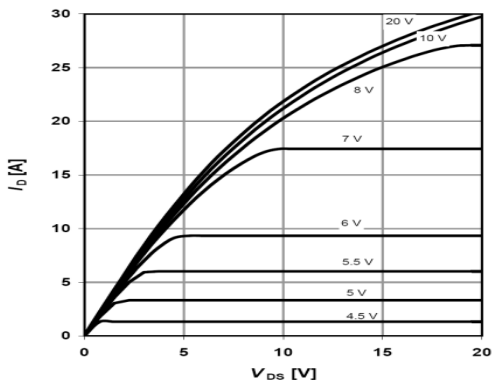


Figure 9. On-Region Characteristics@25°C

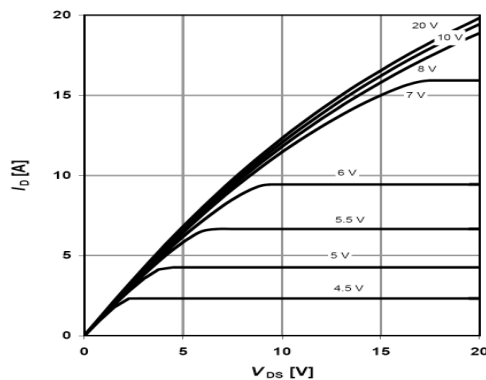


Figure 10. On-Region Characteristics@125°C

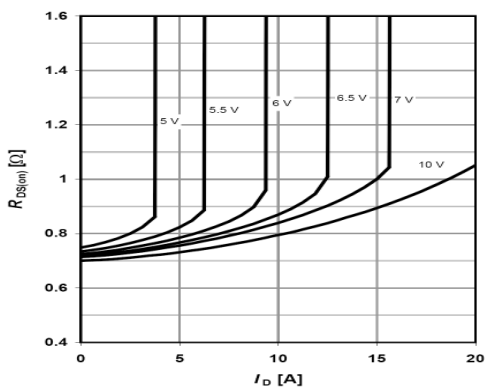


Figure 11. On-Resistance Variation vs Drain Current and Gate Voltage@125°C

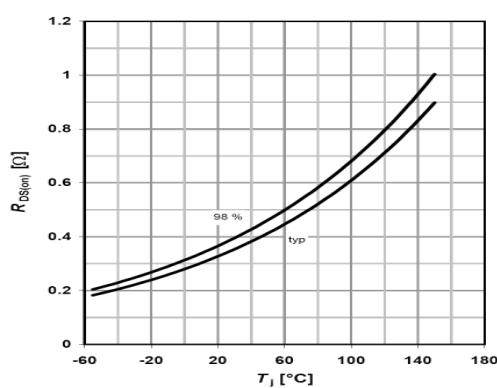


Figure 12. On-Resistance Variation vs Temperature

Typical Characteristics (Continued)

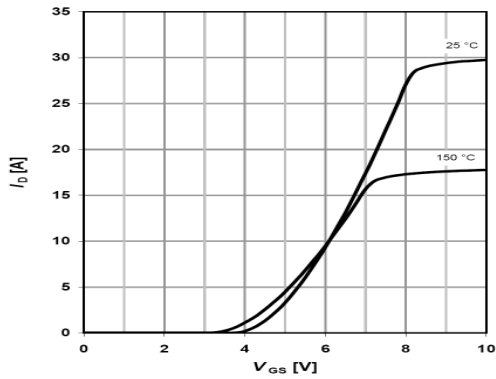


Figure 13. Transfer Characteristics

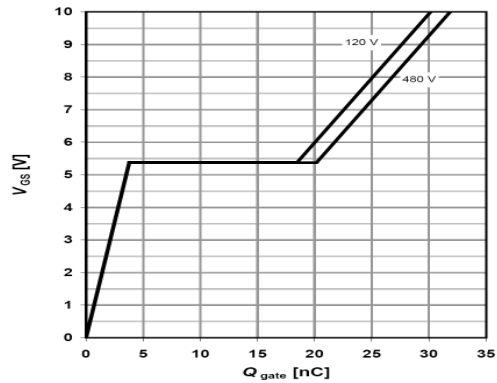


Figure 14. Gate Charge Characteristics

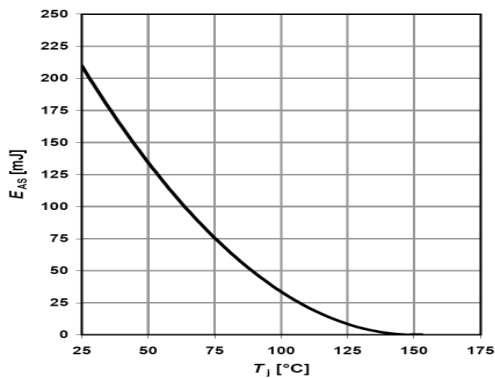


Figure 15. Avalanche Energy Characteristics

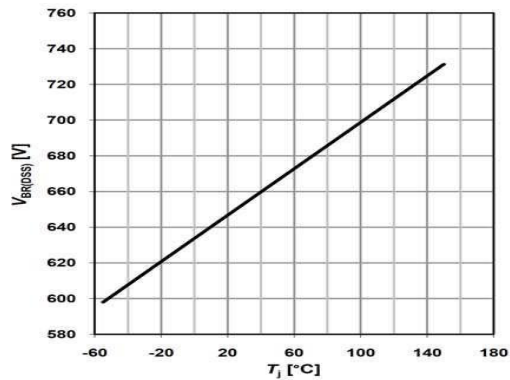


Figure 16. Breakdown Voltage Variation vs Temperature

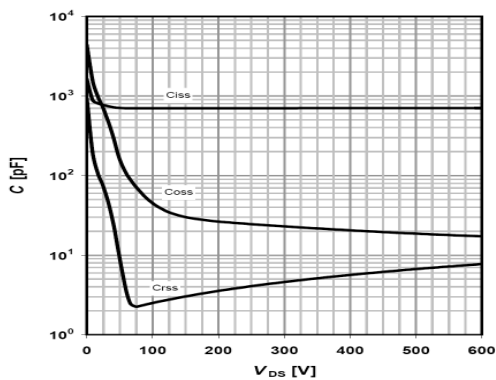


Figure 17. Capacitance Characteristics

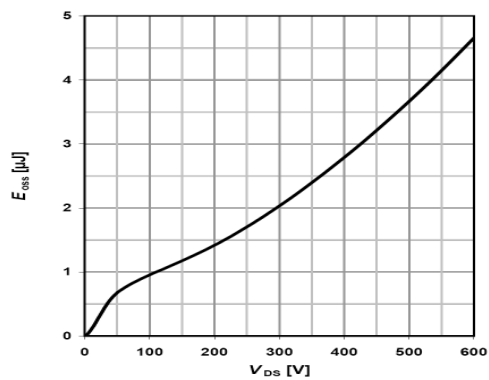


Figure 18. On-Resistance Variation vs Temperature

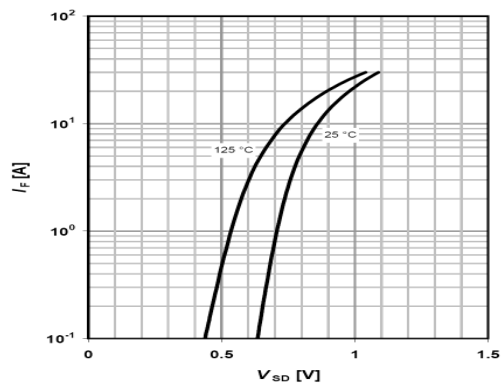
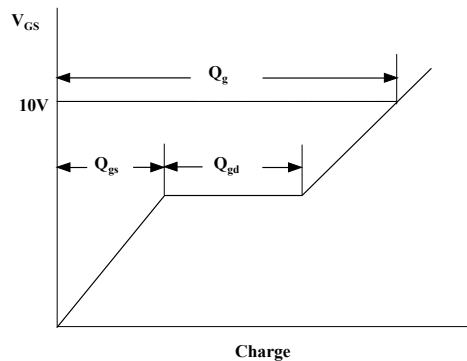
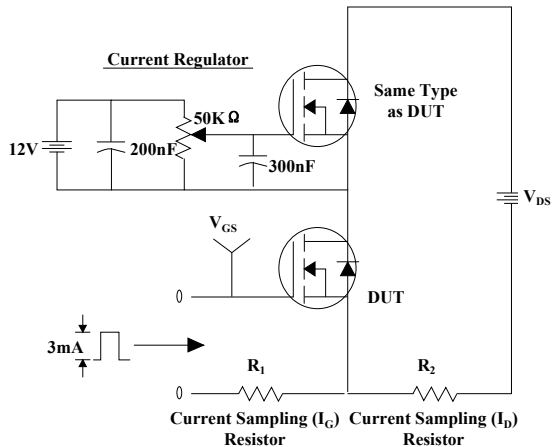
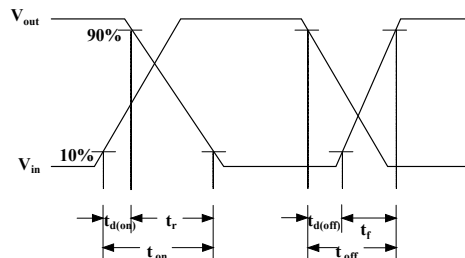
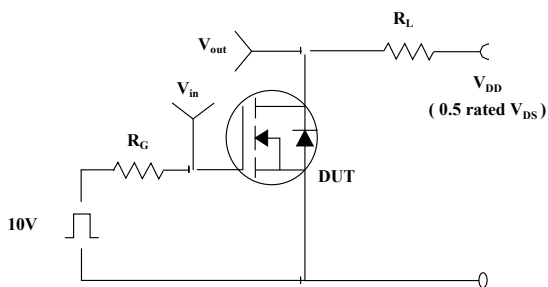
Typical Characteristics (Continued)

Figure 19. Body Diode Forward Voltage Variation with Source Current and Temperature

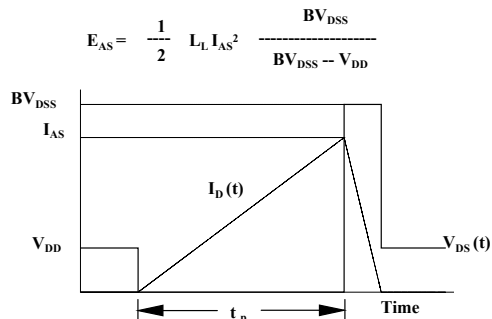
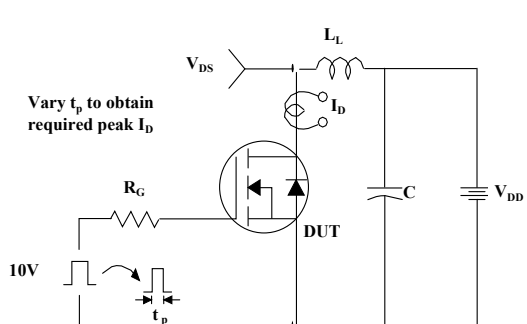
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms



Peak Diode Recovery dv/dt Test Circuit & Waveforms

