



Surface Mountable 5 TAP Digital Delay Modules

Select smaller 14 PIN SOIC
or 14 PIN STD units

FEATURES

- Low profile
- Industry standard .050 or .100 pin pattern
- Schottky TTL buffered input/output
- 5 equally spaced taps
- Leading edge precision
- Meets requirements of infrared and vapor phase soldering methods
- Lead coplanarity of $\pm .002$
- Minimum input pulse width: 40% total delay

SUPPLY CURRENT

- For high level output: 30 mA MAX
- For low level output: 60 mA MAX

ELECTRICAL SPECIFICATIONS (25°C AND $V_{CC}=5.0$ V)

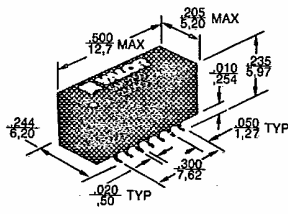
Total Delay ⁽¹⁾ (ns $\pm 5\%$)	Tap Delay ⁽¹⁾⁽²⁾ (ns)	Rise Time ⁽³⁾ (ns)	Gull Wing	
			14 PIN SOIC	14 PIN STD <small>NEW</small>
25	5	3	SM0025	SG5025
30	6	3	SM0030	SG5030
35	7	3	SM0035	SG5035
40	8	3	SM0040	SG5040
45	9	3	SM0045	SG5045
50	10	3	SM0050	SG5050
60	12	3	SM0060	SG5060
75	15	3	SM0075	SG5075
80	16	3	SM0080	SG5080
100	20	3	SM0100	SG5100
125	25	4	SM0125	SG5125
150	30	4	SM0150	SG5150
175	35	4	SM0175	SG5175
200	40	4	SM0200	SG5200
225	45	4	SM0225	SG5225
250	50	4	SM0250	SG5250

NOTES: (1) Delays measured at 1.5 V levels of positive going edge.

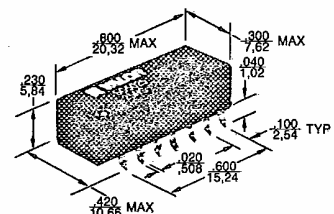
(2) TOL. 10% or 2 ns whichever is greater.

(3) Rise Time measured from .75 V to 2.4 V levels.

14 PIN STD SOIC Gull Wing Configuration



14 PIN STD Gull Wing Configuration



LEAD COPLANARITY
 $\pm .002$
 $\pm .051$

