

Dual Enhancement Mode MOSFET (N-and P-Channel)

Features

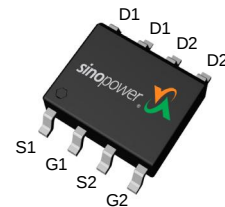
- **N Channel**
30V/8A,
 $R_{DS(ON)} = 22m\Omega$ (max.) @ $V_{GS} = 10V$
 $R_{DS(ON)} = 30m\Omega$ (max.) @ $V_{GS} = 4.5V$
- **P Channel**
-30V/-7A,
 $R_{DS(ON)} = 28m\Omega$ (max.) @ $V_{GS} = -10V$
 $R_{DS(ON)} = 44m\Omega$ (max.) @ $V_{GS} = -4.5V$
- 100% UIS Tested
- Reliable and Rugged
- Lead Free Available (RoHS Compliant)
- HBM ESD protection level pass 3KV

Note : The diode connected between the gate and source serves only as protection against ESD. No gate overvoltage rating is implied.

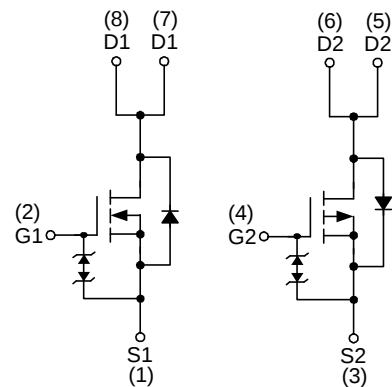
Applications

- Synchronous Rectification.
- Motor Control.
- High Current, High Speed Switching.

Pin Description



Top View of SOP-8



N-Channel MOSFET

P-Channel MOSFET

Ordering and Marking Information

SM4901CS □□-□□ □ Assembly Material Handling Code Temperature Range Package Code	Package Code K : SOP-8 Operating Junction Temperature Range C : -55 to 150 °C Handling Code TR : Tape & Reel (2500ea/reel) Assembly Material G : Halogen and Lead Free Device
SM4901CS K :	4901CS XXXXX XXXXX - Lot Code

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter			N Channel	P Channel	Unit	
Common Ratings							
V _{DSS}	Drain-Source Voltage			30	-30	V	
V _{GSS}	Gate-Source Voltage			±20	±20	V	
T _J	Maximum Junction Temperature			150		°C	
T _{STG}	Storage Temperature Range			-55 to 150		°C	
I _S	Diode Continuous Forward Current			2	-2	A	
I _{DP} ^a	Pulse Drain Current Tested	V _{GS} =10V(N),V _{GS} =-10V(P)		32	-28	A	
I _D	Continuous Drain Current			T _A =25°C	8	-7	A
				T _A =70°C	6.3	-5.6	
P _D ^b	Maximum Power Dissipation		T _A =25°C	2.08	2.08	W	
			T _A =70°C	1.33	1.33		
R _{θJL}	Thermal Resistance-Junction to Lead		Steady State	50	50	°C/W	
R _{θJA} ^c	Thermal Resistance-Junction to Ambient		t ≤ 10s	60	60	°C/W	
			Steady State	100	100		
I _{AS} ^d	Avalanche Current, Single pulse		L=0.5mH	7.2	11	A	
E _{AS} ^d	Avalanche Energy, Single pulse		L=0.5mH	13	30	mJ	

Note a : Pulse width limited by max. junction temperature.

Note b : $t < 10\text{sec}$.

Note c : $R_{\theta JA}$ steady state $t=999\text{s}$. $R_{\theta JA}$ is measured with the device mounted on 1in^2 , FR-4 board with 2oz. Copper.

Note d : UIS tested and pulse width limited by maximum junction temperature 150°C (initial temperature $T_J=25^\circ\text{C}$).

N Channel Electrical Characteristics (T_A = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	N Channel			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =24V, V _{GS} =0V	-	-	1	μA
		T _J =85°C	-	-	30	
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1.5	1.8	2.5	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±10	μA
R _{DS(ON)} ^e	Drain-Source On-state Resistance	V _{GS} =10V, I _{DS} =8A	-	18.5	22	mΩ
		V _{GS} =4.5V, I _{DS} =5A	-	23	30	
Diode Characteristics						
V _{SD} ^e	Diode Forward Voltage	I _{SD} =1A, V _{GS} =0V	-	0.75	1.1	V
t _{rr}	Reverse Recovery Time	I _{DS} =8A, dI _{SD} /dt=100A/μs	-	12	-	ns
Q _{rr}	Reverse Recovery Charge		-	3.5	-	nC
Dynamic Characteristics ^f						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	1.8	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =15V, Frequency=1.0MHz	-	408	-	pF
C _{oss}	Output Capacitance		-	72	-	
C _{rss}	Reverse Transfer Capacitance		-	40	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =15V, R _L =15Ω, I _{DS} =1A, V _{GEN} =10V, R _G =6Ω	-	5.2	-	ns
t _r	Turn-on Rise Time		-	8.6	-	
t _{d(OFF)}	Turn-off Delay Time		-	13.5	-	
t _f	Turn-off Fall Time		-	3.4	-	
Gate Charge Characteristics ^f						
Q _g	Total Gate Charge	V _{DS} =15V, V _{GS} =10V, I _{DS} =8A	-	8.3	12	nC
Q _g	Total Gate Charge	V _{DS} =15V, V _{GS} =4.5V, I _{DS} =8A	-	3.9	6	
Q _{gth}	Threshold Gate Charge		-	0.73	-	
Q _{gs}	Gate-Source Charge		-	1.6	-	
Q _{gd}	Gate-Drain Charge		-	1.4	-	

Note e : Pulse test ; pulse width≤300μs, duty cycle≤2%.

Note f : Guaranteed by design, not subject to production testing.

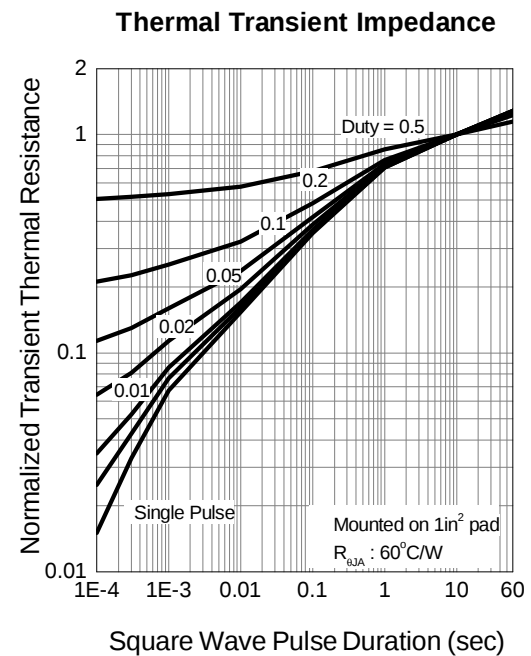
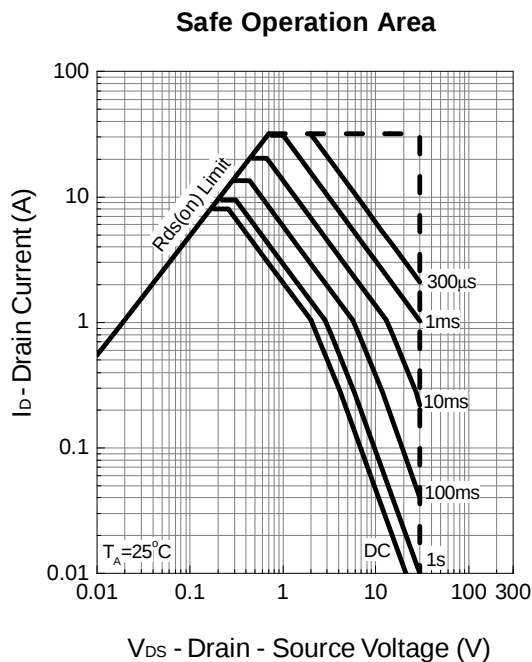
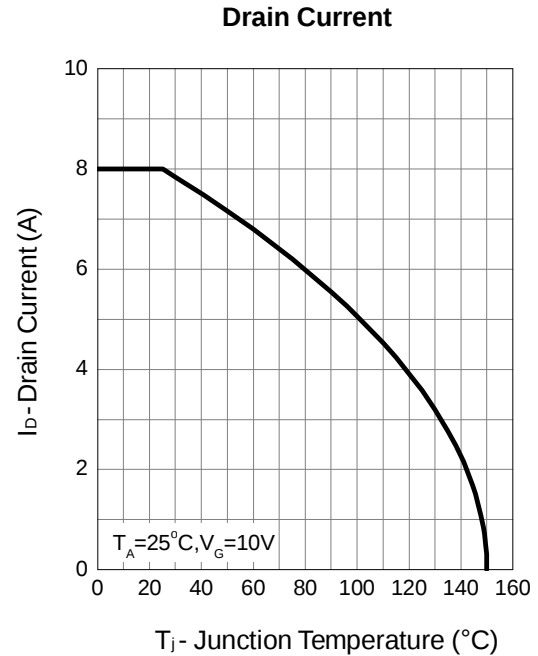
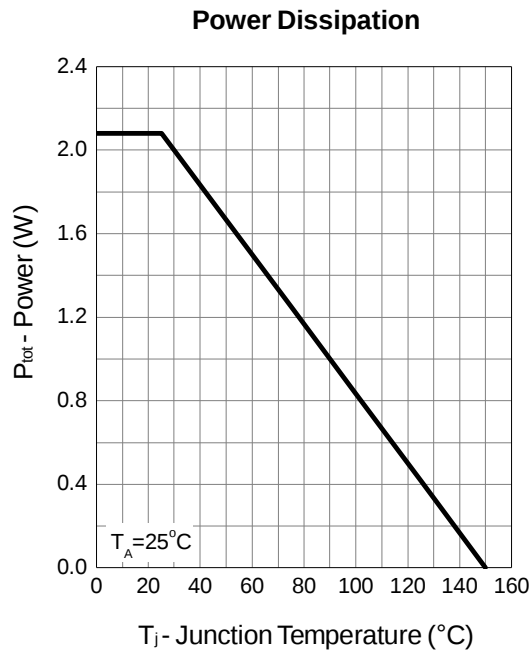
P Channel Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Test Conditions	P Channel			Unit
			Min.	Typ.	Max.	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =-250μA	-30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-24V, V _{GS} =0V	-	-	-1	μA
		T _J =85°C	-	-	-30	mA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =-250μA	-1.3	-1.8	-2.3	V
I _{GSS}	Gate Leakage Current	V _{GS} =±20V, V _{DS} =0V	-	-	±10	μA
R _{DS(ON)} ^e	Drain-Source On-state Resistance	V _{GS} =-10V, I _{DS} =-7A	-	22	28	mΩ
		V _{GS} =-4.5V, I _{DS} =-4A	-	32	44	
Diode Characteristics						
V _{SD} ^e	Diode Forward Voltage	I _{SD} =-1A, V _{GS} =0V	-	-0.75	-1	V
t _{rr}	Reverse Recovery Time	I _{DS} =-7A, dI _{SD} /dt=100A/μs	-	14	-	ns
Q _{rr}	Reverse Recovery Charge		-	7	-	nC
Dynamic Characteristics ^f						
R _G	Gate Resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	-	12	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-15V, Frequency=1.0MHz	-	750	-	pF
C _{oss}	Output Capacitance		-	142	-	
C _{rss}	Reverse Transfer Capacitance		-	102	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =-15V, R _L =15Ω, I _{DS} =-1A, V _{GEN} =-10V, R _G =6Ω	-	9	-	ns
t _r	Turn-on Rise Time		-	11	-	
t _{d(OFF)}	Turn-off Delay Time		-	55	-	
t _f	Turn-off Fall Time		-	34	-	
Gate Charge Characteristics ^f						
Q _g	Total Gate Charge	V _{DS} =-15V, V _{GS} =-10V, I _{DS} =-7A	-	17	-	nC
Q _g	Total Gate Charge	V _{DS} =-15V, V _{GS} =-4.5V, I _{DS} =-7A	-	8	-	
Q _{gth}	Threshold Gate Charge		-	1	-	
Q _{gs}	Gate-Source Charge		-	2	-	
Q _{gd}	Gate-Drain Charge		-	4	-	

Note e : Pulse test; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

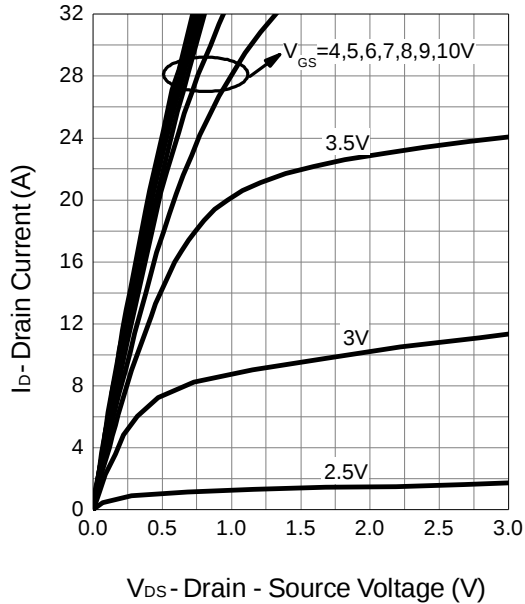
Note f : Guaranteed by design, not subject to production testing.

N Channel Typical Operating Characteristics

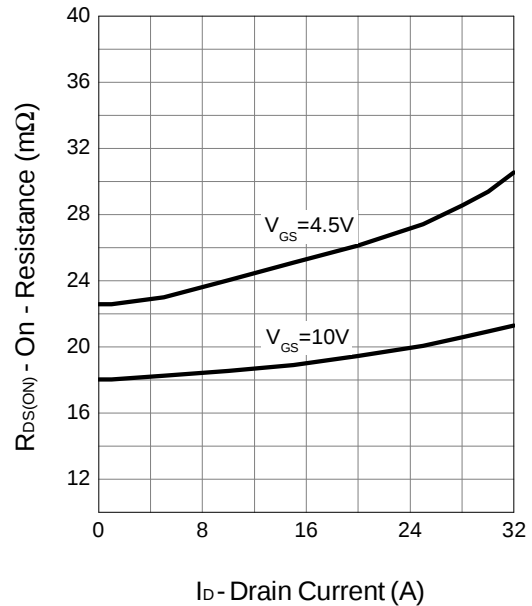


N Channel Typical Operating Characteristics (Cont.)

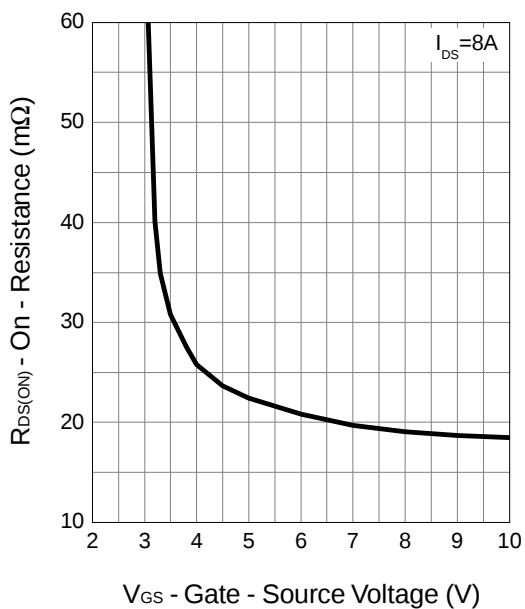
Output Characteristics



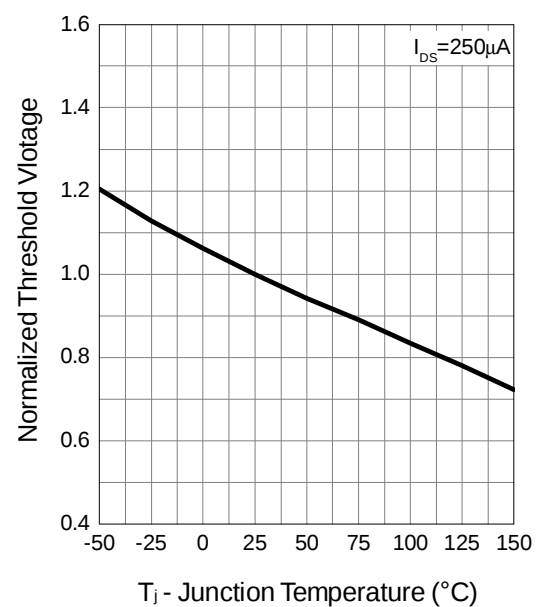
Drain-Source On Resistance



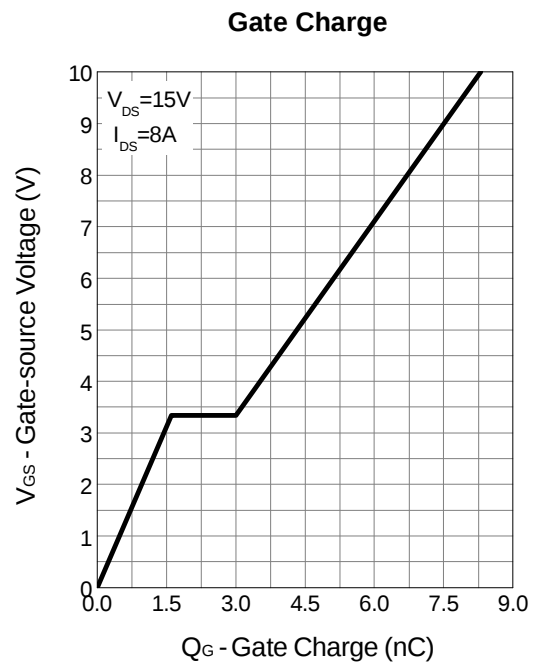
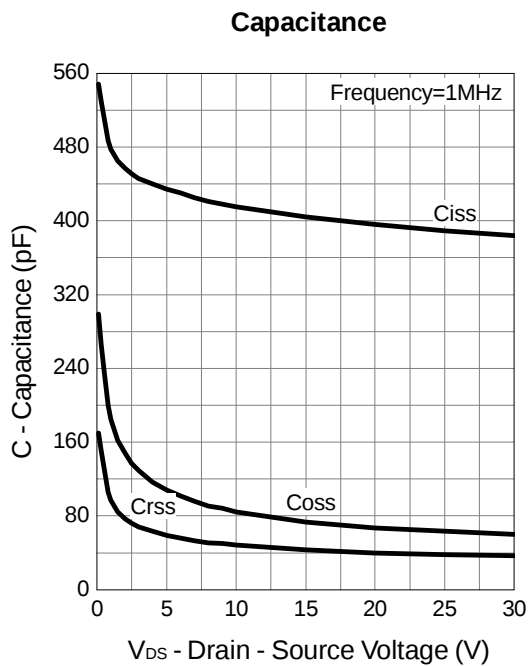
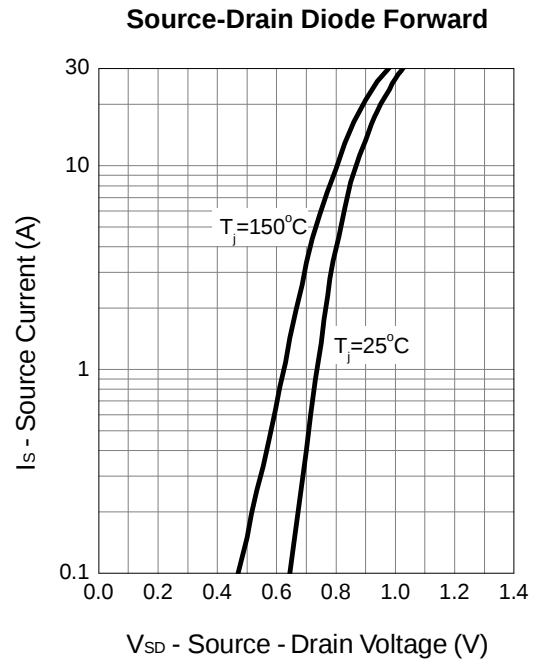
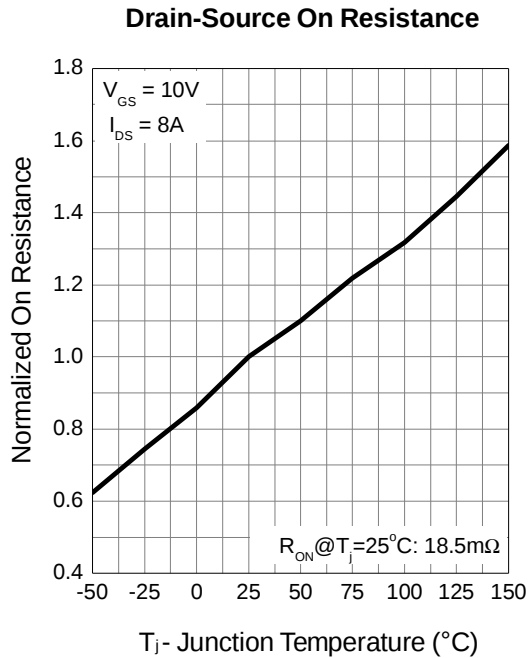
Gate-Source On Resistance



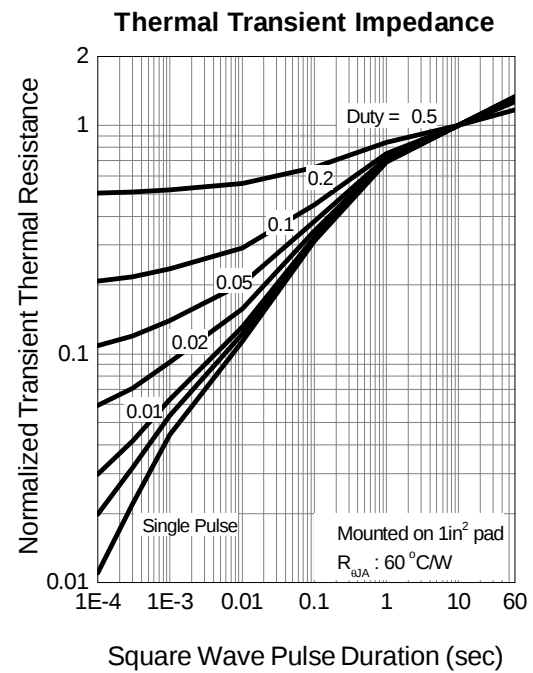
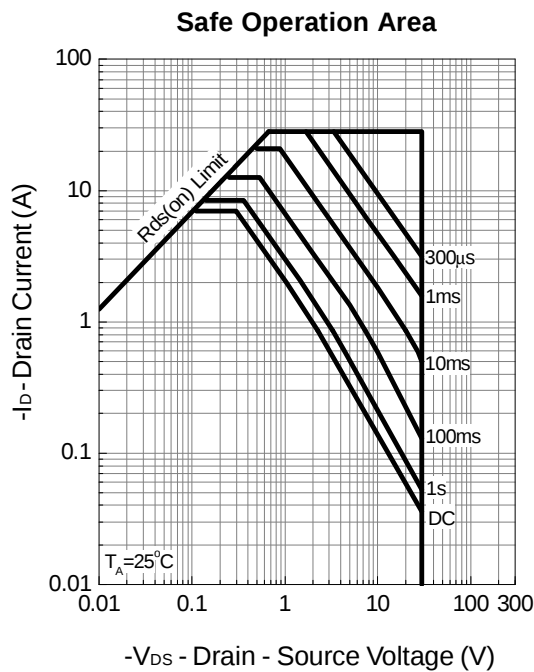
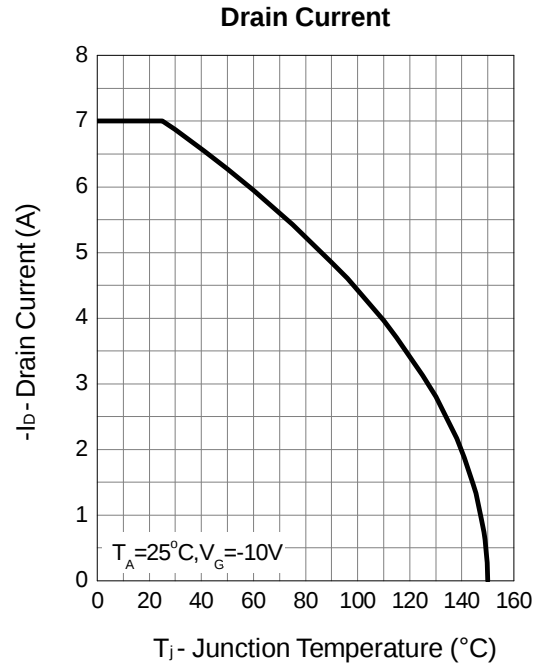
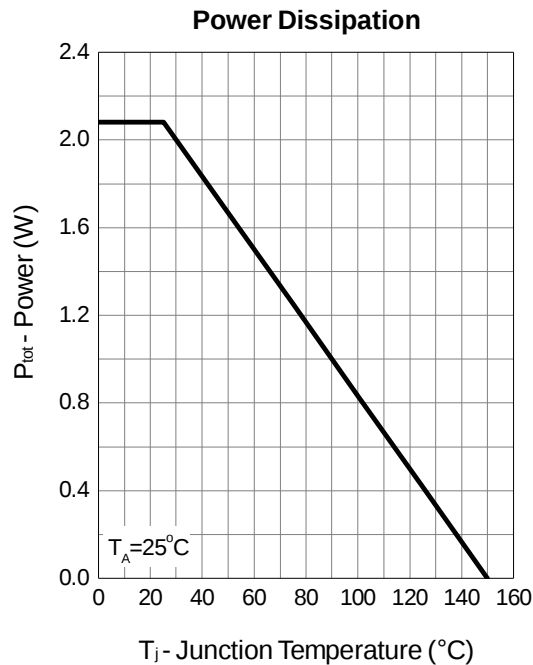
Gate Threshold Voltage



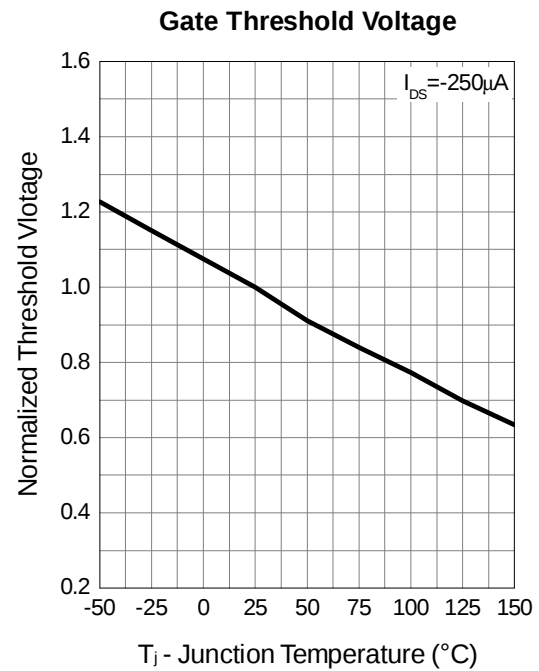
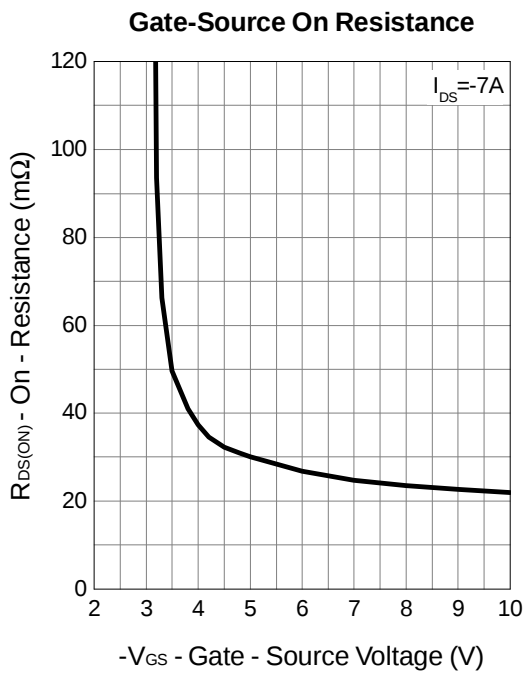
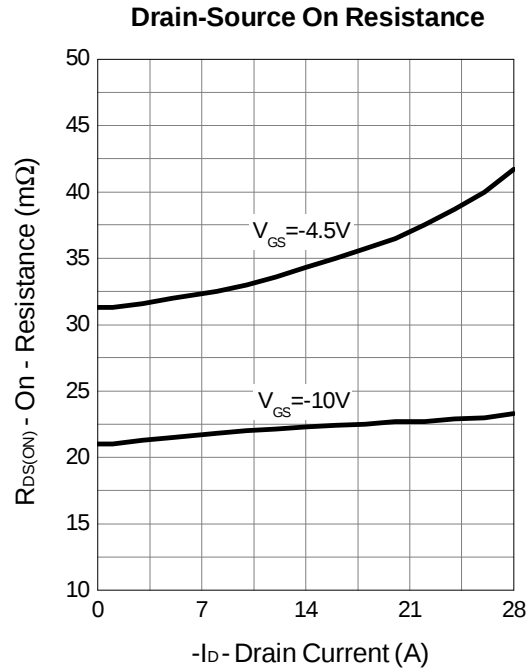
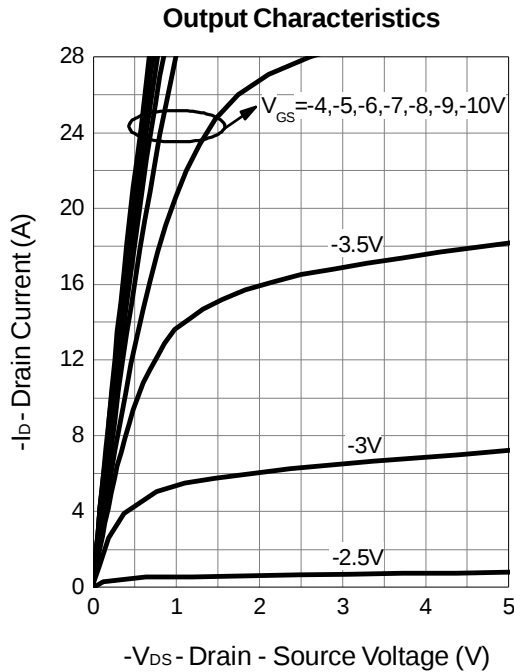
N Channel Typical Operating Characteristics (Cont.)



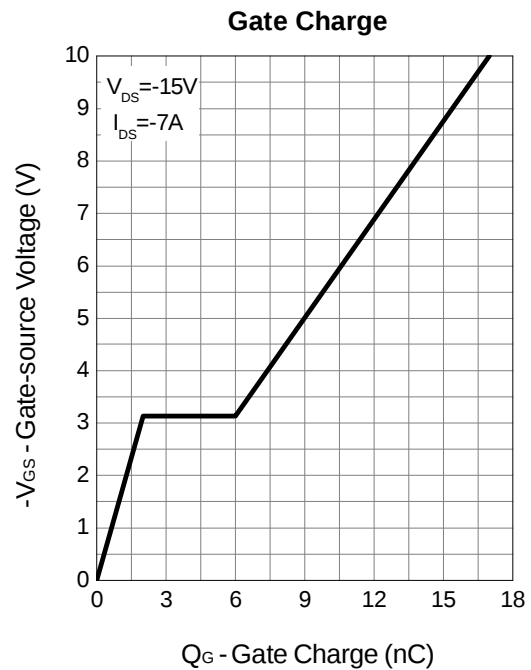
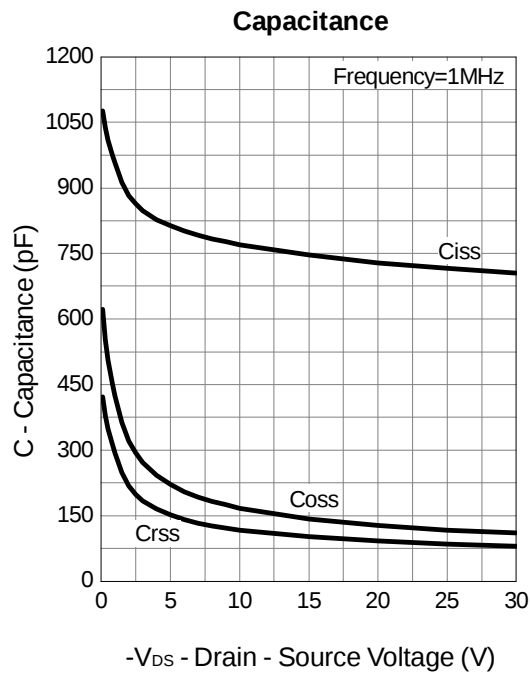
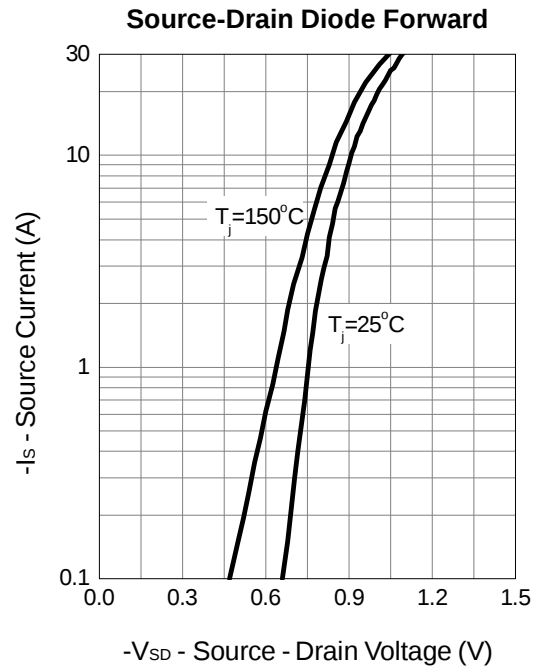
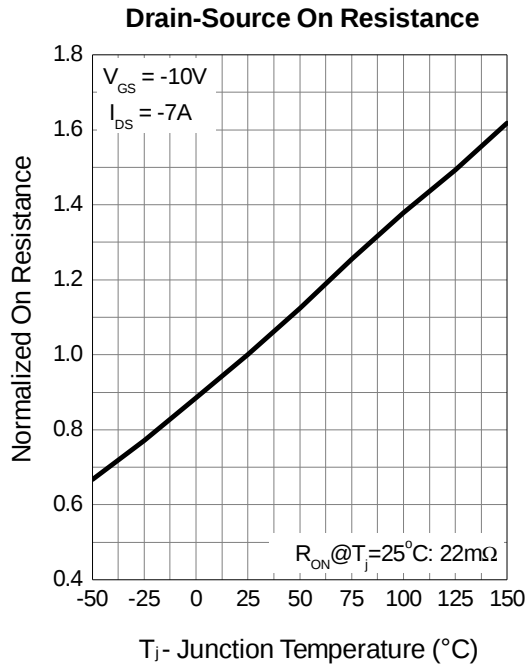
P Channel Typical Operating Characteristics



P Channel Typical Operating Characteristics (Cont.)

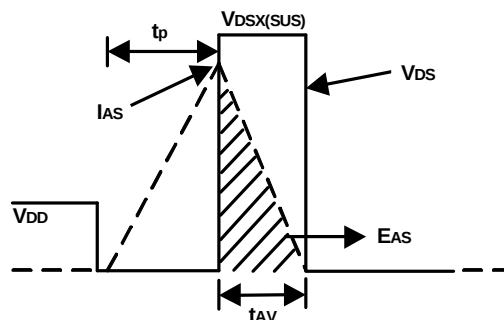
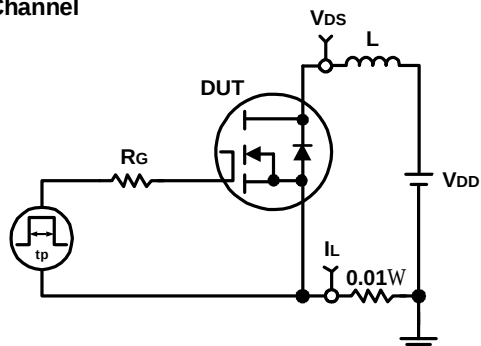


P Channel Typical Operating Characteristics (Cont.)

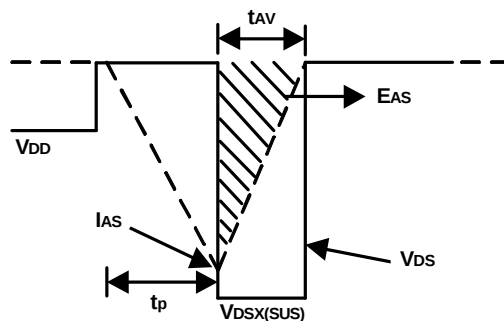
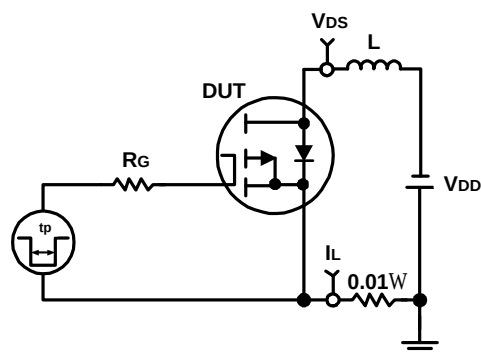


Avalanche Test Circuit and Waveforms

N Channel

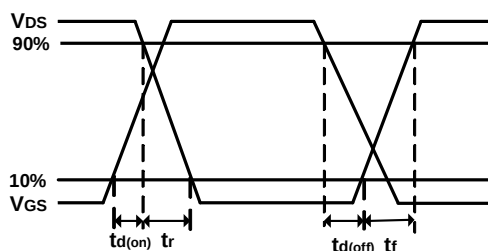
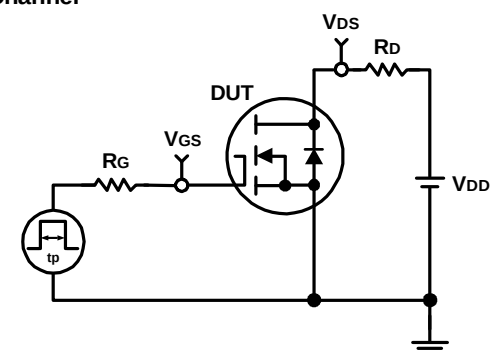


P Channel

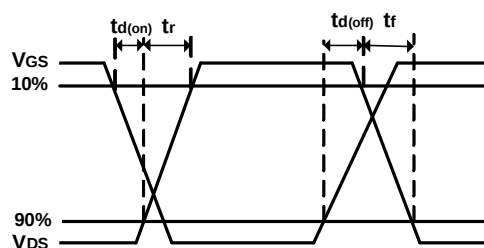
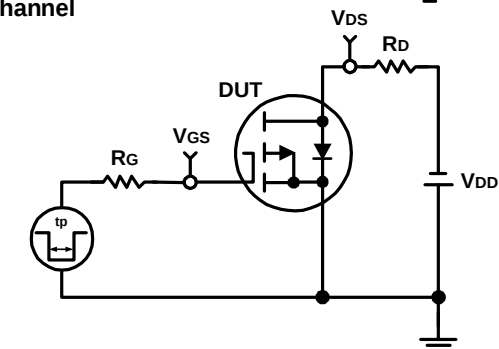


Switching Time Test Circuit and Waveforms

N Channel

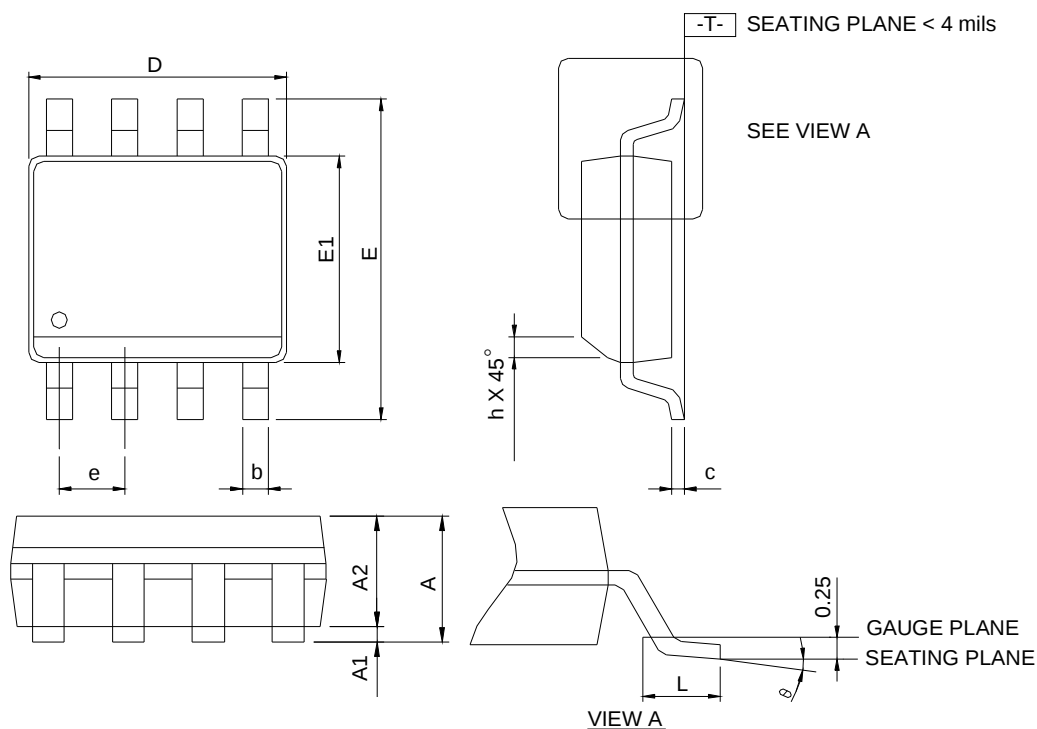


P Channel



Package Information

SOP-8

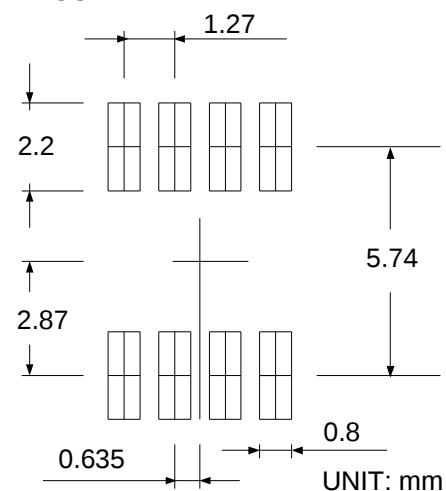


DIMENSIONS	SOP-8			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	-	1.75	-	0.069
A1	0.10	0.25	0.004	0.010
A2	1.25	-	0.049	-
b	0.31	0.51	0.012	0.020
c	0.17	0.25	0.007	0.010
D	4.80	5.00	0.189	0.197
E	5.80	6.20	0.228	0.244
E1	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
h	0.25	0.50	0.010	0.020
L	0.40	1.27	0.016	0.050
θ	0°	8°	0°	8°

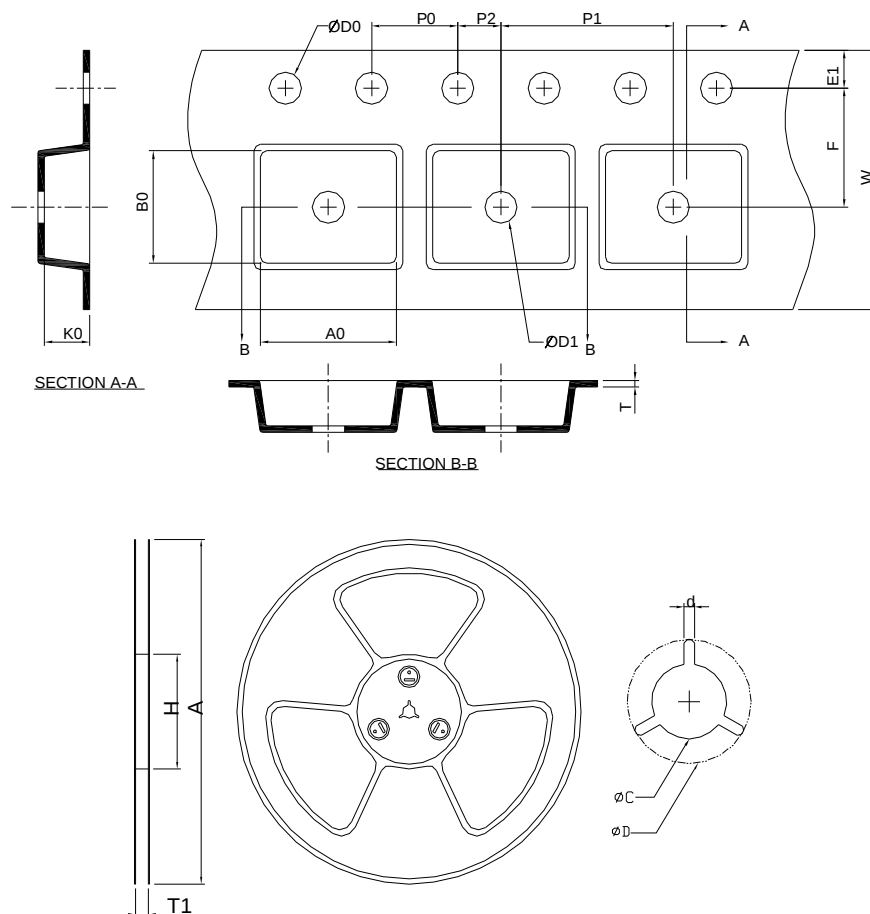
Note: 1. Follow JEDEC MS-012 AA.

- Dimension "D" does not include mold flash, protrusions or gate burrs. Mold flash, protrusion or gate burrs shall not exceed 6 mil per side.
- Dimension "E" does not include inter-lead flash or protrusions. Inter-lead flash and protrusions shall not exceed 10 mil per side.

RECOMMENDED LAND PATTERN



Carrier Tape & Reel Dimensions

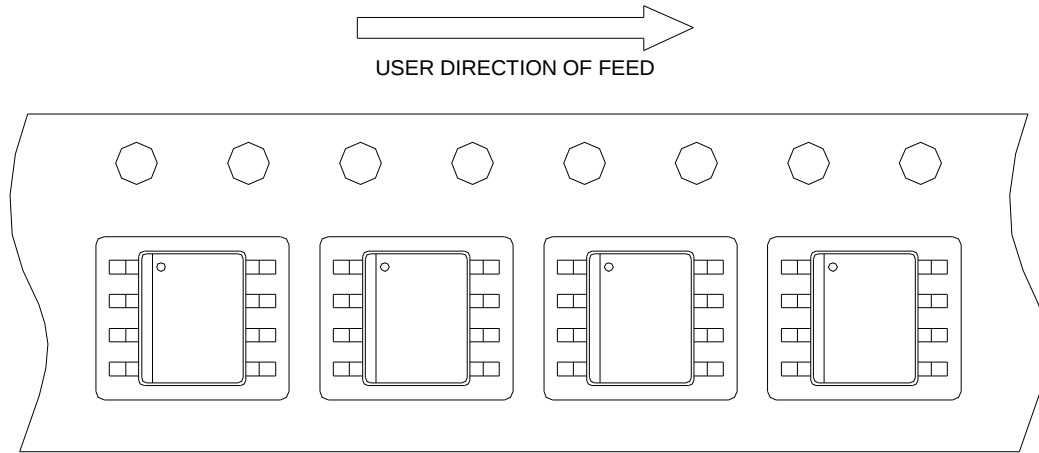


Application	A	H	T1	C	d	D	W	E1	F
SOP-8	330.0 ± 2.00	50 MIN.	$12.4 + 2.00 - 0.00$	$13.0 + 0.50 - 0.20$	1.5 MIN.	20.2 MIN.	12.0 ± 0.30	1.75 ± 0.10	5.5 ± 0.05
	P0	P1	P2	D0	D1	T	A0	B0	K0
	4.0 ± 0.10	8.0 ± 0.10	2.0 ± 0.05	$1.5 + 0.10 - 0.00$	1.5 MIN.	$0.6 + 0.00 - 0.40$	6.40 ± 0.20	5.20 ± 0.20	2.10 ± 0.20

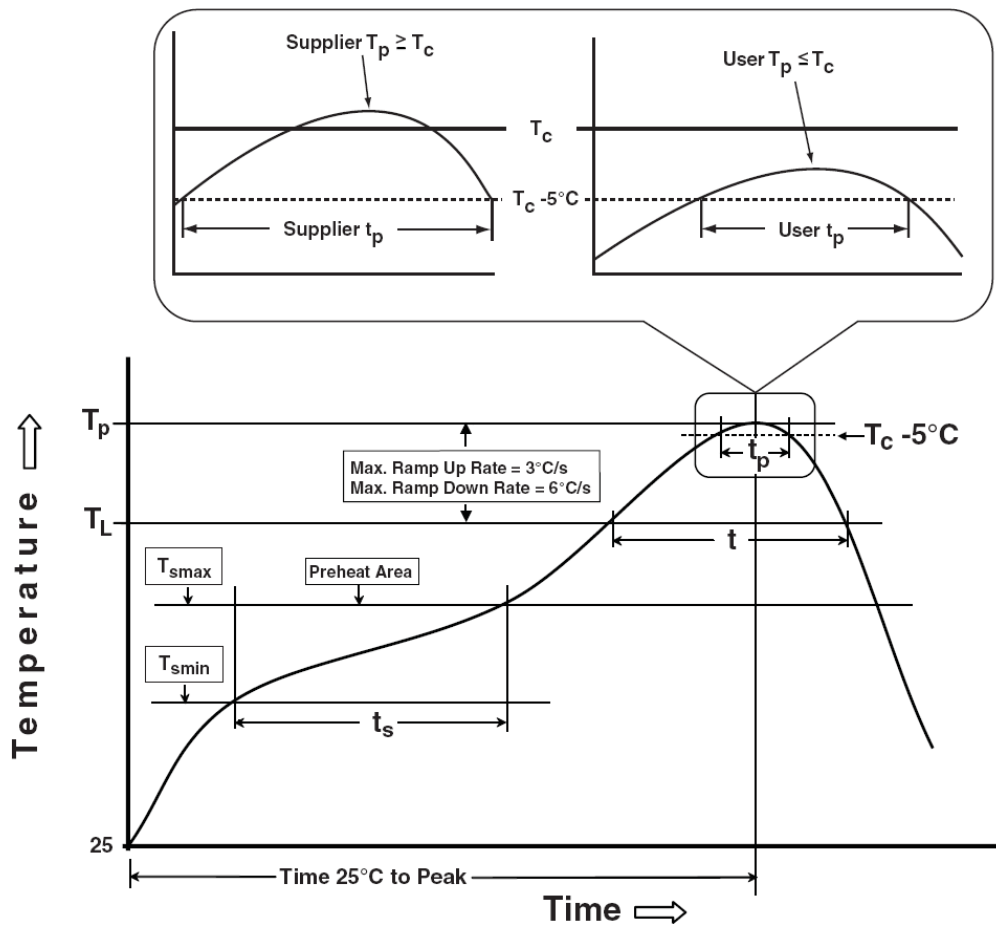
(mm)

Taping Direction Information

SOP-8



Classification Profile



Disclaimer

Sinopower Semiconductor, Inc. (hereinafter “Sinopower”) has been making great efforts to development high quality and better performance products to satisfy all customers’ needs. However, a product may fail to meet customer’s expectation or malfunction for various situations.

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In order to unify the quality and performance, Sinopower has been following JEDEC while defines assembly rule. Notwithstanding all the suppliers basically follow the rule for each product, different processes may cause slightly different results.

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Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak Temperature min (T_{smin}) Temperature max (T_{smax}) Time (T_{smin} to T_{smax}) (t_s)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L) Time at liquidous (t_L)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum. ** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ T_{jmax}
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ T_{jmax}
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

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