

## Dual N-Channel Enhancement Mode MOSFET

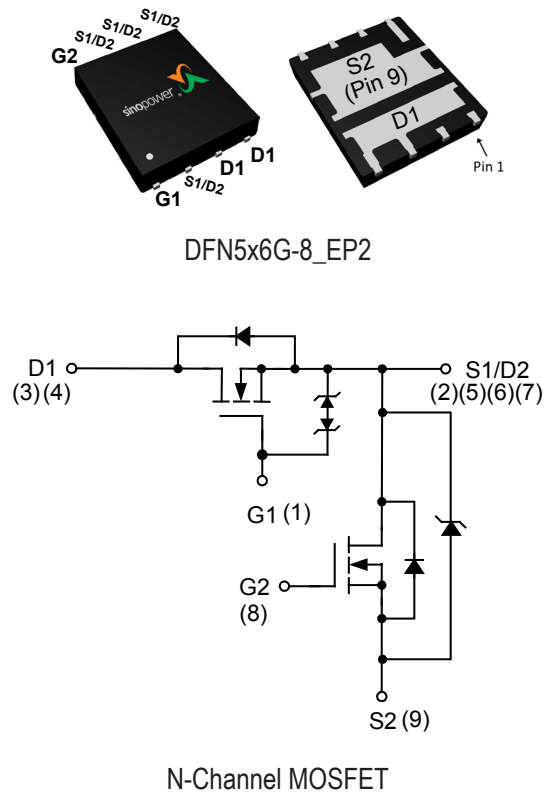
### Features

- Channel 1 (ESD Protection)**  
 30V/67A  
 $R_{DS(ON)}=4.3m\Omega(max.)@V_{GS}=10V$   
 $R_{DS(ON)}=7.2m\Omega(max.)@V_{GS}=4.5V$
- Channel 2 (Integrated Schottky diode)**  
 30V/100A  
 $R_{DS(ON)}=2m\Omega(max.)@V_{GS}=10V$   
 $R_{DS(ON)}=2.5m\Omega(max.)@V_{GS}=4.5V$
- 100% UIS +  $R_g$  Tested
- Dual Dies Package and Minimize Board Space
- Lower  $Q_g$  and  $Q_{gd}$  for High-Speed Switching
- Lower  $R_{DS(ON)}$  to Minimize Conduction Losses
- Reliable and Rugged
- Lead Free and Green Devices Available (RoHS Compliant)

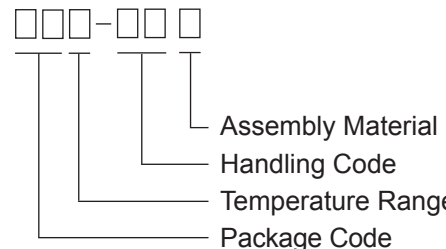

### Applications

- Power Management in Desktop Computer or DC/DC Converters.

### Pin Description



### Ordering and Marking Information

SM7342EK <span style="font-family: monospace;">□□□-□□□</span> 	Package Code KP : DFN5x6G-8_EP2 Operating Junction Temperature Range C : -55 to 150 °C Handling Code TR : Tape & Reel Assembly Material G : Halogen and Lead Free Device
SM7342EK KP : 	XXXXX - Lot Code

Note : SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the leadfree requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

**Absolute Maximum Ratings** ( $T_A=25^{\circ}\text{C}$  Unless Otherwise Noted)

Symbol	Parameter	Channel 1	Channel 2	Unit	
<b>Common Ratings</b>					
$V_{DSS}$	Drain-Source Voltage	30		V	
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	$\pm 12$	V	
$T_J$	Maximum Junction Temperature	150		$^{\circ}\text{C}$	
$T_{STG}$	Storage Temperature Range	-55 to 150		$^{\circ}\text{C}$	
$I_S$	Diode Continuous Forward Current	$T_C=25^{\circ}\text{C}$	14	28	A
$I_D$	Continuous Drain Current	$T_C=25^{\circ}\text{C}$	67	100 <sup>a</sup>	A
		$T_C=100^{\circ}\text{C}$	42	86	
$I_{DM}^b$	Pulse Drain Current	$T_C=25^{\circ}\text{C}$	268	400	A
$P_D$	Maximum Power Dissipation	$T_C=25^{\circ}\text{C}$	31.25	62.5	W
		$T_C=100^{\circ}\text{C}$	12.5	25	
$R_{\theta JC}$	Thermal Resistance-Junction to Case	Steady State	4	2	$^{\circ}\text{C}/\text{W}$
$I_D$	Continuous Drain Current	$T_A=25^{\circ}\text{C}$	14	22	A
		$T_A=70^{\circ}\text{C}$	11	18	
$I_{DM}^b$	Pulse Drain Current	$T_A=25^{\circ}\text{C}$	56	88	A
$P_D$	Maximum Power Dissipation	$T_A=25^{\circ}\text{C}$	1.39	1.67	W
		$T_A=70^{\circ}\text{C}$	0.89	1.07	
$R_{\theta JA}^c$	Thermal Resistance-Junction to Ambient	$t \leq 10\text{s}$	40	38	$^{\circ}\text{C}/\text{W}$
		Steady State	90	75	
$I_{AS}^d$	Avalanche Current, Single pulse	$L=0.1\text{mH}$	24	45	A
$E_{AS}^d$	Avalanche Energy, Single pulse	$L=0.1\text{mH}$	28.8	101.25	mJ

Note a : Max. continuous current is limited by bonding wire.

Note b : Pulse width is limited by max. junction temperature.

Note c : Surface mounted on  $1\text{in}^2$  pad area, steady state  $t = 999\text{s}$ .

Note d : UIS tested and pulse width are limited by maximum junction temperature  $150^{\circ}\text{C}$ (initial temperature  $T_J=25^{\circ}\text{C}$ ).

**Channel 1 Electrical Characteristics** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Test Conditions	Channel 1			Unit
			Min.	Typ.	Max.	
<b>Static Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu A$	30	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=24V, V_{GS}=0V$	-	-	1	$\mu A$
		$T_J=85^\circ C$	-	-	30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	1.3	1.7	2.3	V
$I_{GSS}$	Gate Leakage Current	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	$\pm 10$	$\mu A$
$R_{DS(ON)}^e$	Drain-Source On-state Resistance	$V_{GS}=10V, I_{DS}=15A$	-	3.6	4.3	m $\Omega$
		$T_J=125^\circ C$	-	5.3	-	
		$V_{GS}=4.5V, I_{DS}=12A$	-	5.5	7.2	
Gfs	Forward Transconductance	$V_{DS}=5V, I_{DS}=12A$	-	21	-	S
<b>Diode Characteristics</b>						
$V_{SD}^e$	Diode Forward Voltage	$I_{SD}=15A, V_{GS}=0V$	-	0.8	1.1	V
$t_{rr}$	Reverse Recovery Time	$I_{SD}=15A, di_{SD}/dt=100A/\mu s$	-	14	-	ns
$t_a$	Charge Time		-	12.3	-	
$t_b$	Discharge Time		-	26.3	-	
$Q_{rr}$	Reverse Recovery Charge		-	14.1	-	
<b>Dynamic Characteristics<sup>f</sup></b>						
$R_G$	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	-	1.6	-	$\Omega$
$C_{iss}$	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=15V,$ Frequency=1.0MHz	-	925	-	pF
$C_{oss}$	Output Capacitance		-	550	-	
$C_{riss}$	Reverse Transfer Capacitance		-	46	-	
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=15V, R_L=15\Omega,$ $I_{DS}=1A, V_{GEN}=10V,$ $R_G=6\Omega$	-	13.6	-	ns
$t_r$	Turn-on Rise Time		-	9.9	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	26.7	-	
$t_f$	Turn-off Fall Time		-	22.2	-	
<b>Gate Charge Characteristics<sup>f</sup></b>						
$Q_g$	Total Gate Charge	$V_{DS}=15V, V_{GS}=10V,$ $I_{DS}=15A$	-	15.3	21.4	nC
$Q_g$	Total Gate Charge	$V_{DS}=15V, V_{GS}=4.5V,$ $I_{DS}=15A$	-	7.3	-	
$Q_{gth}$	Threshold Gate Charge		-	1.7	-	
$Q_{gs}$	Gate-Source Charge		-	2.9	-	
$Q_{gd}$	Gate-Drain Charge		-	2.2	-	

Note e : Pulse test ; pulse width $\leq 300\mu s$ , duty cycle $\leq 2\%$ .

Note f : Guaranteed by design, not subject to production testing.

**Channel 2 Electrical Characteristics** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

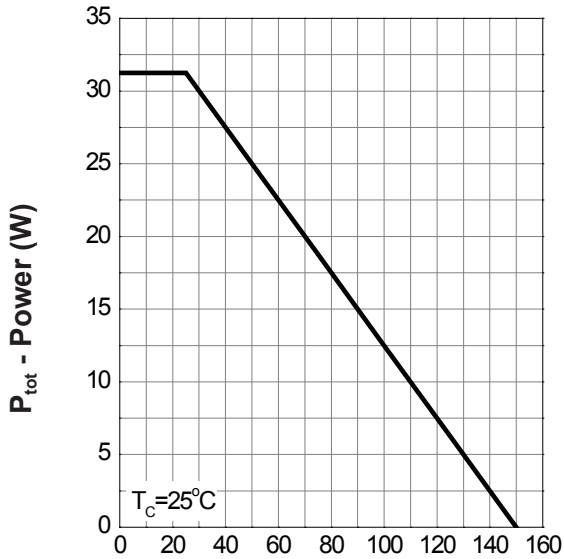
Symbol	Parameter	Test Conditions	Channel 2			Unit
			Min.	Typ.	Max.	
<b>Static Characteristics</b>						
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu A$	30	-	-	V
$I_{DSS}$	Zero Gate Voltage Drain Current	$V_{DS}=24V, V_{GS}=0V$	-	-	500	$\mu A$
		$T_J=85^\circ C$	-	-	5	mA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	1.1	1.5	2	V
$I_{GSS}$	Gate Leakage Current	$V_{GS}=\pm 12V, V_{DS}=0V$	-	-	$\pm 100$	nA
$R_{DS(ON)}^e$	Drain-Source On-state Resistance	$V_{GS}=10V, I_{DS}=25A$	-	1.5	2	m $\Omega$
		$T_J=125^\circ C$	-	2.5	-	
		$V_{GS}=4.5V, I_{DS}=20A$	-	1.9	2.5	
Gfs	Forward Transconductance	$V_{DS}=5V, I_{DS}=20A$	-	74	-	S
<b>Diode Characteristics</b>						
$V_{SD}^e$	Diode Forward Voltage	$I_{SD}=1A, V_{GS}=0V$	-	0.45	0.7	V
		$I_{SD}=20A, V_{GS}=0V$	-	0.8	1.1	
$t_{rr}$	Reverse Recovery Time	$I_{SD}=25A, di_{SD}/dt=100A/\mu s$	-	19.1	-	ns
$t_a$	Charge Time		-	17.4	-	
$t_b$	Discharge Time		-	36.5	-	
$Q_{rr}$	Reverse Recovery Charge		-	22.5	-	
<b>Dynamic Characteristics<sup>f</sup></b>						
$R_G$	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	-	0.9	-	$\Omega$
$C_{iss}$	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=15V,$ Frequency=1.0MHz	-	4130	-	pF
$C_{oss}$	Output Capacitance		-	970	-	
$C_{rss}$	Reverse Transfer Capacitance		-	135	-	
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=15V, R_L=15\Omega,$ $I_{DS}=1A, V_{GEN}=10V,$ $R_G=6\Omega$	-	20.1	-	ns
$t_r$	Turn-on Rise Time		-	10.4	-	
$t_{d(OFF)}$	Turn-off Delay Time		-	71	-	
$t_f$	Turn-off Fall Time		-	41.3	-	
<b>Gate Charge Characteristics<sup>f</sup></b>						
$Q_g$	Total Gate Charge	$V_{DS}=15V, V_{GS}=10V,$ $I_{DS}=25A$	-	57.6	80.7	nC
$Q_g$	Total Gate Charge	$V_{DS}=15V, V_{GS}=4.5V,$ $I_{DS}=25A$	-	25	-	
$Q_{gth}$	Threshold Gate Charge		-	6	-	
$Q_{gs}$	Gate-Source Charge		-	10	-	
$Q_{gd}$	Gate-Drain Charge	-	3.2	-		

Note e : Pulse test ; pulse width $\leq 300\mu s$ , duty cycle $\leq 2\%$ .

Note f : Guaranteed by design, not subject to production testing.

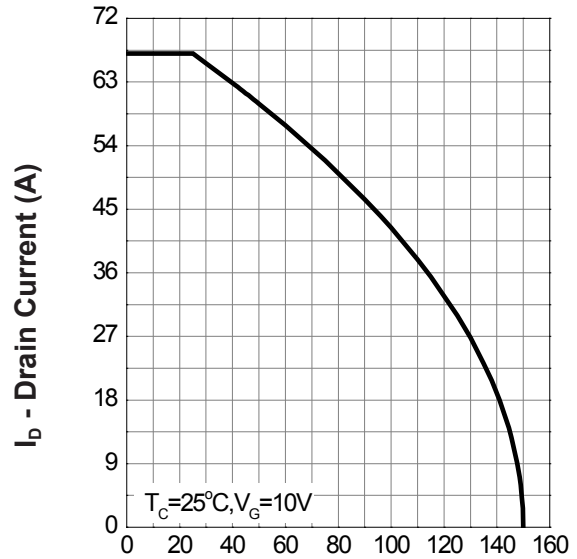
Channel 1 Typical Operating Characteristics

Power Dissipation



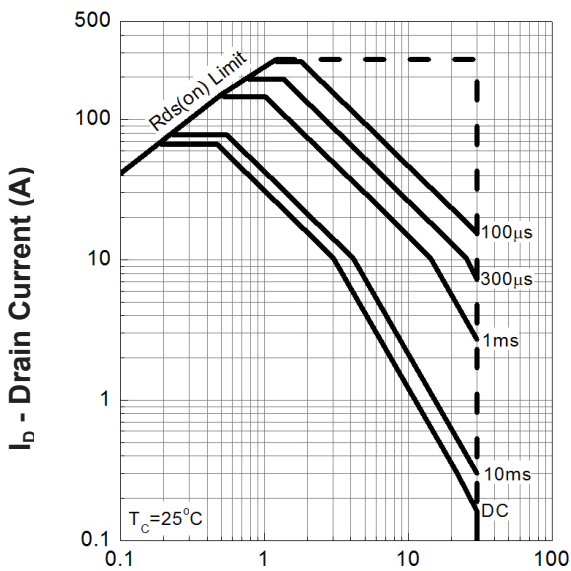
$T_j$  - Junction Temperature ( $^{\circ}\text{C}$ )

Drain Current



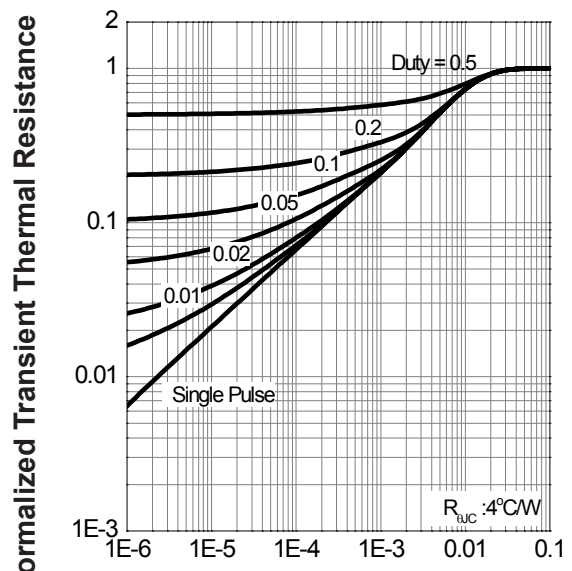
$T_j$  - Junction Temperature ( $^{\circ}\text{C}$ )

Safe Operation Area



$V_{DS}$  - Drain - Source Voltage (V)

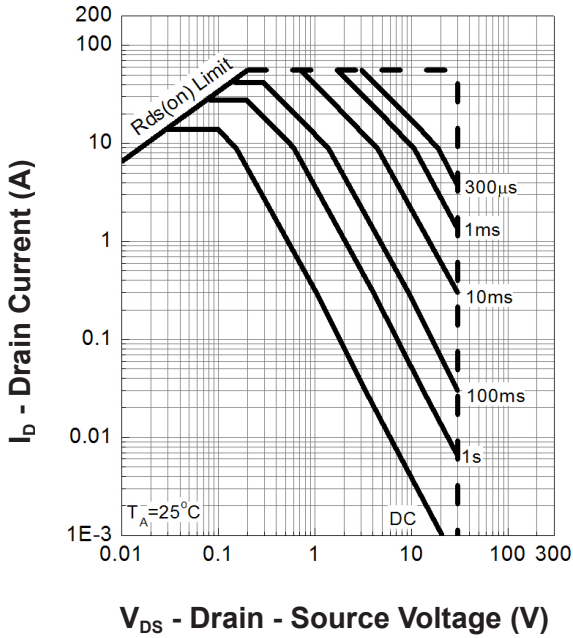
Thermal Transient Impedance



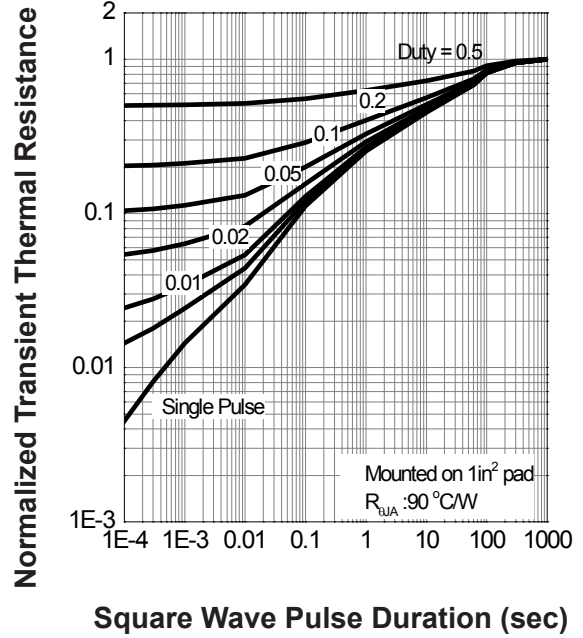
Square Wave Pulse Duration (sec)

Channel 1 Typical Operating Characteristics(Cont.)

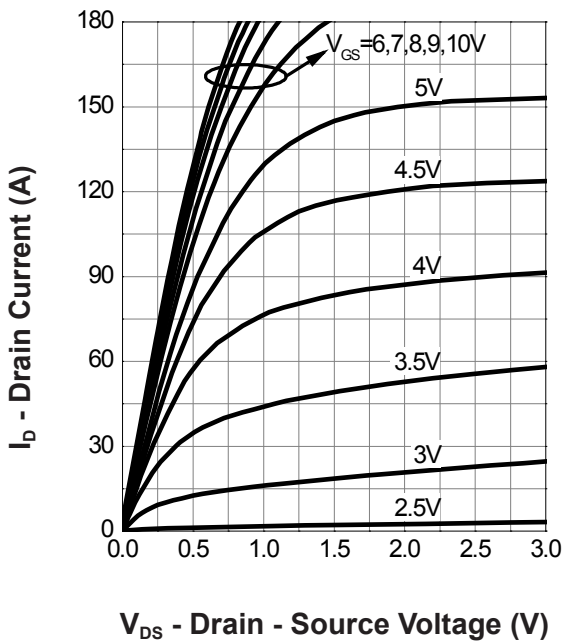
Safe Operation Area



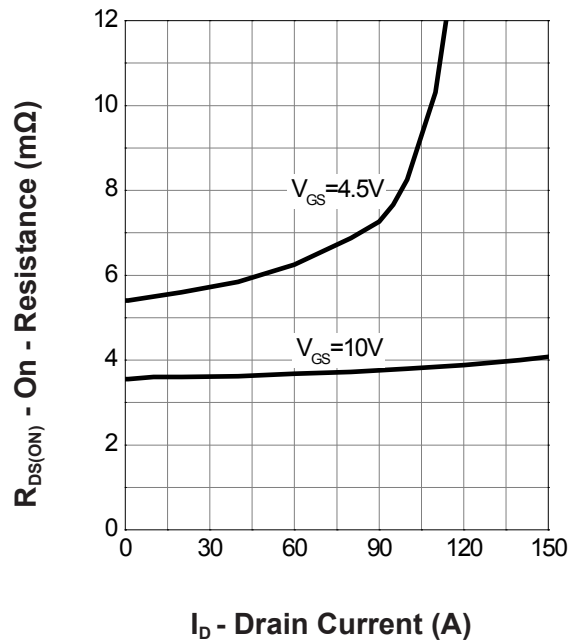
Thermal Transient Impedance



Output Characteristics

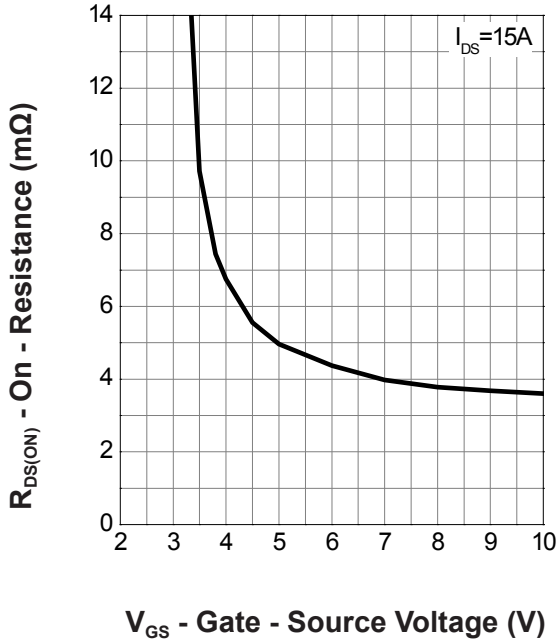


Drain-Source On Resistance

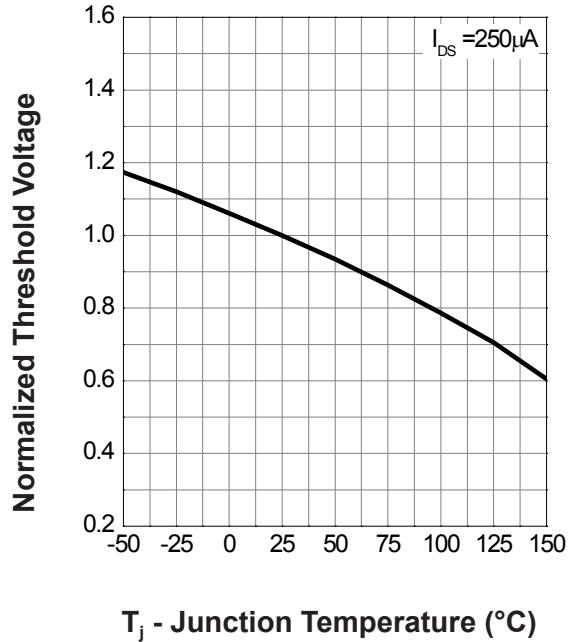


Channel 1 Typical Operating Characteristics(Cont.)

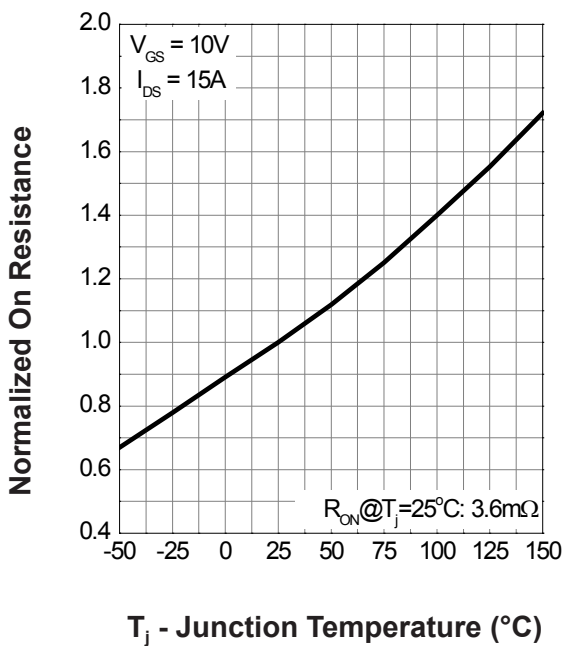
Gate-Source On Resistance



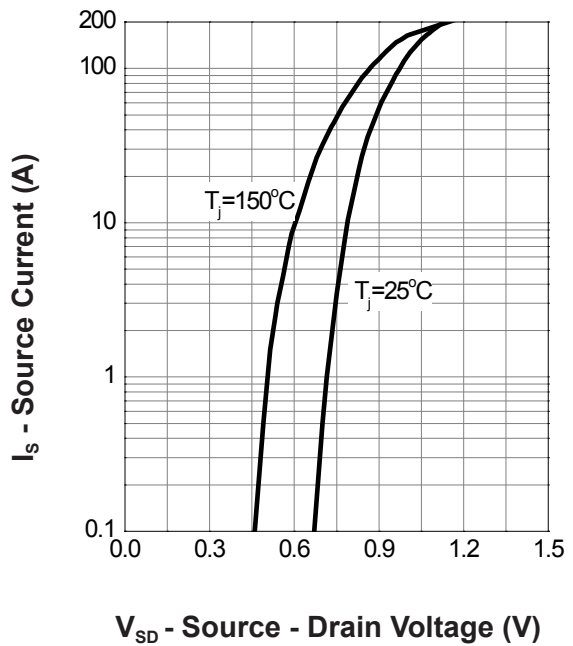
Gate Threshold Voltage



Drain-Source On Resistance

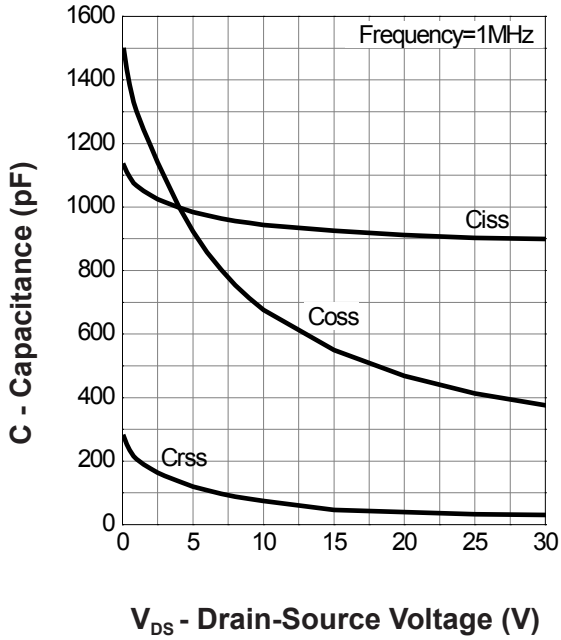


Source-Drain Diode Forward

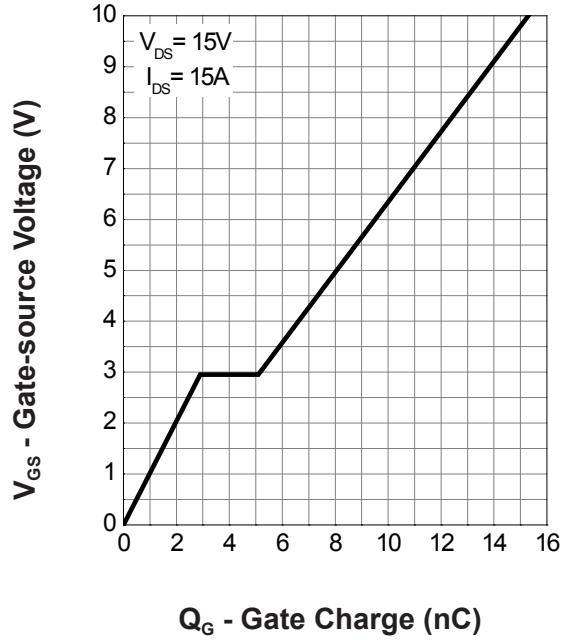


Channel 1 Typical Operating Characteristics(Cont.)

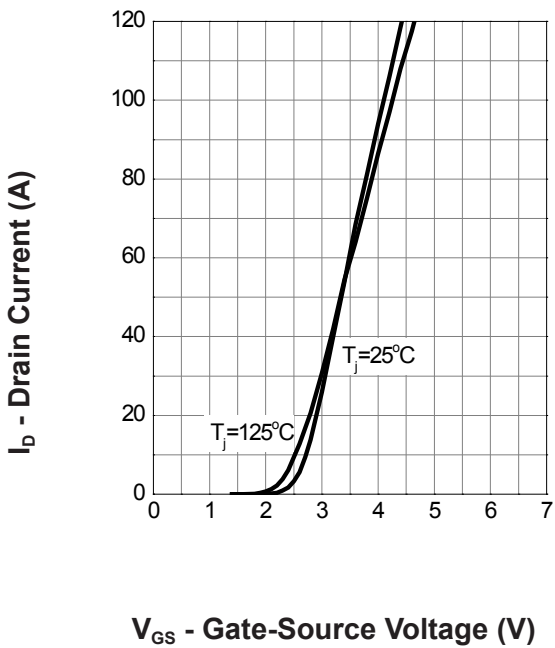
Capacitance



Gate Charge



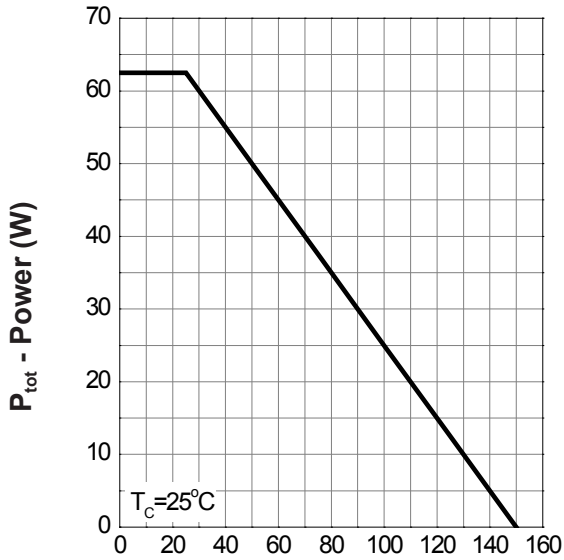
Transfer Characteristics





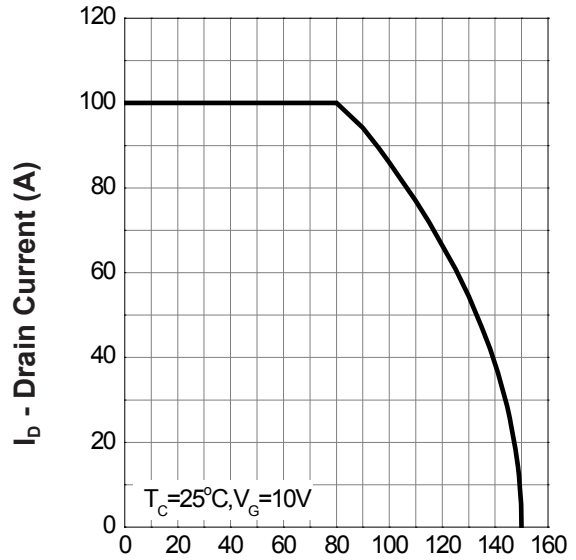
Channel 2 Typical Operating Characteristics

Power Dissipation



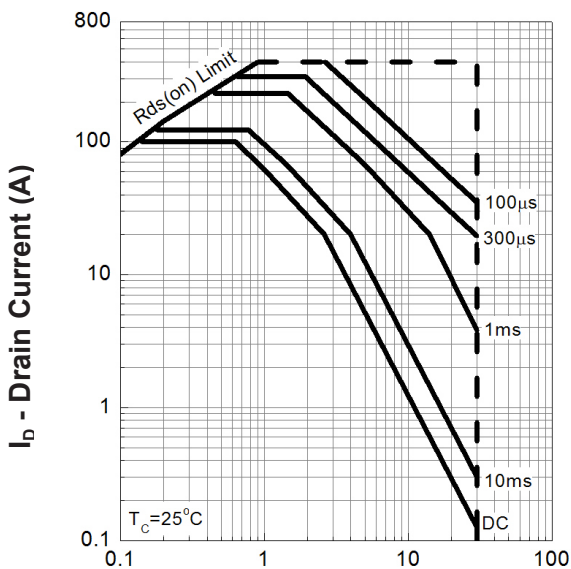
T<sub>j</sub> - Junction Temperature (°C)

Drain Current



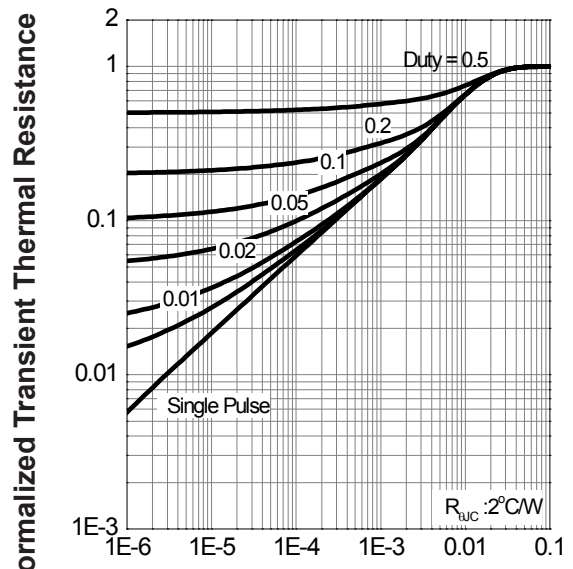
T<sub>j</sub> - Junction Temperature (°C)

Safe Operation Area



V<sub>DS</sub> - Drain - Source Voltage (V)

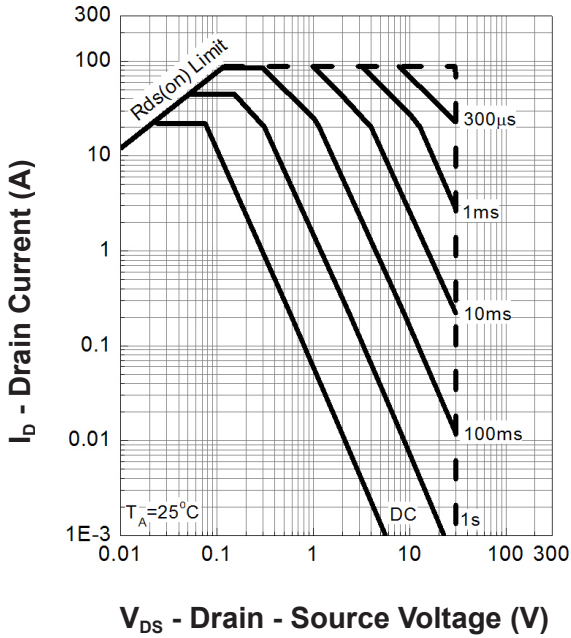
Thermal Transient Impedance



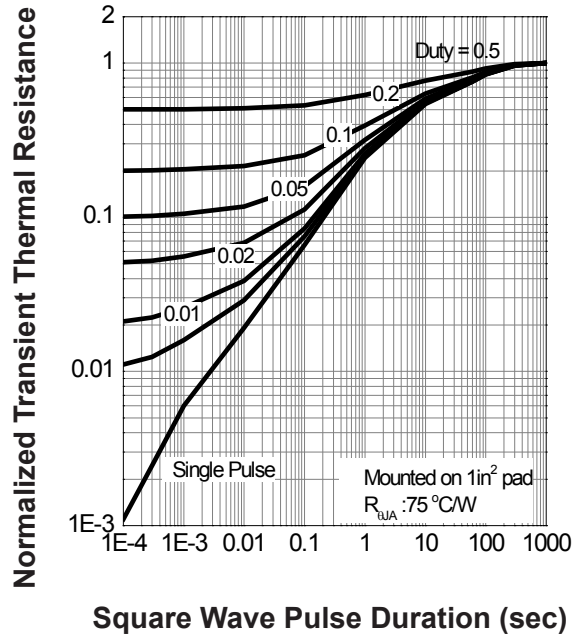
Square Wave Pulse Duration (sec)

Channel 2 Typical Operating Characteristics(Cont.)

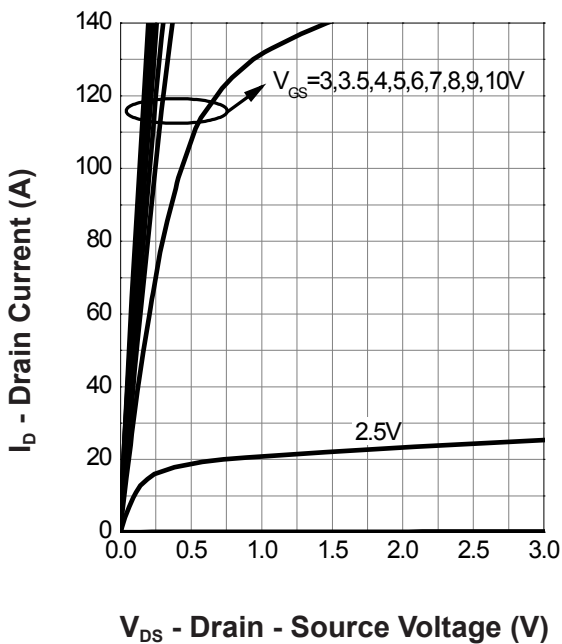
Safe Operation Area



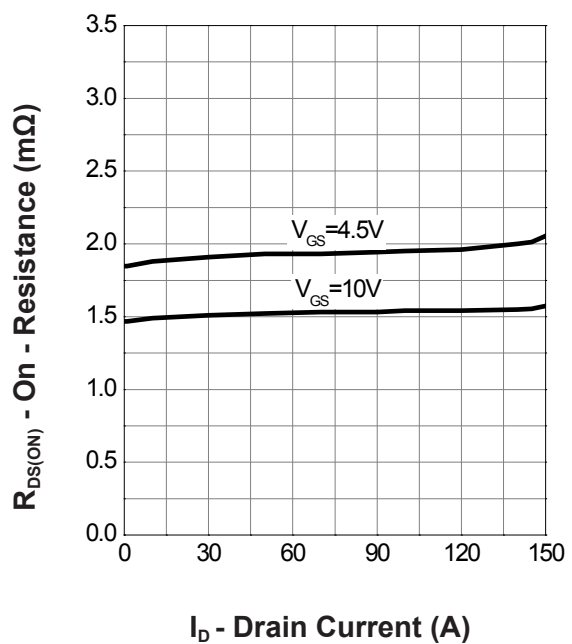
Thermal Transient Impedance



Output Characteristics

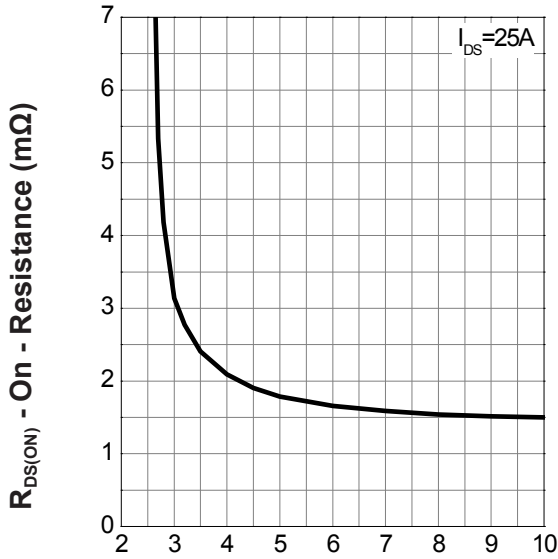


Drain-Source On Resistance



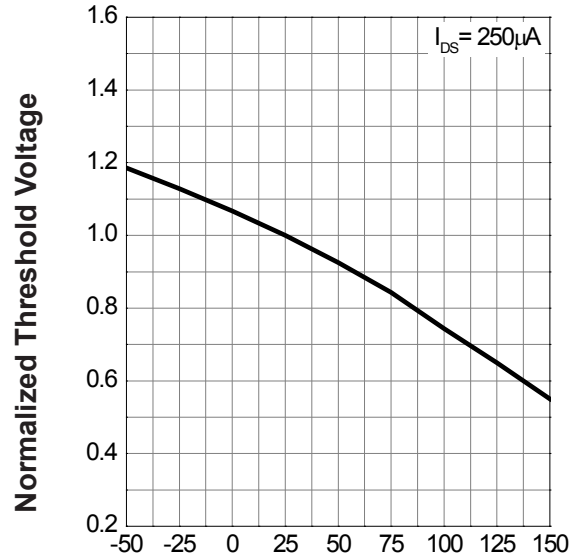
Channel 2 Typical Operating Characteristics(Cont.)

Gate-Source On Resistance



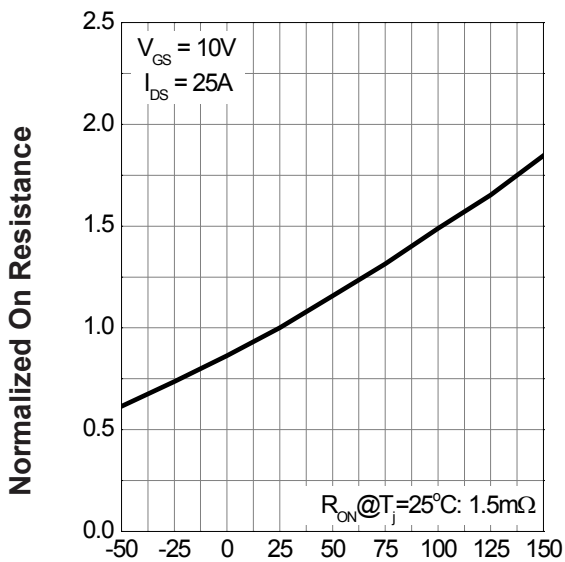
V<sub>GS</sub> - Gate - Source Voltage (V)

Gate Threshold Voltage



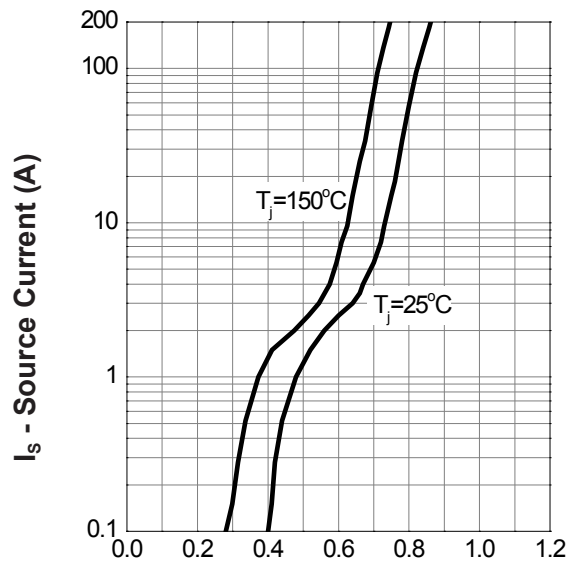
T<sub>J</sub> - Junction Temperature (°C)

Drain-Source On Resistance



T<sub>J</sub> - Junction Temperature (°C)

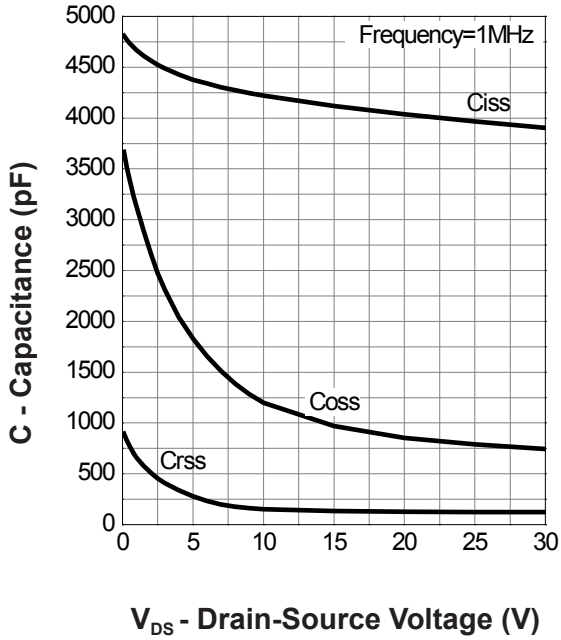
Source-Drain Diode Forward



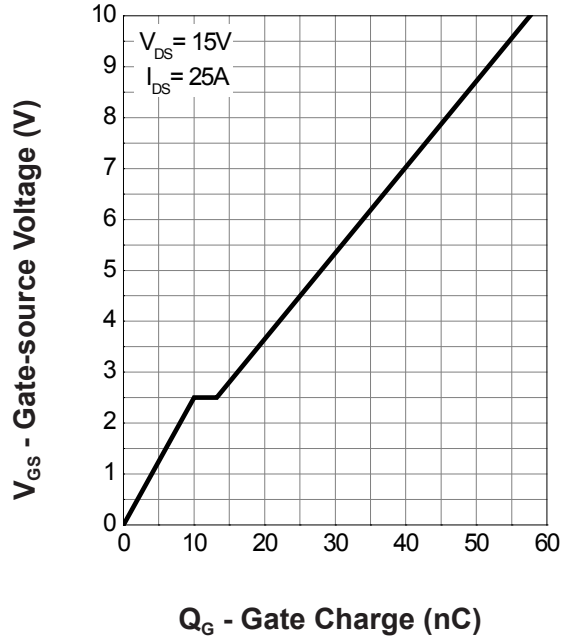
V<sub>SD</sub> - Source - Drain Voltage (V)

Channel 2 Typical Operating Characteristics(Cont.)

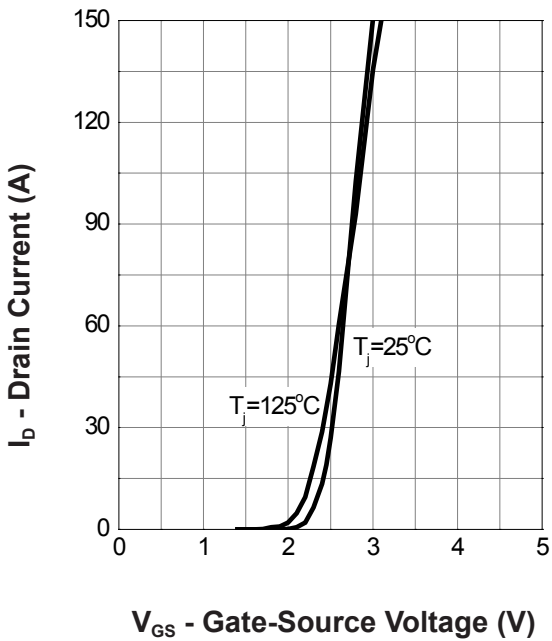
Capacitance



Gate Charge

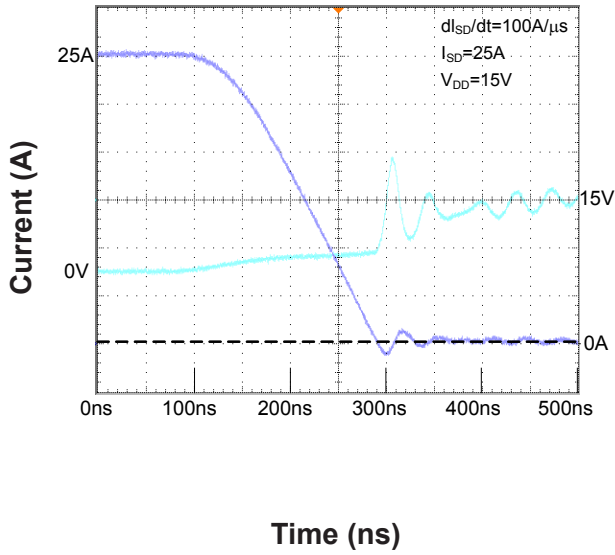


Transfer Characteristics

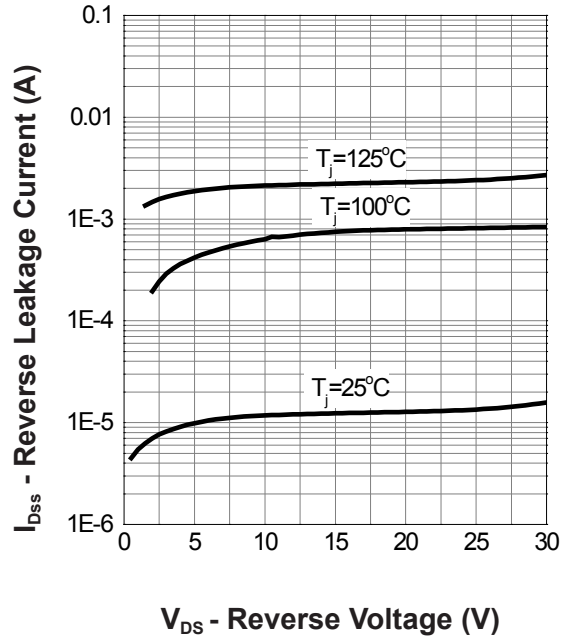


## Channel 2 Schottky Body Diode Characteristics

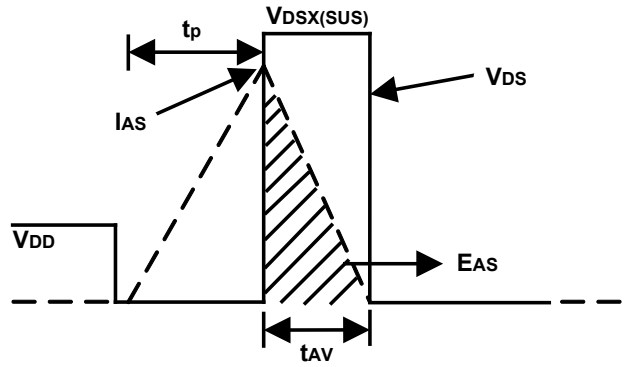
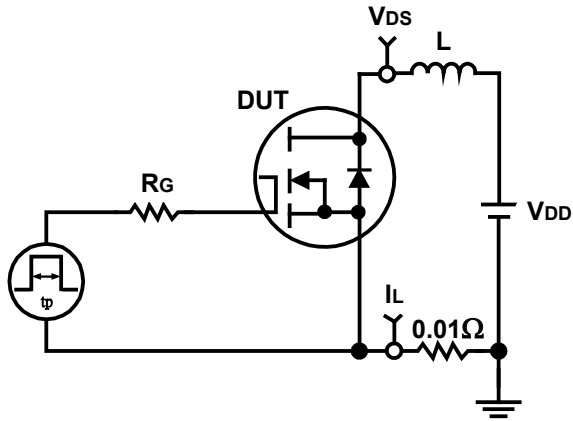
Body Diode Reverse Recovery Diode Characteristics



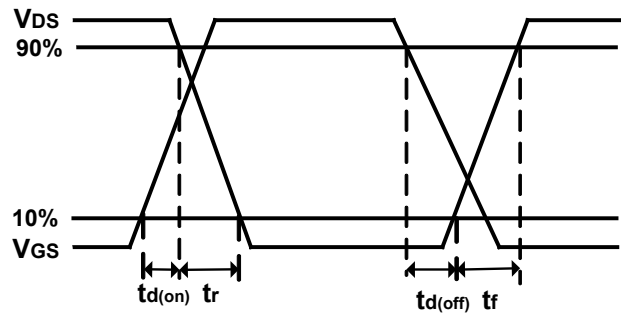
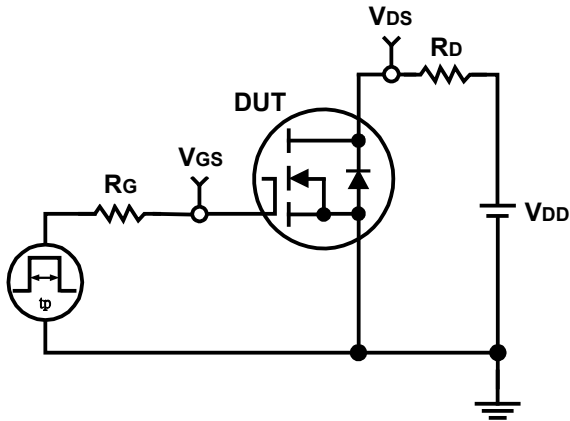
Body Diode Reverse Leakage Current



Avalanche Test Circuit and Waveforms



Switching Time Test Circuit and Waveforms



## Disclaimer

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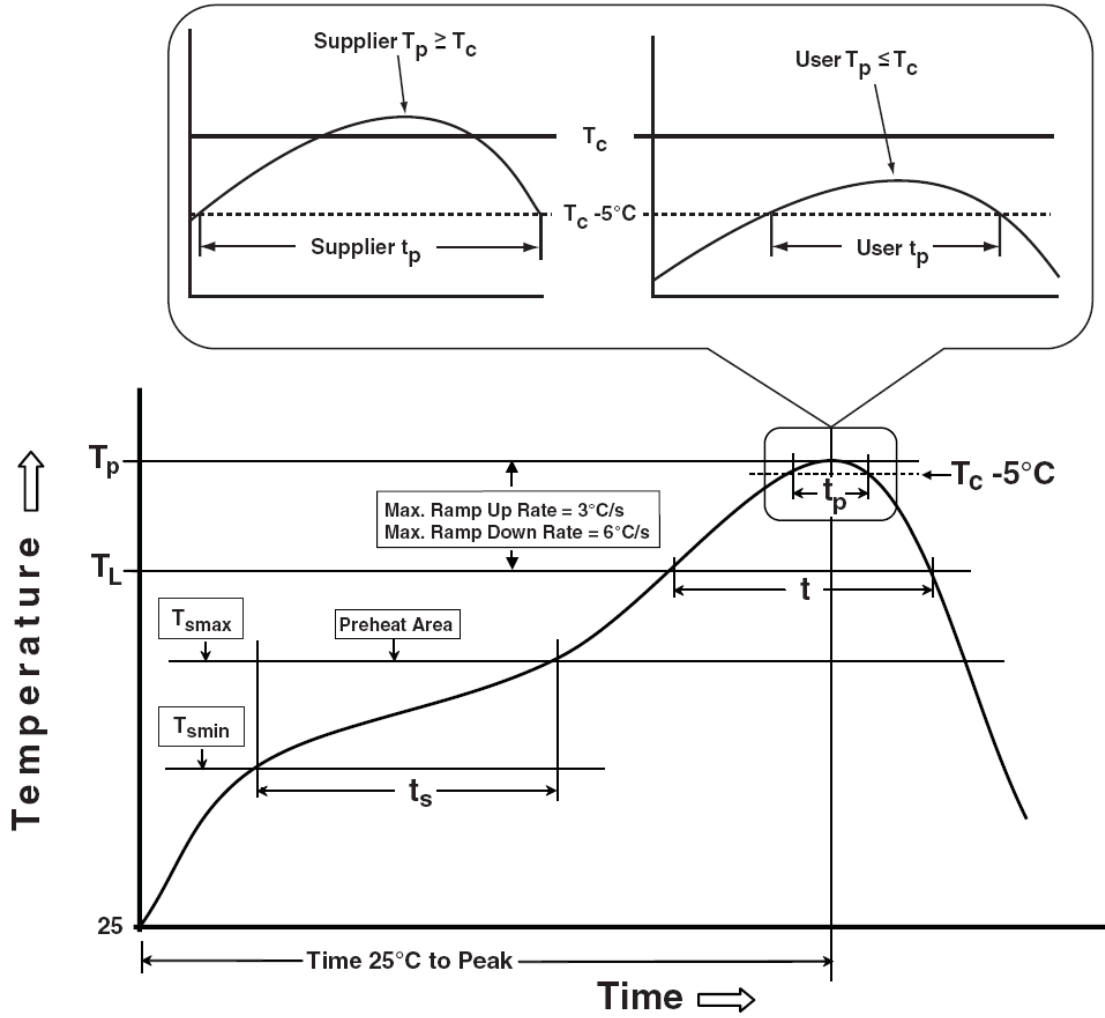
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In order to unify the quality and performance, Sinopower has been following JEDEC while defines assembly rule. Notwithstanding all the suppliers basically follow the rule for each product, different processes may cause slightly different results.

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Classification Profile





## Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
<b>Preheat &amp; Soak</b>		
Temperature min ( $T_{smin}$ )	100 °C	150 °C
Temperature max ( $T_{smax}$ )	150 °C	200 °C
Time ( $T_{smin}$ to $T_{smax}$ ) ( $t_s$ )	60-120 seconds	60-120 seconds
Average ramp-up rate ( $T_{smax}$ to $T_p$ )	3 °C/second max.	3°C/second max.
Liquidous temperature ( $T_L$ )	183 °C	217 °C
Time at liquidous ( $t_L$ )	60-150 seconds	60-150 seconds
Peak package body Temperature ( $T_p$ )*	See Classification Temp in table 1	See Classification Temp in table 2
Time ( $t_p$ )** within 5°C of the specified classification temperature ( $T_c$ )	20** seconds	30** seconds
Average ramp-down rate ( $T_p$ to $T_{smax}$ )	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature ( $T_p$ ) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature ( $t_p$ ) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures ( $T_c$ )

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures ( $T_c$ )

Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

## Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ $T_{jmax}$
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ $T_{jmax}$
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

## Customer Service

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