

N-Channel Enhancement Mode MOSFET

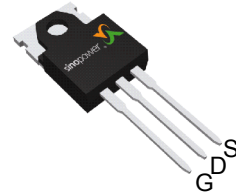
Features

- 75V/80A,
 $R_{DS(ON)} = 11m\Omega$ (max.) @ $V_{GS} = 10V$
- Avalanche Rated
- Reliable and Rugged
- Lead Free and Green Devices Available
 (RoHS Compliant)

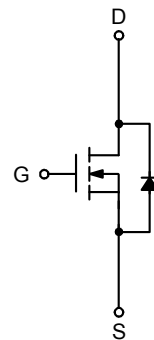
Applications

- Power Management for Inverter Systems.

Pin Description




Top View of TO-220H



N-Channel MOSFET

Ordering and Marking Information

<p>SM7501NS □□□-□□□</p> <div style="margin-left: 20px;"> <p>└─ Assembly Material</p> <p>└─ Handling Code</p> <p>└─ Temperature Range</p> <p>└─ Package Code</p> </div>	<p>Package Code FH : TO-220H</p> <p>Operating Junction Temperature Range C : -55 to 175 °C</p> <p>Handling Code TU : Tube (50ea/tube)</p> <p>Assembly Material G : Halogen and Lead Free Device</p>
<p>SM7501NS FH : </p>	<p>XXXXX - Lot Code</p>

Note: SINOPOWER lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish; which are fully compliant with RoHS. SINOPOWER lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020D for MSL classification at lead-free peak reflow temperature. SINOPOWER defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

SINOPOWER reserves the right to make changes to improve reliability or manufacturability without notice, and advise customers to obtain the latest version of relevant information to verify before placing orders.

Absolute Maximum Ratings (T_A = 25°C Unless Otherwise Noted)

Symbol	Parameter	Rating	Unit	
Common Ratings				
V _{DSS}	Drain-Source Voltage	75	V	
V _{GSS}	Gate-Source Voltage	±25		
T _J	Maximum Junction Temperature	175	°C	
T _{STG}	Storage Temperature Range	-55 to 175	°C	
I _S	Diode Continuous Forward Current	T _C =25°C	80	A
Mounted on Large Heat Sink				
I _{DP}	300µs Pulse Drain Current Tested	T _C =25°C	300*	A
I _D	Continuous Drain Current	T _C =25°C	80	A
		T _C =100°C	67	
P _D	Maximum Power Dissipation	T _C =25°C	176	W
		T _C =100°C	88	
R _{θJC}	Thermal Resistance-Junction to Case	0.85	°C/W	
Drain-Source Avalanche Ratings				
E _{AS}	Avalanche Energy, Single Pulsed	L=2mH	1	J

Note * : Pulse width limited by safe operating area.

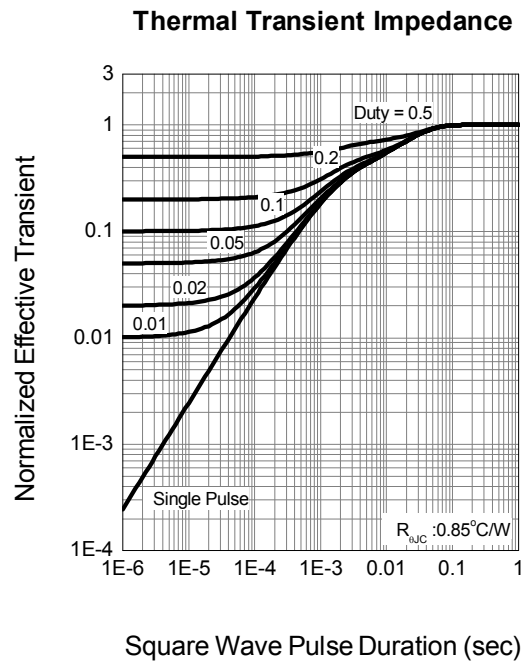
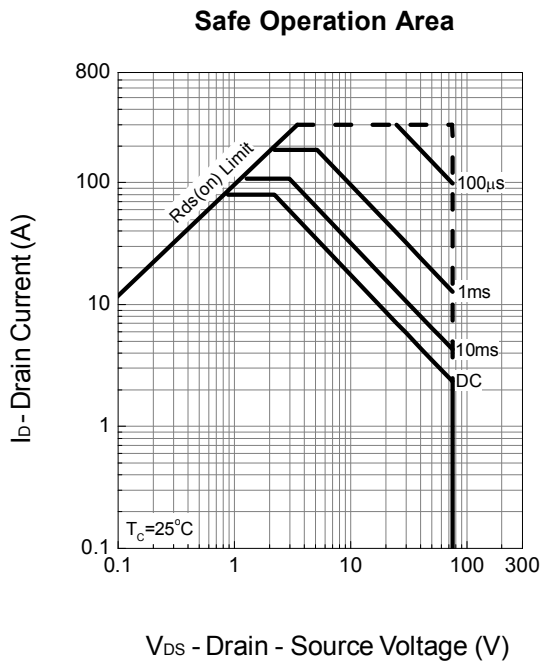
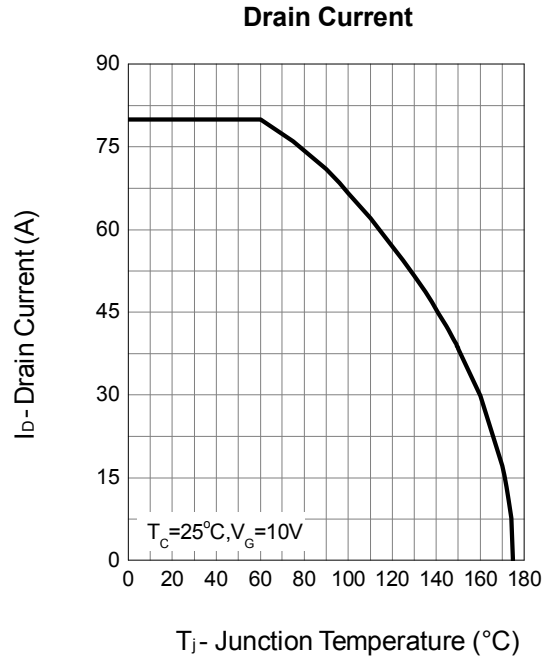
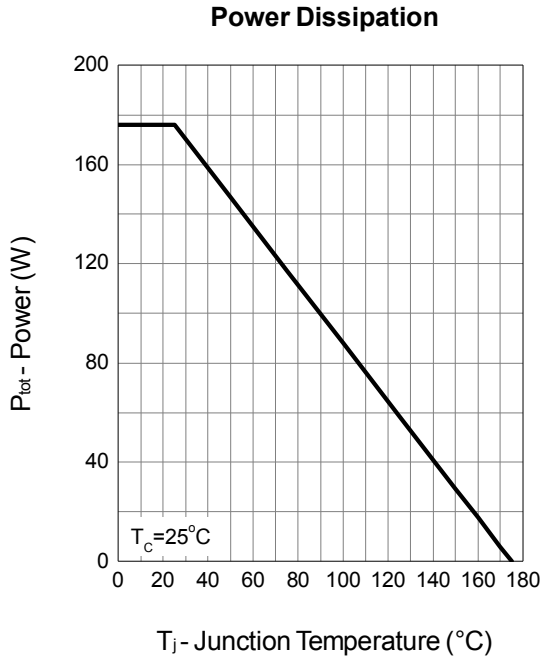
Electrical Characteristics ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
Static Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_{DS}=250\mu A$	75	-	-	V
$\Delta BV_{DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	$V_{GS}=0V, I_{DS}=250\mu A$	-	0.07	-	$V/^\circ\text{C}$
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=60V, V_{GS}=0V$	-	-	1	μA
		$T_J=85^\circ\text{C}$	-	-	30	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_{DS}=250\mu A$	2	3	4	V
I_{GSS}	Gate Leakage Current	$V_{GS}=\pm 25V, V_{DS}=0V$	-	-	± 100	nA
$R_{DS(ON)}^a$	Drain-Source On-state Resistance	$V_{GS}=10V, I_{DS}=40A$	-	9	11	m Ω
Diode Characteristics						
V_{SD}^a	Diode Forward Voltage	$I_{SD}=20A, V_{GS}=0V$	-	0.8	1.1	V
t_{rr}	Reverse Recovery Time	$I_{DS}=40A,$ $di_{SD}/dt=100A/\mu s$	-	55	-	ns
Q_{rr}	Reverse Recovery Charge		-	117	-	nC
Dynamic Characteristics^b						
R_G	Gate Resistance	$V_{GS}=0V, V_{DS}=0V, F=1$ MHz	-	1.5	-	Ω
C_{iss}	Input Capacitance	$V_{GS}=0V,$ $V_{DS}=30V,$ Frequency=1.0MHz	-	3450	-	pF
C_{oss}	Output Capacitance		-	470	-	
C_{riss}	Reverse Transfer Capacitance		-	190	-	
$t_{d(ON)}$	Turn-on Delay Time	$V_{DD}=30V, R_L=30\Omega,$ $I_{DS}=1A, V_{GEN}=10V,$ $R_G=6\Omega$	-	23	42	ns
T_r	Turn-on Rise Time		-	12	23	
$t_{d(OFF)}$	Turn-off Delay Time		-	77	140	
T_f	Turn-off Fall Time		-	69	125	
Gate Charge Characteristics^b						
Q_g	Total Gate Charge	$V_{DS}=64V, V_{GS}=10V,$ $I_{DS}=80A$	-	80	112	nC
Q_{gs}	Gate-Source Charge		-	19	-	
Q_{gd}	Gate-Drain Charge		-	27	-	

Note a : Pulse test ; pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

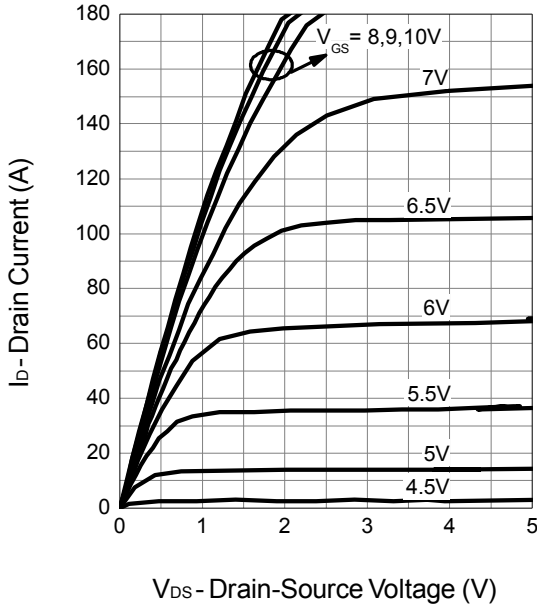
Note b : Guaranteed by design, not subject to production testing.

Typical Operating Characteristics

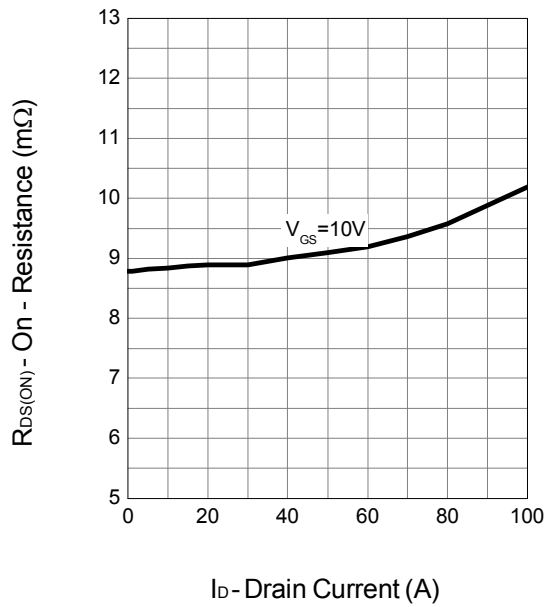


Typical Operating Characteristics (Cont.)

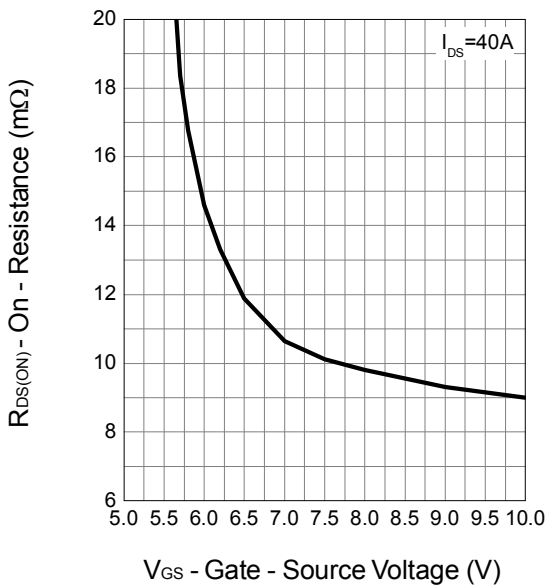
Output Characteristics



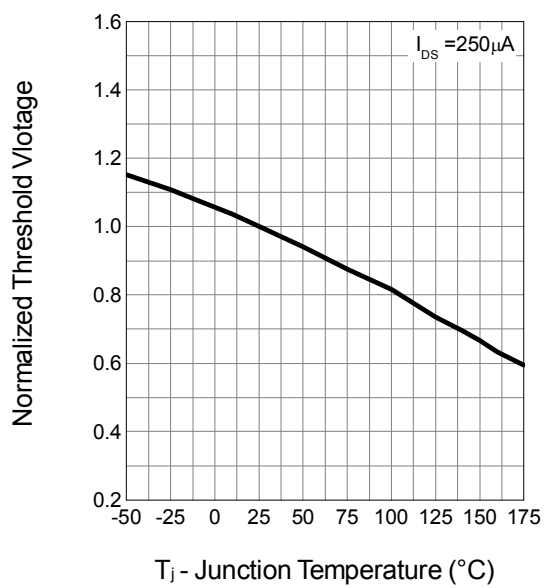
Drain-Source On Resistance



Gate-Source On Resistance

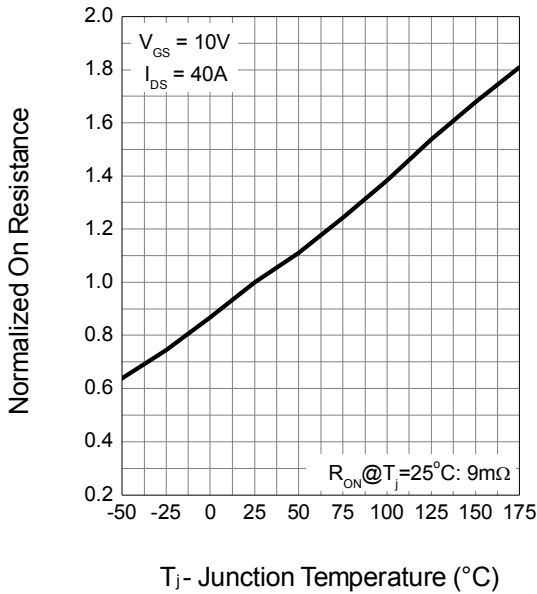


Gate Threshold Voltage

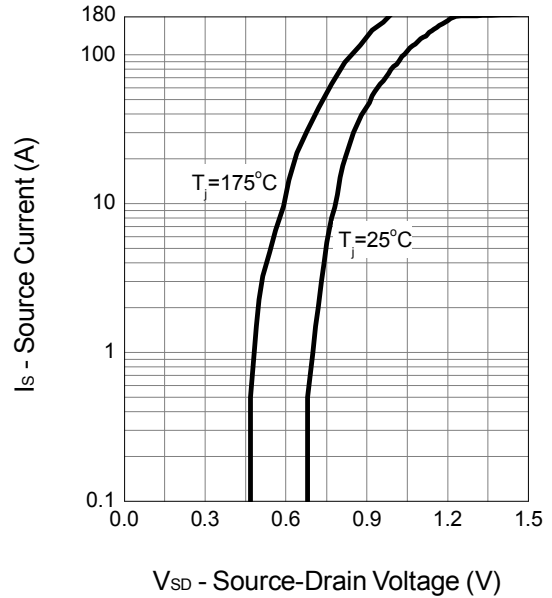


Typical Operating Characteristics (Cont.)

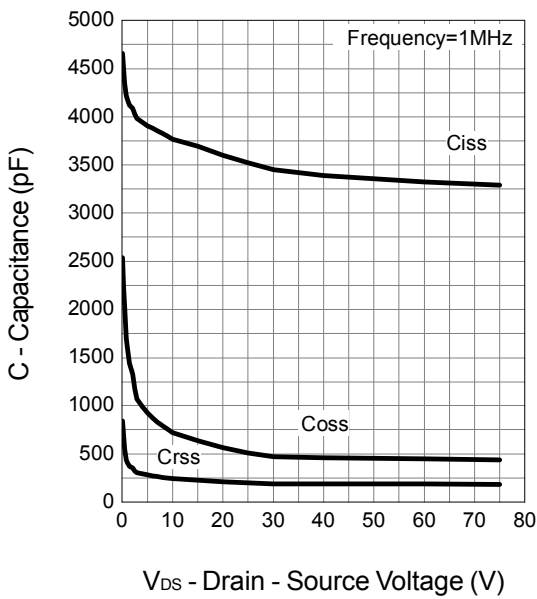
Drain-Source On Resistance



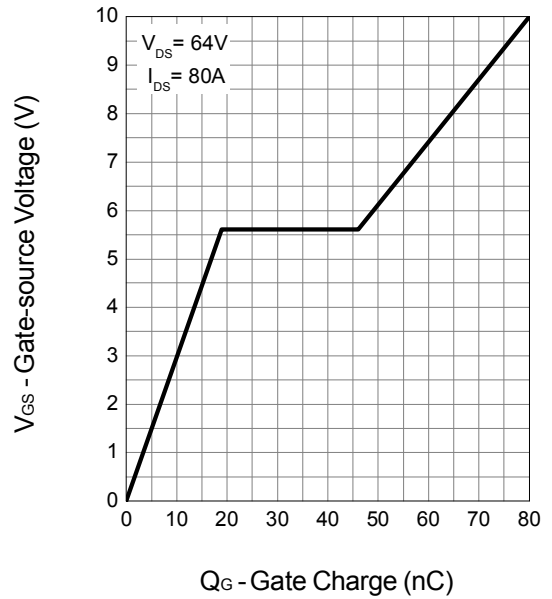
Source-Drain Diode Forward



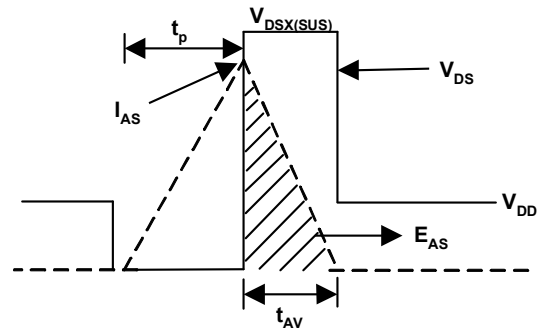
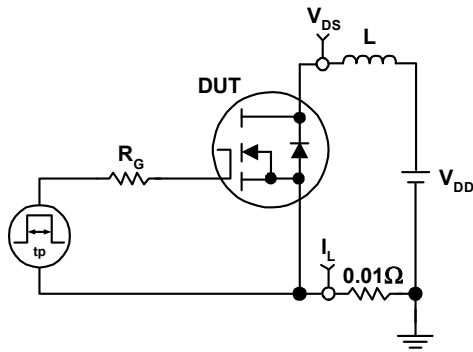
Capacitance



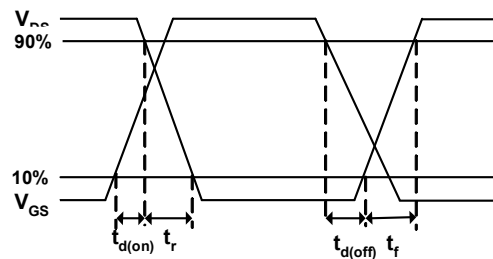
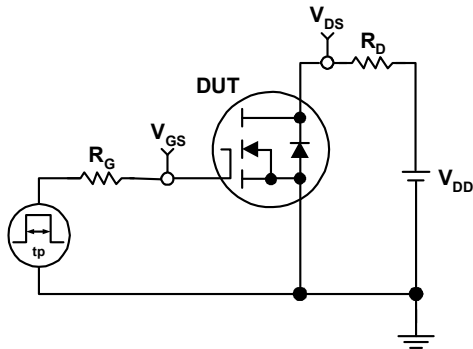
Gate Charge



Avalanche Test Circuit and Waveforms

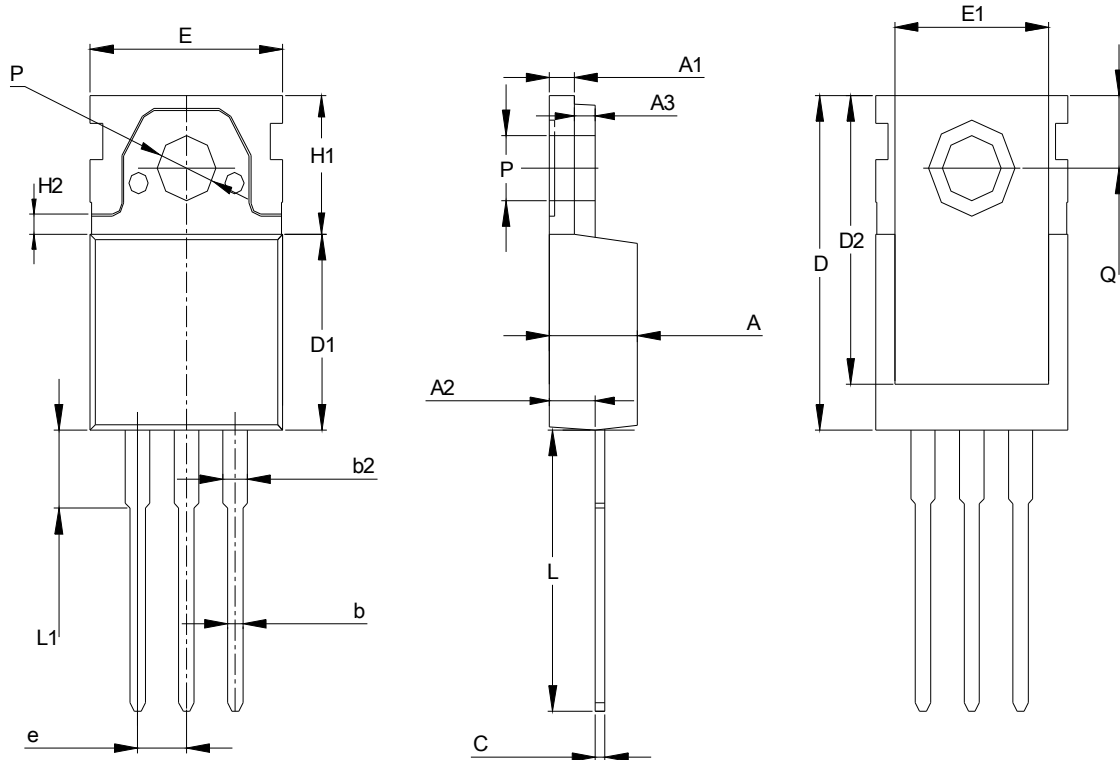


Avalanche Test Circuit and Waveforms



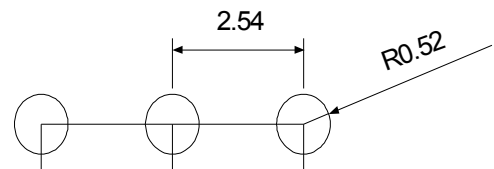
Package Information

TO-220H



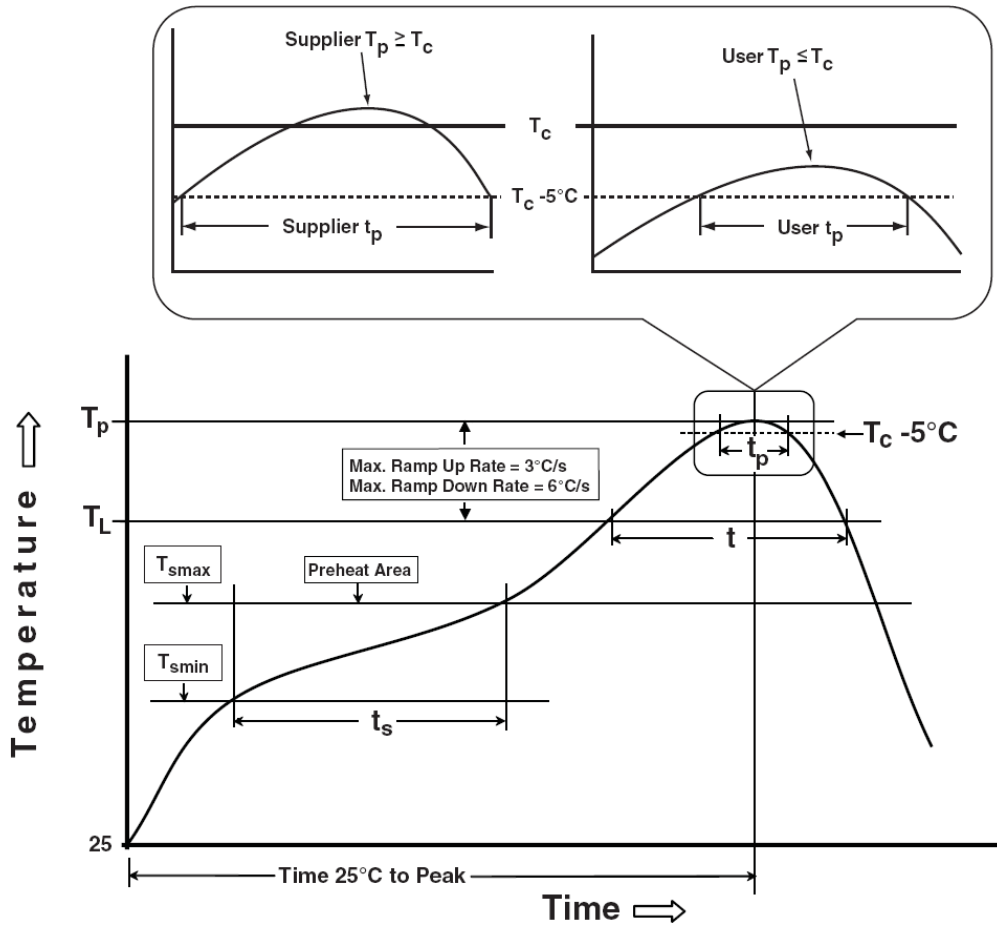
SYMBOL	TO-220H			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	3.56	4.83	0.140	0.190
A1	0.51	1.50	0.020	0.059
A2	2.03	2.92	0.080	0.115
A3	1.05	1.15	0.041	0.045
b	0.38	1.02	0.015	0.040
b2	1.14	1.78	0.045	0.070
c	0.36	0.61	0.014	0.024
D	14.22	16.51	0.560	0.650
D1	8.38	9.30	0.330	0.366
D2	12.19	13.65	0.480	0.537
E	9.65	10.67	0.380	0.420
E1	6.86	8.89	0.270	0.350
e	2.54 BSC		0.100 BSC	
H1	5.84	6.86	0.230	0.270
H2	0.95	1.05	0.037	0.041
L	12.70	14.73	0.500	0.580
L1	-	6.35	-	0.250
P	3.00	3.20	0.118	0.126
Q	3.30	3.50	0.130	0.138

RECOMMENDED LAND PATTERN



UNIT: mm

Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_p)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_p)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_p)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_p to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
* Tolerance for peak profile Temperature (T_p) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature (t_p) is defined as a supplier minimum and a user maximum.		

Table 1. SnPb Eutectic Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2. Pb-free Process – Classification Temperatures (T_c)

Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	1000 Hrs, 80% of VDS max @ T_{jmax}
HTGB	JESD-22, A108	1000 Hrs, 100% of VGS max @ T_{jmax}
PCT	JESD-22, A102	168 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	500 Cycles, -65°C~150°C

Customer Service

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