

SM7512

**3W STEREO AUDIO POWER AMPLIFIER WITH ADVANCED DC VOLUME CONTROL** 

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www.DataSheet.in



#### **GENERAL DESCRIPTION**

The SM7512 is a stereo audio power amplifier that drives 3 W/channel of continuous RMS power into a 3- $\Omega$  load. Advanced dc volume control minimizes external components and allows BTL (speaker) volume control and SE (headphone) volume control. Notebook and pocket PCs benefit from the integrated feature set that minimizes external components without sacrificing functionality.

To simplify design, the speaker volume level is adjusted by applying a dc voltage to the VOLUME terminal.

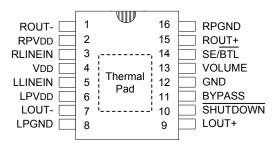
### FEATURES

- \* Advanced DC Volume Control With 2-dB Steps From -40 dB to 20 dB
- Maximum Volume Setting for SE Mode
- Adjustable SE Volume control
  - Referenced to BTL Volume Control
- \* 3 W Into 3-Ω Speakers

#### APPLICATIONS

- \* Notebook PC
- \* LCD Monitors
- \* Portable DVD Player
- \* Digital photo frame

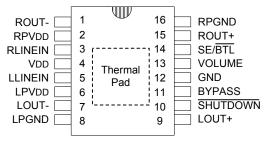
#### **PIN ASSIGNMENTS (TOP VIEW)**







#### **PIN ASSIGNMENTS**





#### **PIN DESCRIPTIONS**

No.	Pin Name	1/0	Function	
1	ROUT-	0	Right channel negative audio output	
2	RPVDD	-	Supply voltage terminal for power stage	
3	RLINEIN	I	Right channel line input	
4	Vdd	-	Supply voltage terminal	
5	LLINEIN	I	Left channel line input	
6	LPVDD	-	Supply voltage terminal for power stage	
7	LOUT-	0	Left channel negative audio output	
8	LPGND	-	Power ground	
9	LOUT+	0	Left chanel positive audio output	
10	SHUTDOWN	I	Places the amplifier in shutdown mode if a TTL logic low is placed on this terminal	
11	BYPASS	I	Tap to voltage divider for internal midsupply bias generator used for analog reference	
12	GND	-	Analog power supply ground	
13	VOLUME	I	Terminal for dc volume control.DC voltage range is 0 to VDD	
14	SE/BTL	I	Output MUX control. When this terminal is high,SE outputs are selected When this terminal is low, BTL outputs are selected	
15	ROUT+	0	Right channel positive audio output	
16	RPGND	-	Power ground	



## MAXIMUM RATINGS (Ta = 40~85°C)

Characteristic	Symbol	Rating	Unit
Supply Voltage, VDD, PVDD	Vss	-0.3 ~ 6	V
Input Voltage	VI	- 0.3 ~ Vdd +0.3	V
Operating free-air temperature range	ТА	- 40 ~ 85	-
Operaing junction temperature range	TJ	- 40 ~ 150	°C
Storage temperatuer range	Tstg	- 65 ~ 150	°C
Continous total power dissipation	-	See Dissipation Rating Table	°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	-	260	°C

### **DISSIPATION RATING TABLE**

PACKAGE	Ta≤25°C	Derating Factor	TA=70°C	Ta=85°C
	Power Rating	Above TA=25°C	Power Rating	Power Rating
SOP /SSOP	2.7mW	21.8 mW/°C	1.7W	1.4W

## **RECOMMENDED OPERATING CONDITION**

Characteristic	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply Voltage, VDD, PVDD	Vss		4.0	-	5.5	V
High-level input voltage	Vін	SE/BTL	0.8 Vdd	-	-	V
nigh-level liput voltage	Vін	SHUTDOWN	2.0	-	-	v
Low-level input voltage	VIL	SE/BTL	-	-	0.3 Vdd	V
	VI∟	SHUTDOWN	I	-	0.6	v
Operating free-air temperature	TA		-40	-	85	°C



# ELECTRICAL CHARACTERISTICS (VDD =PVDD = 5.5V, Ta = 25°C unless otherwise noted)

Characteristic	Symbol	Condition	Min.	Тур.	Max.	Unit
Output offset voltage (measured differentially)	Voo	VDD=5.5V,Gain=20dB, SE/BTL=0V	-	-	60	mV
Power supply rejection ratio	PSRR	VDD=PVDD=4.0V to 5.5V	-42	-70	-	dB
High-level input current (SE/BTL,SHUTDOWN,VOLUME)	і Іін і	Vdd=PVdd=5.5V, Vi=Vdd=PVdd	-	-	1	uA
Low-level input current(SE/BTL,SHUTDOWN,VOLUME)	IIL	VDD=PVDD=5.5V , VI=0V	-	-	1	uA
Supply current, no load	מס	VDD=PVDD=5.5V,SE/BTL=0V, SHUTDOWN=2V	5.5	8.0	10	mA
Supply current, no load	שטו	VDD=PVDD=5.5V,SE/BTL=5.5V, SHUTDOWN=2V	3.0	5.0	6.0	IIIA
Supply current, max power into a $3\Omega$ load	ססו	VDD=5V=PVDD,SE/BTL=0V, SHUTDOWN=2V,RL=3Ω, Po=2W,Stereo	-	1.5	-	Arms
Supply current, shutdown mode	IDD(SD)	SHUTDOWN=0.0V	-	1	20	uA

#### OPERATING CHARACTERISTICS (VDD =PVDD = 5V, $R_L$ = 3 $\Omega$ , Gain = 6dB, Ta = 25°C unless otherwise noted)

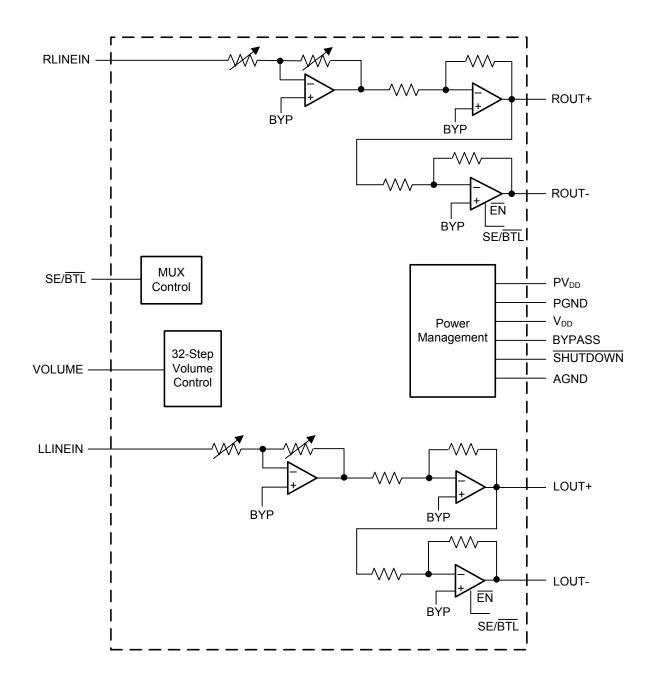
Characteristic	Symbol	Condition	Condition		Min.	Тур.	Max.	Unit
		f = 1KHz, R∟ =4Ω,	THD+	N=1%	-	1.9	-	
		BTL Mode , Gain =20 dB		<b>\</b> =10%	-	2.3	-	
Output Power <sup>(1)</sup>		THD+	N=1%	-	1.4	-	w	
	BTL Mode , Gain =20 dB T		THD+N	<b>l=</b> 10%	-	1.7	-	
		V <sub>DD</sub> =5.5V, f = 1KHz, THI		10%	-	3	-	
Total harmonic distortion+noise	THD+N	Po =1W, RL=8Ω, f=20Hz to 20kHz		-	< 0.6	-	%	
High-level output voltage	Vон	RL=8Ω, Measured between output and VDD		-	-	600	mV	
Low-level output voltage	Vol	RL=8Ω, Measured between output and GND		-	-	400	mV	
Bypass voltage(Nominally VDD/2) <sup>(2)</sup>	V(BYPASS)	Measured at pin 11, No load, VDD=5.5V		2.65	2.75	2.85	V	
Noise output voltage		f= 20 Hz to 20 kHz, Gain=0 dB, C(BYP)= 1.0	uF	BTL	-	85	-	µVrмs

(1) Output power is measured at the output terminals of the IC.

(2) At 4V < V\_{DD} < 5.5V the DC bypass voltage is approximately V\_DD/2



#### FUNCTIONAL BLOCK DIAGRAM





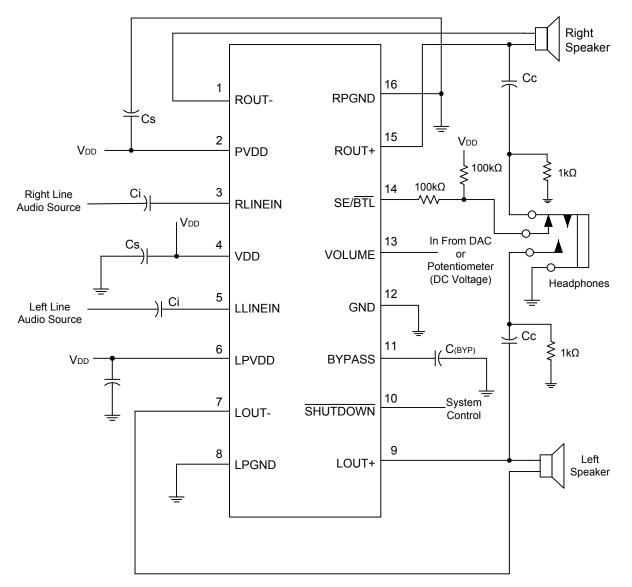
VOL	JME	GAIN OF AMPLIFIER
FROM (V)	TO (V)	(Тур.)
0.00	0.23	-85
0.31	0.34	-40
0.42	0.46	-38
0.54	0.56	-36
0.65	0.67	-34
0.76	0.79	-32
0.87	0.90	-30
0.98	1.01	-28
1.10	1.12	-26
1.21	1.24	-24
1.32	1.35	-22
1.43	1.46	-20
1.54	1.57	-18
1.66	1.68	-16
1.77	1.79	-14
1.88	1.91	-12
1.99	2.02	-10
2.10	2.13	-8
2.21	2.24	-6
2.33	2.35	-4
2.44	2.47	-2
2.55	2.57	0
2.67	2.70	2
2.77	2.80	4
2.89	2.92	6
3.00	3.03	8
3.11	3.14	10
3.22	3.26	12
3.33	3.37	14
3.44	3.48	16
3.55	3.60	18
3.67	5.00	20

# Table 1. DC Volume Control (BTL Mode, VDD=5V)



#### **APPLICATION INFORMATION**

The figure is schematic diagrams of typical notebook computer application circuits.

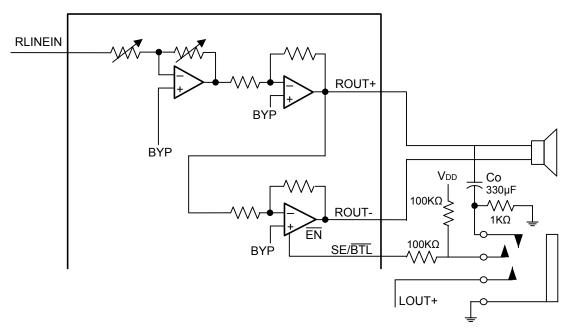






# SE/BTL OPERATION

The ability of the SM7512 to easily switch between BTL and SE modes is one of its most important cost saving features. This feature eliminates the requirement for an additional headphone amplifier in applications where internal stereo speakers are driven in BTL mode but external headphone or speakers must be accommodated. Internal to the SM7512, two separate amplifiers drive OUT+ and OUT-. The SE/BTL input controls the operation of the follower amplifier that drives LOUT- and ROUT-. When SE/BTL is held low, the amplifier is on and the SM7512 is in the BTL mode. When SE/BTL is held high, the OUT- amplifiers are in a high output impedance state, which configures the SM7512 as an SE driver from LOUT+ and ROUT+. IDD is reduced by approximately one-third in SE mode. Control of the SE/BTL input can be from a logic-level CMOS source or, more typically, from a resistor divider network as shown in Figure 2. The trip level for the SE/BTL input can be found in the *recommended operating condition* table.



#### **APPLICATION INFORMATION (continued)**



Using a 1/8-in. (3,5mm) stereo headphone jack, the control switch is closed when no plug is inserted. When closed the  $100K\Omega/1K\Omega$  divider pulls the SE/BTL input low. When a plug is inserted, the  $1K\Omega$  resistor is disconnected and SE/BTL input is pulled high. When the input goes high, the OUT- amplifier is shut down causing the speaker to mute (open-circuits the speaker). The OUT+ amplifier then drives through the output capacitor (Co) into the headphone jack.



#### SHUTDOWN MODES

The SM7512 employs a shutdown mode of operation designed to reduce supply current (IDD) to the absolute minimum level during periods of nonuse for battery-power conservation. The SHUTDOWN input terminal should be held high during normal operation when the amplifier is in use. Pulling SHUTDOWN low causes the outputs to mute and the amplifier to enter a low-current state, IDD=  $20\mu$ A. SHUTDOWN should never be left unconnected because amplifier operation would be unpredictable.

#### **VOLUME OPERATION**

The VOLUME controls the BTL volume when driving speakers and the SE volume when driving headphones. The pin is controlled with a dc voltage, which should not exceed VDD.

When driving speakers in BTL mode, the VOLUME pin is the only pin that controls the gain. Table 1 shows the gain for the BTL mode. The voltage listed in the table are for VDD=5V. For a different VDD, the values in the table scale linearly. If VDD=4V, multiply all the voltages in the table by 4V/5V or 0.8.

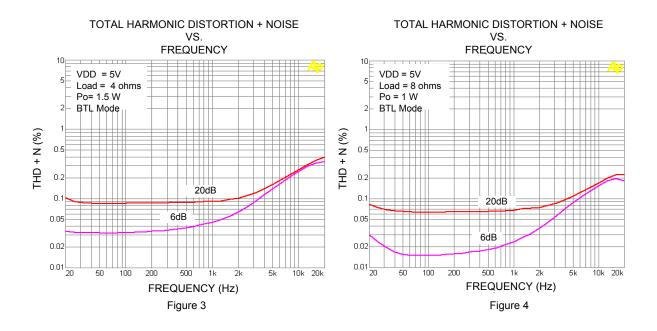
Some audio systems require that the gain be limited in the single-ended mode to a level that is comfortable for headphone listening. Most volume control devices only have one terminal for setting the gain. For example, if the speaker gain is 20 dB, the gain in the headphone channel is fixed at 14 dB.

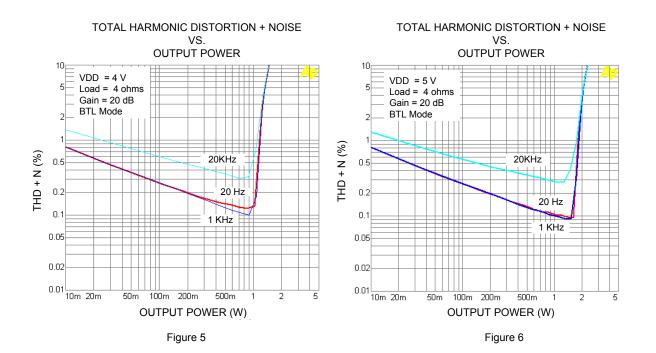
Table 1 show a range of voltages for each gain step. There is a gap in the voltage between each gain step. This gap represents the hysteresis about each trip point in the internal comparator. The hysteresis ensures that the gain control is monotonic and does not oscillate from one gain step to another. If a potentiometer is used to adjust the voltage on the control terminals, the gain increases as the potentiometer is turned in one direction and decreases as it is turned back the other direction. The trip point, where the gain actually changes, is different depending on whether the voltage is increased or decreased as a result of the hysteresis about each trip point. The gaps can also be thought of as indeterminate states where the gain could be in the next higher gain step or the lower gain step depending on the direction the voltage is changing. If using a DAC to control the volume, set the voltage in the middle of each range to ensure that the desired gain is achieved.

#### **TABLE OF GRAPHS**

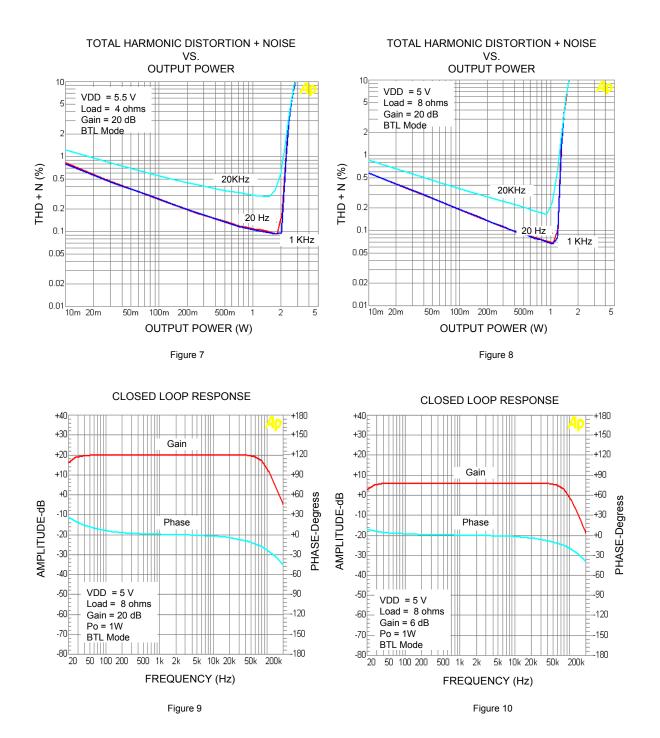
		FIGURE
THD+N Total harmonic distortion plus noise(BTL)	vs Frequency	3,4,5
	vs Output power	6,7,8
Closed loop response		9,10
Crosstalk	vs Frequency	11,12
PSRR Power supply ripple rejection(BTL)	vs Frequency	13



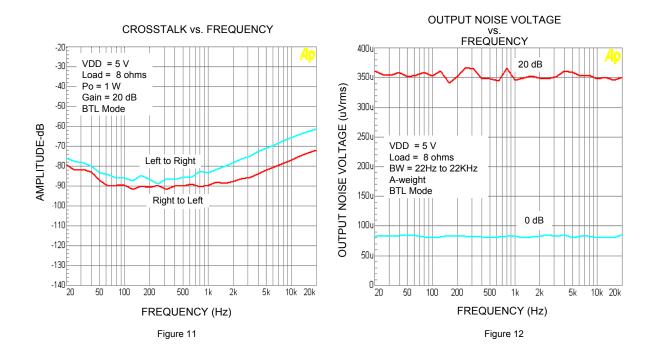








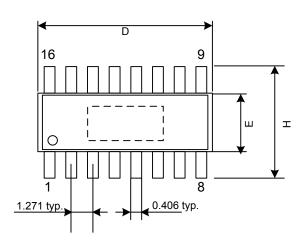


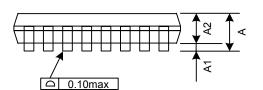




#### PACKAGE OUTLINE

SM7512S 16PIN SOP

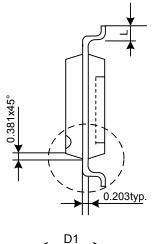


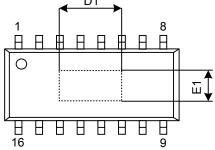


SYMBOLS	MIN.	NOM.
А	1.35	1.75
A1	0.05	0.15
D	9.81	10.02
E	3.81	4.0
Н	5.80	6.20
L	0.41	1.27
θ°	0°	8°

#### THERMALLY ENHANCED DIMENSIONS

PAD SIZE	E1	D1
95 x 18E	2.19 REF	4.12 REF





NOTES:

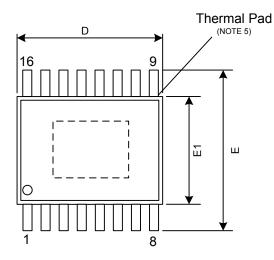
- 1. JEDEC OUTLINE : N/A
- 2. DIMENSIONS "D" DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.MOLD FLASH PROTRUSIONS AND GATE BURRS SHALL NOT EXCEED 0.15mm PER SIDE.
- 3. DIMENSION "E" DOES NOT INCLUDE INTER-LEAD FLASH, OR PROTRUSIONS. INTER-LEAD FLASH AND PROTRUSIONS SHALL NOT EXCEED 0.25mm PER SIDE.

Unit : mm

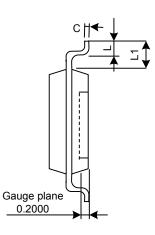


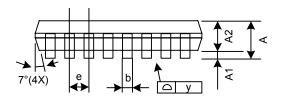
#### PACKAGE OUTLINE

SM7512F 16PIN SSOP









SYMBOLS	MIN.	NOM.	MAX.
A	1.35	1.50	1.65
A1	0.00	—	0.10
A2	_	1.45	—
b	0.20	0.25	0.30
С	0.19	—	0.25
D	4.80	—	5.00
E	5.80	6.0	6.20
E1	3.80	3.9	4.00
е	_	0.64	—
L	0.40	—	1.27
У	_	_	0.10
θ	0°	_	8°
L1	0.95	1.05	1.15

NOTES:

Page 14

- 1. PACKAGE BODY SIZE EXCLUDE MOLD FLASH PROTRUSION OR GATE BURRS.
- 2. TOLERANCE ± 0.1mm UNLESS OTHERWISE SPECIFIED
- 3. DIMENSION L IS MEASURED IN GAGE PLANE
- 4. CONTROLLING DIMENSION IS MILLIMETER. CONVERTED INCH DIMENSION ARE NOT NECESSARILY EXACT.
- 5. DIE PAD EXPOSURE SIZE IS ACCORDING TO LEAD FRAME DESIGN
- 6. FOLLOWED FROM JEDEC MO-137



# **3W AUDIO AMPLIFIER**

## **Ordering Information**

Package	Marking	Part Number ( Tape and Reel )
SOP-16	SM7512S	SM7512S
SSOP-16	SM7512F	SM7512F

#### Lead Free Information

Package	Marking	Lead Free Part Number
SOP-16	SM7512SL	SM7512SL
SSOP-16	SM7512FL	SM7512FL

#### **Green Information**

Package	Marking	Green Part Number
SOP-16	SM7512SG	SM7512SG
SSOP-16	SM7512FG	SM7512FG