

■ DESCRIPTION

SMC2355DSQ is the Dual P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced trench technology devices are well suited for high efficiency fast switching applications.

■ PART NUMBER INFORMATION

SMC 2355D SQ - TR G

a	b	c	d	e
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a : Company name.

b : Product Serial number.

c : Package code SQ: SOT-23-6L

d : Handling code TR: Tape&Reel

e : Green produce code G: RoHS Compliant

■ FEATURES

$V_{DS}=-20V, I_D=-3.5A$

$R_{DS(ON)}=60m\Omega(Typ.)@V_{GS}=-4.5V$

$R_{DS(ON)}=80m\Omega(Typ.)@V_{GS}=-2.5V$

$R_{DS(ON)}=110m\Omega(Typ.)@V_{GS}=-1.8V$

◆Fast switch

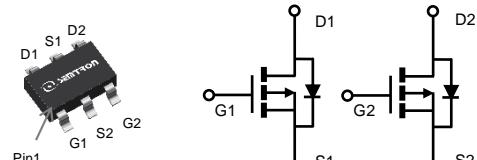
◆1.8V Low gate drive applications

◆High power and current handling capability

■ APPLICATIONS

◆Hand-Held Instruments

◆Load Switch



SOT-23-6L

■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ C$ Unless otherwise noted)

Symbol	Parameter	Rating	Units	
V_{DSS}	Drain-Source Voltage	-20	V	
V_{GSS}	Gate-Source Voltage	± 12	V	
I_D	Continuous Drain Current ^A ($V_{GS}=-4.5V$)	$T_A=25^\circ C$	-3.5	A
		$T_A=70^\circ C$	-2.8	A
I_{DM}	Pulsed Drain Current ^B	-13.8	A	
I_{AS}	Avalanche Current ^B	10	A	
E_{AS}	Single Pulse Avalanche energy L=0.1mH ^B	5	mJ	
P_D	Power Dissipation ^A	$T_A=25^\circ C$	1.4	W
		$T_A=70^\circ C$	0.9	W
T_J	Operation Junction Temperature	-55/150	°C	
T_{STG}	Storage Temperature Range	-55/150	°C	

■ THERMAL RESISTANCE

Symbol	Parameter	Typ	Max	Units
$R_{\theta JA}$	Thermal Resistance Junction to Ambient ^A $t \leq 10s$		90	°C/W
	Thermal Resistance Junction to Ambient ^{AC} Steady-State		130	

ELECTRICAL CHARACTERISTICS (TA=25°C Unless otherwise noted)

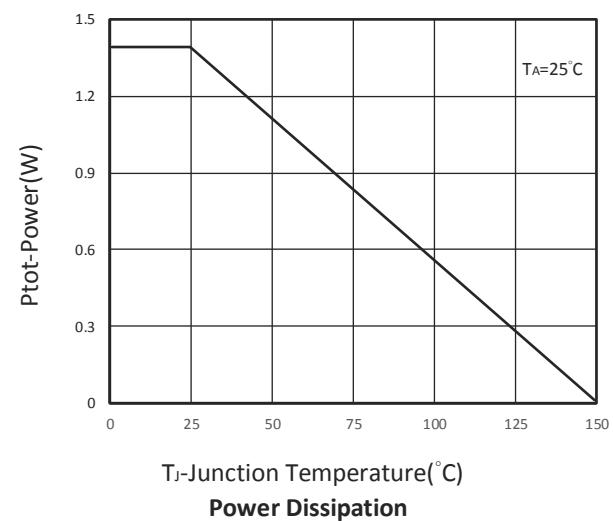
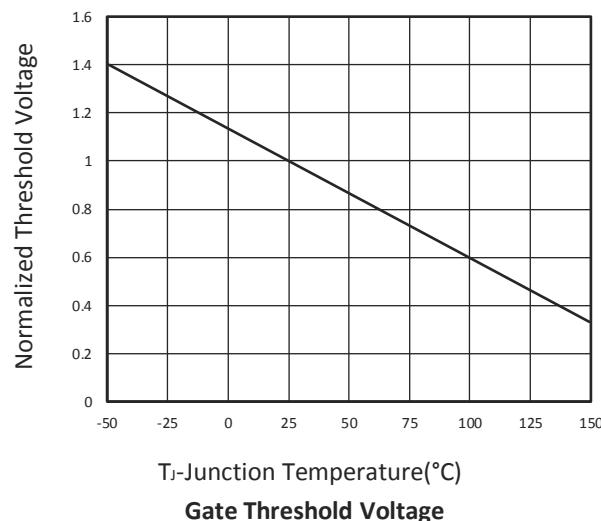
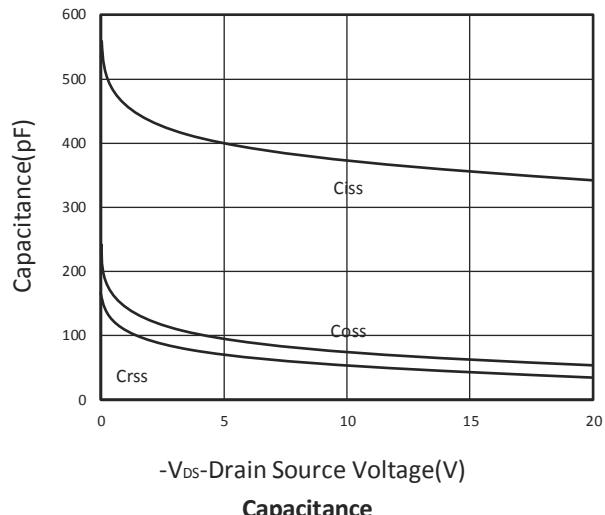
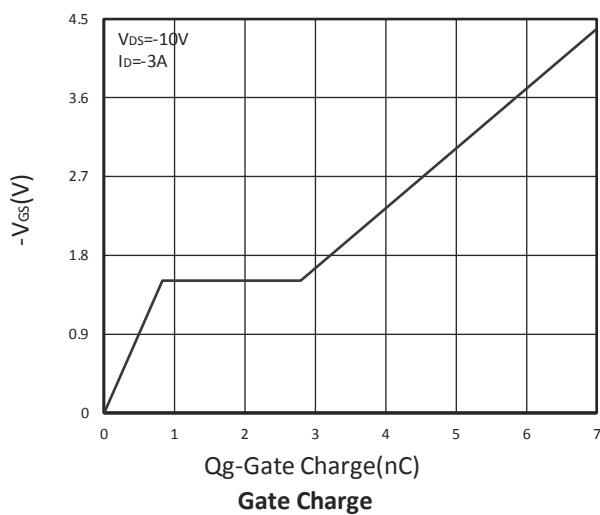
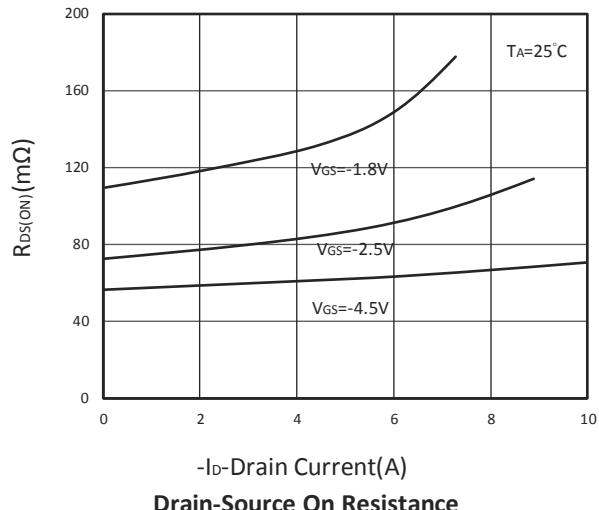
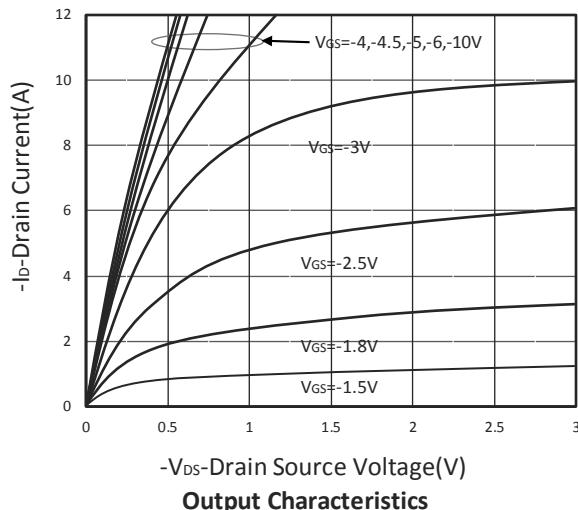
Symbol	Parameter	Condition	Min	Typ	Max	Unit	
Static Parameters							
BVDSS	Drain-Source Breakdown Voltage	VGS=0V, ID=-250µA	-20			V	
VGS(th)	Gate Threshold Voltage	VDS=VGS, ID=-250µA	-0.4	-0.7	-1	V	
IGSS	Gate Leakage Current	VDS=0V, VGS=±12V			±100	nA	
IDSS	Zero Gate Voltage Drain Current	VDS=-20V, VGS=0V, TJ=25°C			-1	µA	
		VDS=-16V, VGS=0V, TJ=75°C			-10		
RDS(ON)	Drain-source On-Resistance ^D	VGS=-4.5V, ID=-3.5A		60	70	mΩ	
		VGS=-2.5V, ID=-2.5A		80	100		
		VGS=-1.8V, ID=-1.5A		110	145		
Gf	Forward Transconductance	VDS=-5V, ID=-3A		2.5		S	
Diode Characteristics							
VSD	Diode Forward Voltage ^D	IS=-1A, VGS=0V			-1	V	
IS	Diode Continuous Forward Current				-3.2	A	
trr	Reverse Recovery Time	IS=-3A, dI/dt=100A/µs TJ=25°C		13.2		ns	
Qrr	Reverse Recovery Charge			7.4		nC	
Dynamic and Switching Parameters^E							
Qg	Total Gate Charge	VDS=-10V, VGS=-4.5V, ID=-3A		7.2	10.1	nC	
Qgs	Gate-Source Charge			0.8	1.1		
Qgd	Gate-Drain Charge			2	2.8		
Ciss	Input Capacitance	VDS=-10V, VGS=0V, f=1MHz		360		pF	
Coss	Output Capacitance			70			
Crss	Reverse Transfer Capacitance			55			
td(on)	Turn-On Time	VDD=-10V, VGEN=-4.5V, RG=3Ω, ID=-3A		4.8	9	nS	
tr				12.8	24		
td(off)	Turn-Off Time			20	38		
tf				6	11		

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

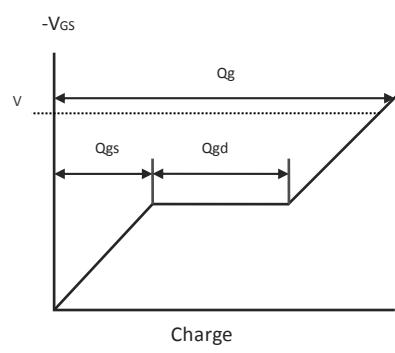
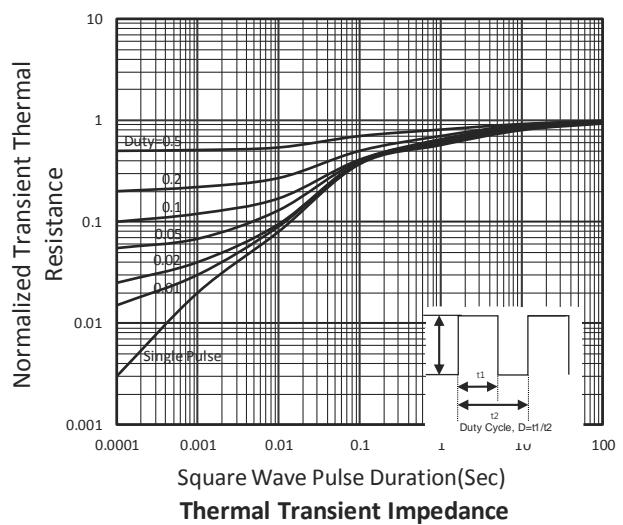
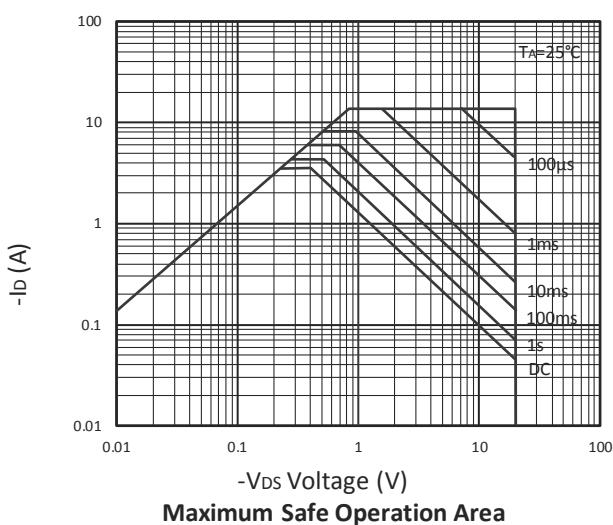
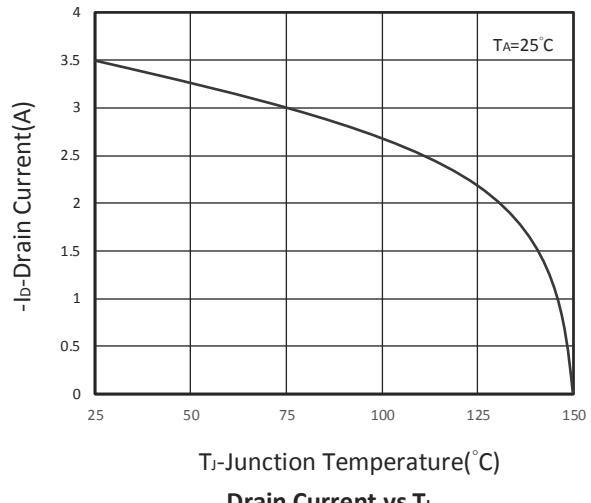
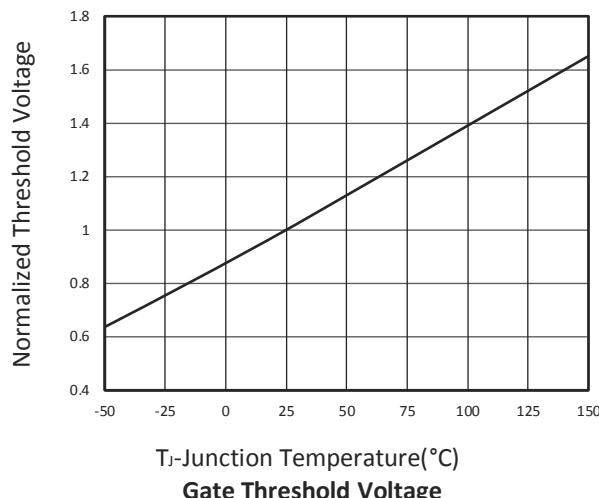
- A. Surface mounted on FR4 board using 1 in² pad size.
- B. Pulsed width limited by maximum junction temperature, TJ(MAX)=150°C (initial temperature TJ=25°C).
- C. Using ≤ 10s junction-to-ambient thermal resistance is base on TJ(MAX)=150°C.
- D. Pulse test width ≤300µs and duty cycle ≤ 2%.
- E. Guaranteed by design, not subject to production testing.

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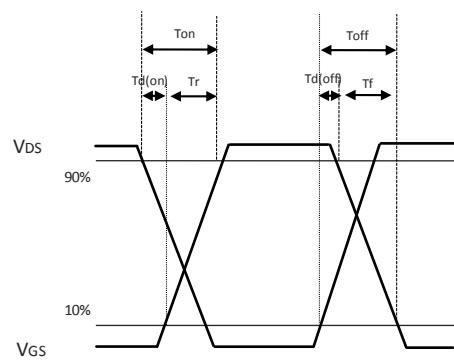
TYPICAL CHARACTERISTICS



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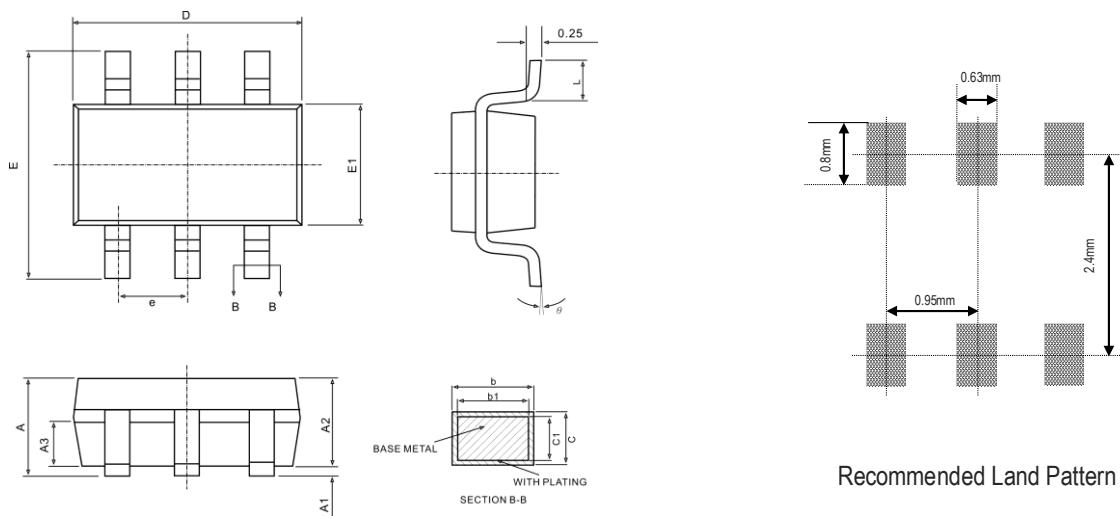


Gate Charge Waveform



Switching Time Waveform

SOT-23-6L PACKAGE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	-	1.300	-	0.051
A1	0.040	0.100	0.002	0.004
A2	1.000	1.200	0.039	0.047
A3	0.550	0.750	0.022	0.030
b	0.340	0.430	0.013	0.017
b1	0.330	0.380	0.013	0.015
c	0.150	0.210	0.006	0.008
c1	0.140	0.160	0.006	0.006
D	2.720	3.120	0.107	0.123
E	2.600	3.000	0.102	0.118
E1	1.400	1.800	0.055	0.071
e	0.950 BSC		0.066 BSC	
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°