

■ DESCRIPTION

SMC2869ESD6 is the dual N-Channel trench MOSFET are well suited for high efficiency fast switching applications, this MOSFET provide superior fast switching performance, this devices are well suited for applications in the small surface mount package.

■ PART NUMBER INFORMATION

SMC 2869 E SD6 - TR G

a	b	c	d	e	f
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a : Company name

b : Product Serial number

c : ESD Protection

d : Package code SD6: SOT-363

e : Handling code TR: Tape&Reel

f : Green produce code G: RoHS Compliant

■ FEATURES

$V_{DS}=-20V, I_D=0.52A$

$R_{DS(ON)}=560m\Omega(\text{Typ.}) @ V_{GS}=-4.5V$

$R_{DS(ON)}=740m\Omega(\text{Typ.}) @ V_{GS}=-2.5V$

$R_{DS(ON)}=1000m\Omega(\text{Typ.}) @ V_{GS}=-1.8V$

$R_{DS(ON)}=1400m\Omega(\text{Typ.}) @ V_{GS}=-1.5V$

◆ High-speed switching, Low On-resistance

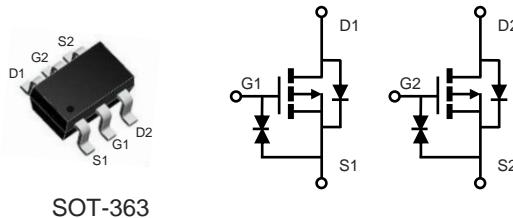
◆ 1.5V Low gate drive

◆ ESD protected

■ APPLICATIONS

◆ Load switch application for portable

◆ DC/DC converter



SOT-363

■ ABSOLUTE MAXIMUM RATINGS ($T_A=25^\circ\text{C}$ Unless otherwise noted)

Symbol	Parameter	Rating	Units
V_{DSS}	Drain-Source Voltage	-20	V
V_{GSS}	Gate-Source Voltage	± 10	V
I_D	Continuous Drain Current $T_A=25^\circ\text{C}$	-0.5	A
		$T_A=70^\circ\text{C}$	A
I_{DM}	Pulsed Drain Current ^B	-1.8	A
P_D	Power Dissipation ^A $T_A=25^\circ\text{C}$	0.28	W
		$T_A=70^\circ\text{C}$	0.18
T_J	Operation Junction Temperature	-55/150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55/150	$^\circ\text{C}$

■ THERMAL RESISTANCE

Symbol	Parameter	Typ	Max	Units
$R_{\theta JA}$	Thermal Resistance Junction to Ambient ^A	$t \leq 10\text{s}$	-	$^\circ\text{C/W}$
	Thermal Resistance Junction to Ambient ^{AC}	Steady-State	450	$^\circ\text{C/W}$

ELECTRICAL CHARACTERISTICS($T_A=25^\circ\text{C}$ Unless otherwise noted)

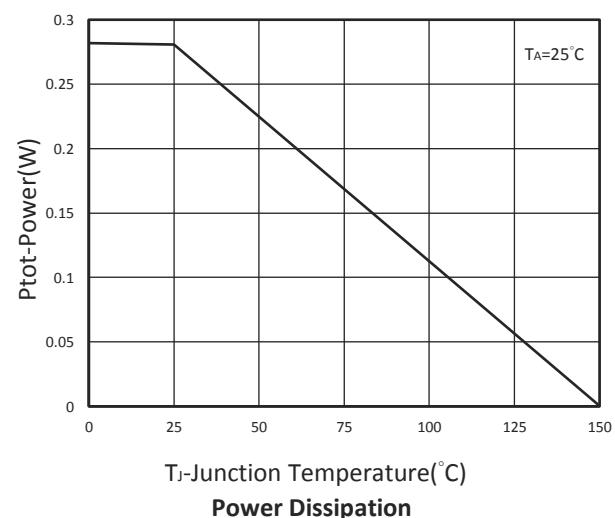
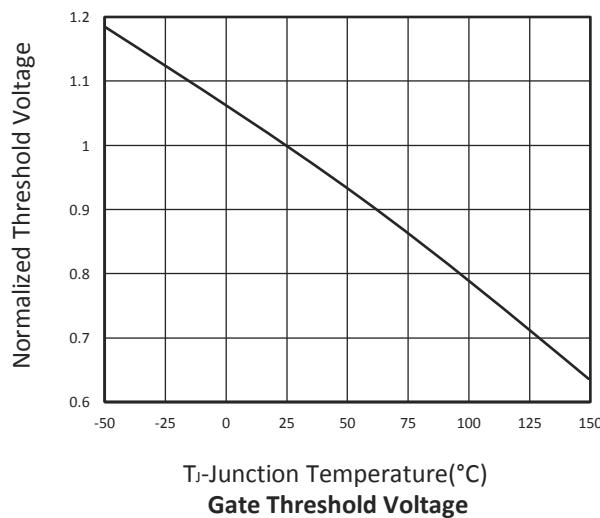
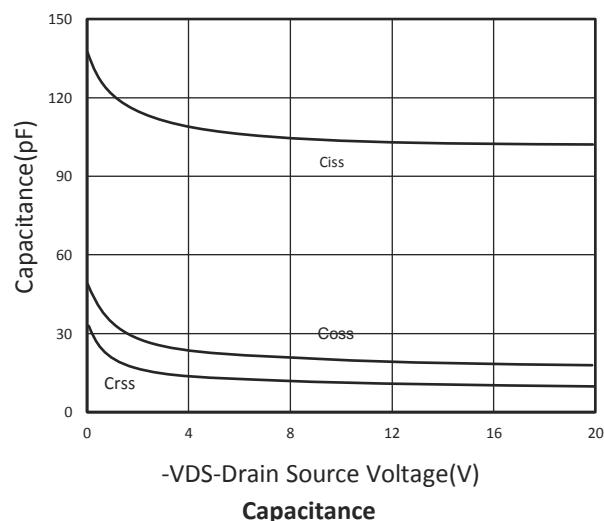
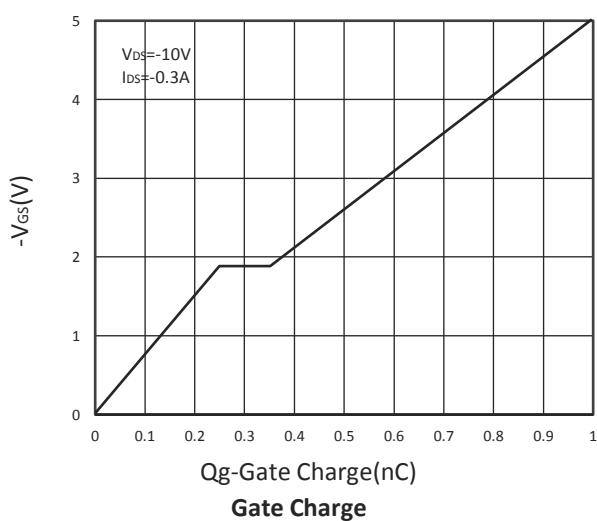
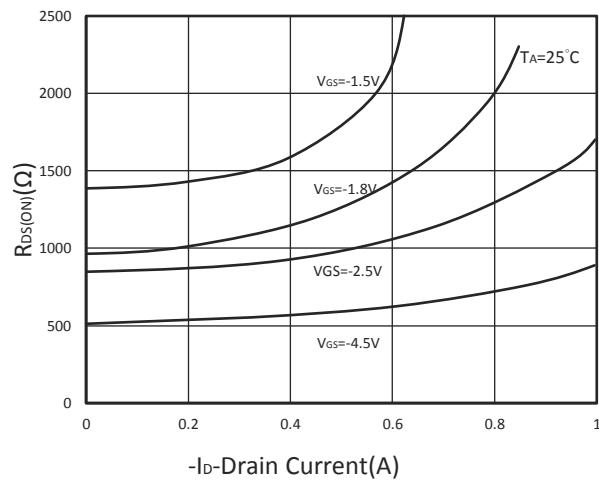
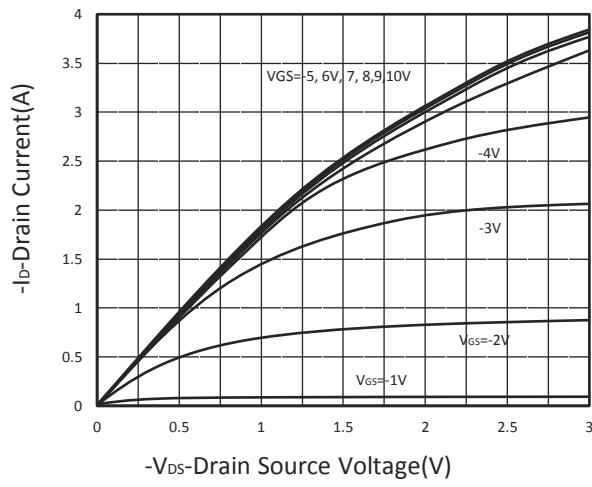
Symbol	Parameter	Condition	Min	Typ	Max	Unit	
Static Parameters							
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-20			V	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-0.5	-0.7	-1	V	
I_{GSS}	Gate Leakage Current	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 8\text{V}$			± 10	μA	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=-20\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$			1	μA	
		$V_{\text{DS}}=-12\text{V}, V_{\text{GS}}=0\text{V}, T_J=85^\circ\text{C}$			-10		
$R_{\text{DS}(\text{ON})}$	Drain-source On-Resistance ^D	$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-0.5\text{A}$		600	750	$\text{m}\Omega$	
		$V_{\text{GS}}=-2.5\text{V}, I_{\text{D}}=-0.3\text{A}$		800	950		
		$V_{\text{GS}}=-1.8\text{V}, I_{\text{D}}=-0.2\text{A}$		1000	1300		
		$V_{\text{GS}}=-1.5\text{V}, I_{\text{D}}=-0.1\text{A}$		1400	1800		
Diode Characteristics							
V_{SD}	Diode Forward Voltage ^D	$I_{\text{S}}=-0.2\text{A}, V_{\text{GS}}=0\text{V}$			-1	V	
I_{S}	Diode Continuous Forward Current				-0.5	A	
Dynamic and Switching Parameters ^F							
Q_g	Total Gate Charge	$V_{\text{DS}}=-10\text{V}, V_{\text{GS}}=-4.5\text{V}$ $I_{\text{D}}=-0.3\text{A}$		1		nC	
Q_{gs}	Gate-Source Charge			0.24			
Q_{gd}	Gate-Drain Charge			0.1			
C_{iss}	Input Capacitance	$V_{\text{DS}}=-10\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$		105		pF	
C_{oss}	Output Capacitance			16			
C_{rss}	Reverse Transfer Capacitance			9.4			
$t_{\text{d}(\text{on})}$	Turn-On Time	$V_{\text{DD}}=-10\text{V}, V_{\text{GS}}=-4.5\text{V}$ $R_{\text{G}}=4.5\Omega, I_{\text{D}}=-0.3\text{A}$		6.2		nS	
t_r				19			
$t_{\text{d}(\text{off})}$	Turn-Off Time			77			
t_f				231			

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

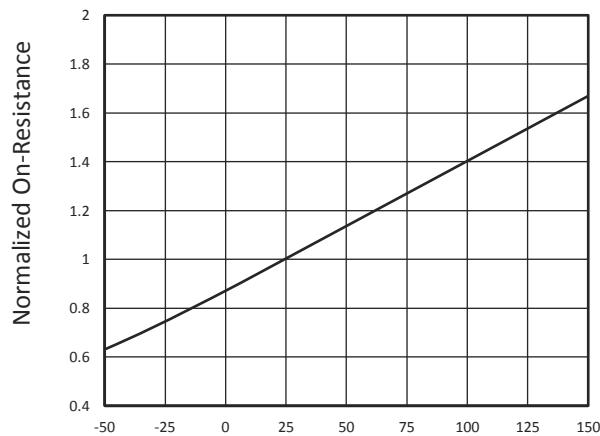
- A. Surface mounted on FR4 board using 1 in² pad size.
- B. Pulsed width limited by maximum junction temperature, $T_J(\text{MAX})=150^\circ\text{C}$ (initial temperature $T_J=25^\circ\text{C}$).
- C. Using $\leq 10\text{s}$ junction-to-ambient thermal resistance is base on $T_J(\text{MAX})=150^\circ\text{C}$.
- D. Pulse test width $\leq 300\mu\text{s}$ and duty cycle $\leq 2\%$.
- E. Guaranteed by design, not subject to production testing.

The products and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date. We assume no responsibility for any infringement of patents, patent rights, or other rights arising from the use of any information and circuitry in this datasheet.

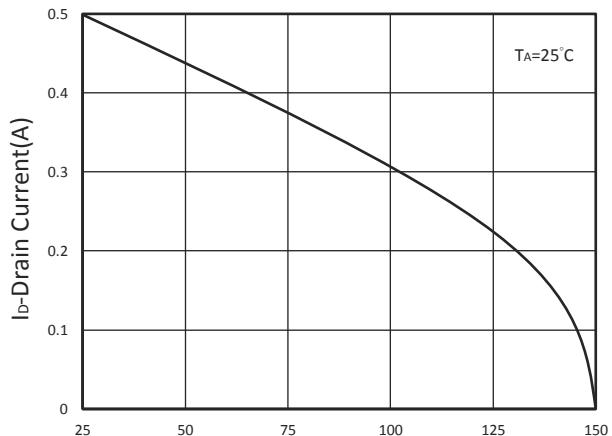
■ TYPICAL CHARACTERISTICS



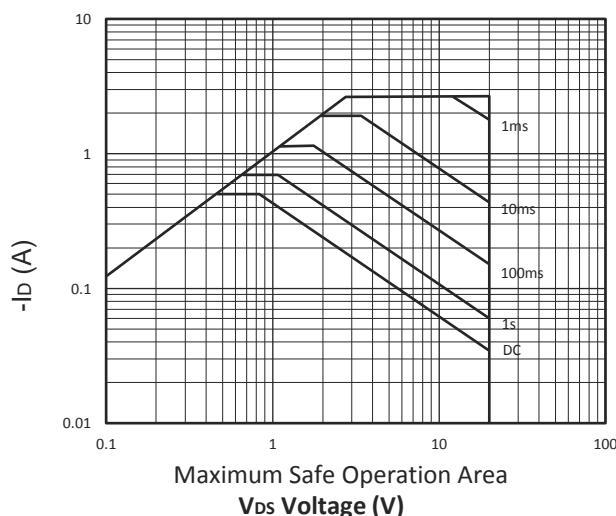
TYPICAL CHARACTERISTICS



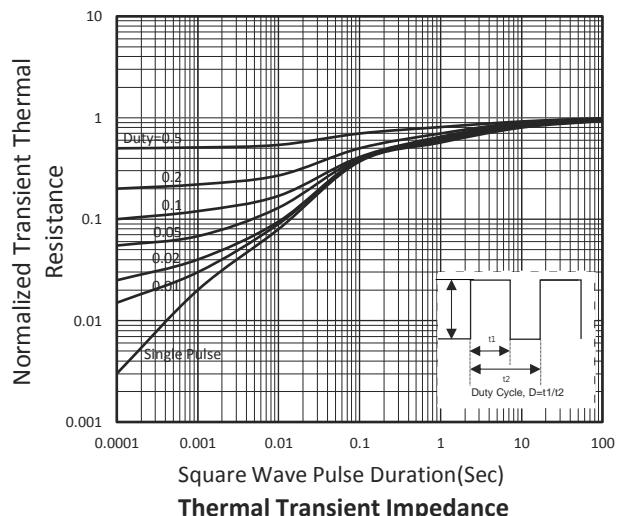
T_j-Junction Temperature(°C)
Drain-Source On Resistance



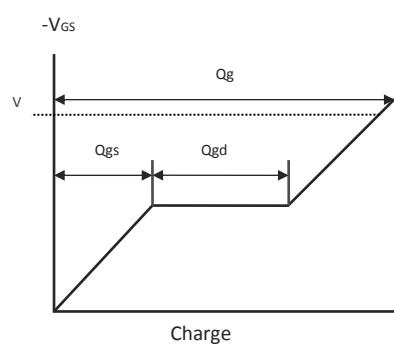
T_j-Junction Temperature(°C)
Drain Current vs T_j



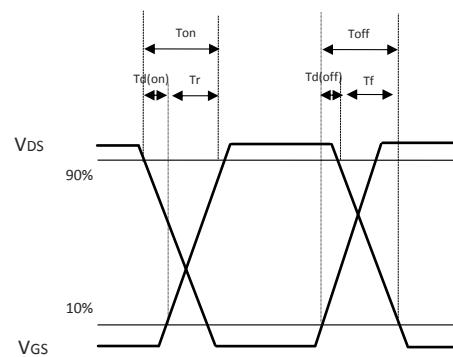
Maximum Safe Operation Area
V_{ds} Voltage (V)



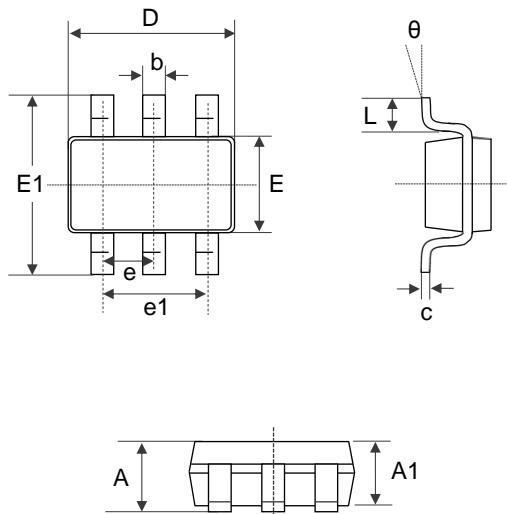
Square Wave Pulse Duration(Sec)
Thermal Transient Impedance



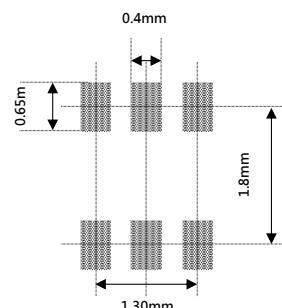
Gate Charge Waveform



Switching Time Waveform

SOT-363 PACKAGE DIMENSIONS


Recommended Land Pattern



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.100	0.035	0.043
A1	0.800	1.000	0.031	0.039
b	0.150	0.350	0.006	0.014
c	0.100	0.250	0.004	0.010
D	1.800	2.200	0.071	0.087
E	1.150	1.350	0.045	0.053
E1	2.000	2.400	0.079	0.094
e	0.650 BSC.		0.026 BSC.	
e1	1.200.	1.400	0.047	0.055
L	0.100	0.350	0.004	0.014
θ	0°	8°	0°	8°