

20V N-Channel Enhancement Mode MOSFET

DESCRIPTION

The SMC3056 is the N-Channel logic enhancement mode power field effect transistor is produced using high cell density. advanced trench technology to provide excellent $R_{DS(ON)}$. low gate charge and operation gate as 1.8V.

This device is suitable for use as a load switch or other general applications.

SMC3056S-TRG ROHS Compliant This is Halogen Free

FEATURE

- ◆ **20V/6.0A, $R_{DS(ON)} = 22m\Omega(typ.)@V_{GS} = 4.5V$**
- ◆ **20V/4.0A, $R_{DS(ON)} = 27m\Omega(typ.)@V_{GS} = 2.5V$**
- ◆ **20V/2.8A, $R_{DS(ON)} = 35m\Omega(typ.)@V_{GS} = 1.8V$**
- ◆ Super high density cell design for extremely low $R_{DS(ON)}$
- ◆ Exceptional on-resistance and Maximum DC current capability

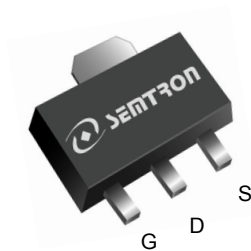
APPLICATIONS

- ◆ Power Management in Note book
- ◆ Portable Equipment
- ◆ DSC
- ◆ LCD Display inverter
- ◆ Battery Powered System

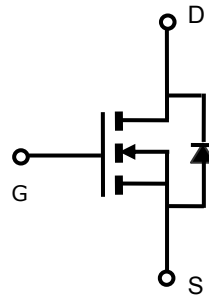


N-Channel Enhancement Mode MOSFET

PIN CONFIGURATION



SOT-89
Top View



PART NUMBER INFORMATION

SMC 3056 K – TR G a b c d e	a : Company name. b : Product Serial number. c : Package code d : Handling code e : Green produce code
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ORDERING INFORMATION

Part Number	Package Code	Handling Code	Shipping
SMC3056K-TRG	K : SOT-89	TR : Tape&Reel	1K/Reel

※ Year Code : 0 ~ 9, 2010 : 0

※ Week Code : A(1~2) ~ Z(53~54)

※ SOT-23L : Only available in tape and reel packaging.

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}\text{C}$ Unless otherwise noted)

Symbol	Parameter		Typical	Unit
V _{DSS}	Drain-Source Voltage		20	V
V _{GSS}	Gate-Source Voltage		±12	V
I _D	Continuous Drain Current (T _C =25°C) ^A	V _{GS} =4.5V	6.0	A
	Continuous Drain Current (T _C =70°C) ^A		5.0	A
I _{DM}	Pulsed Drain Current ^B		14	A
P _D	Power Dissipation	T _A =25°C	2.5	W
		T _A =70°C	1.2	
T _J	Operation Junction Temperature		-55 to150	°C
T _{STG}	Storage Temperature Range		-55 to150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

THERMAL DATA

Symbol	Parameter		Typ	Max	Unit
$R_{\theta JA}$	Thermal Resistance-Junction to Ambient ^A	Steady-State	-	120	$^{\circ}\text{C}/\text{W}$
$R_{\theta JL}$	Thermal Resistance Junction to Lead ^A	Steady-State	-	80	$^{\circ}\text{C}/\text{W}$

ELECTRICAL CHARACTERISTICS($T_J = 25^\circ\text{C}$ Unless otherwise noted)

Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Parameters						
$V_{(BR)DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=250\mu A$	20			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=250\mu A$	0.5		1.0	V
I_{GSS}	Gate Leakage Current	$V_{DS}=0V, V_{GS}=\pm 12V$			± 100	nA
I_{DSS}	Zero Gate Voltage, Drain-Source Leakage Current	$V_{DS}=20V, V_{GS}=0V$ $T_J=25^\circ\text{C}$			1	μA
		$V_{DS}=20V, V_{GS}=0V$ $T_J=55^\circ\text{C}$			5	
$R_{DS(ON)}$	Drain-source On-Resistance ^B	$V_{GS}=4.5V, I_D=6.0A$		22	27	m Ω
		$V_{GS}=2.5V, I_D=4.0A$		27	32	
		$V_{GS}=1.8V, I_D=2.8A$		35	45	
G_{fs}	Forward Transconductance	$V_{DS}=15V, I_D=5.0A$		30		S
Source-Drain Diode						
V_{SD}	Diode Forward Voltage	$I_S=1.7A, V_{GS}=0V$		0.6	1.2	V
I_S	Continuous Source Current ^{AD}				6	A
Dynamic Parameters						
$Q_g(4.5V)$	Total Gate Charge	$V_{DS}=10V$ $V_{GS}=4.5V$ $I_D=5.8A$		7.7		nC
Q_{gs}	Gate-Source Charge			1.1		
Q_{gd}	Gate-Drain Charge			2.35		
C_{iss}	Input Capacitance	$V_{DS}=10V$ $V_{GS}=0V$ $f=1\text{MHz}$		650		pF
C_{oss}	Output Capacitance			92		
C_{rss}	Reverse Transfer Capacitance			75		
$t_{d(on)}$	Turn-On Time	$V_{DD}=10V$ $I_D=1.0A$		19.1		nS
t_r				135		
$t_{d(off)}$	Turn-Off Time	$V_{GEN}=4.5V$ $R_G=6\Omega$		90		
t_f				116		

Note:

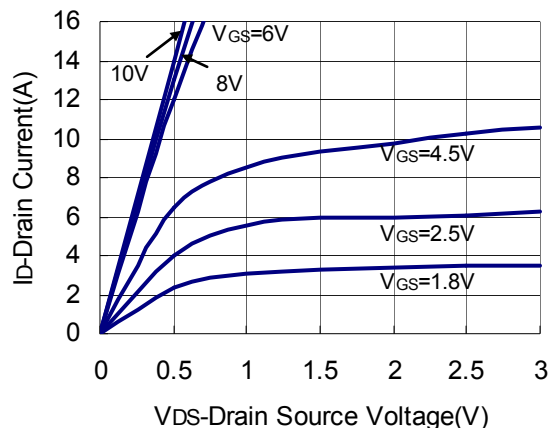
- The value of $R_{\theta JA}$ is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$.
- The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- The EAS data shows Max. rating . The test condition is $V_{DD}=20V, V_{GS}=10V, L=0.1mH$.
- The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

The products and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date

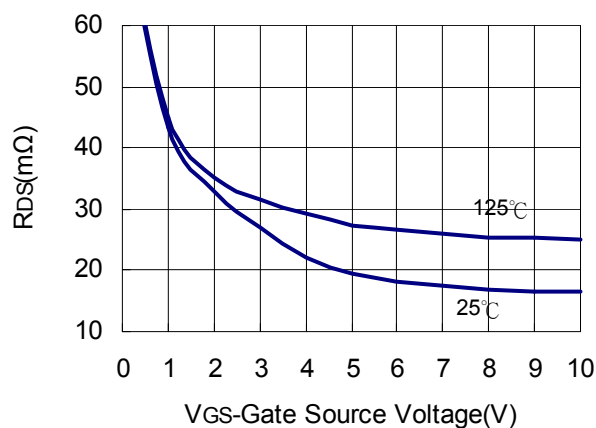
We assume no responsibility for any infringement of patents, patent rights, or other rights arising from the use of any information and circuitry in this datasheet.

TYPICAL CHARACTERISTICS (25°C Unless Note)

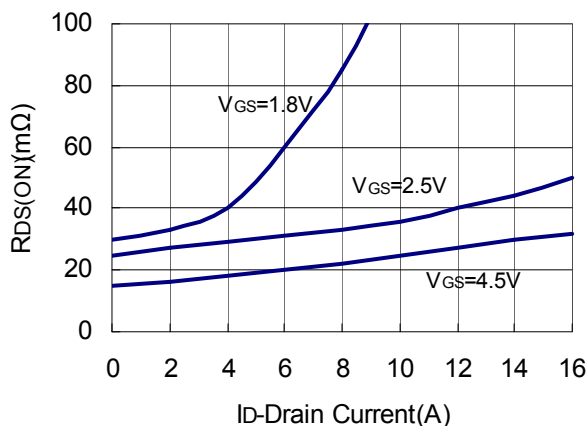
Output Characteristics



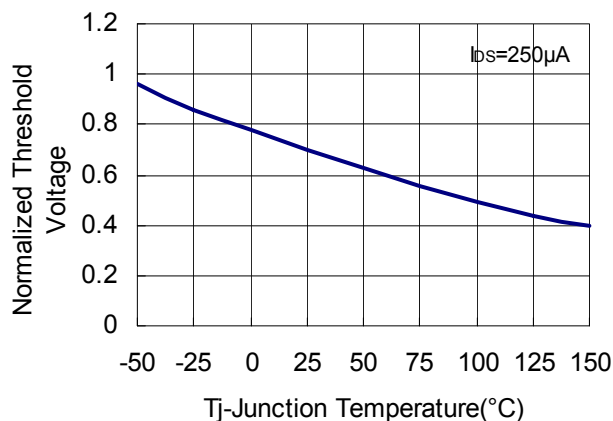
Drain-Source On Resistance



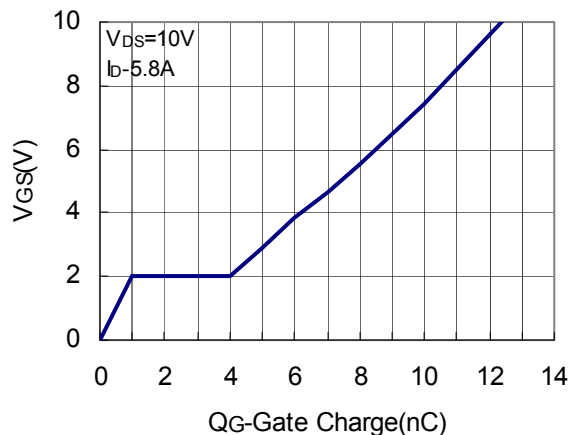
Drain Source On Resistance



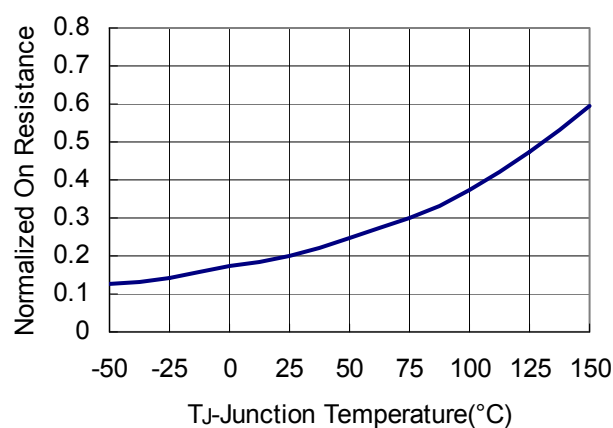
Gate Threshold Voltage



Gate Charge

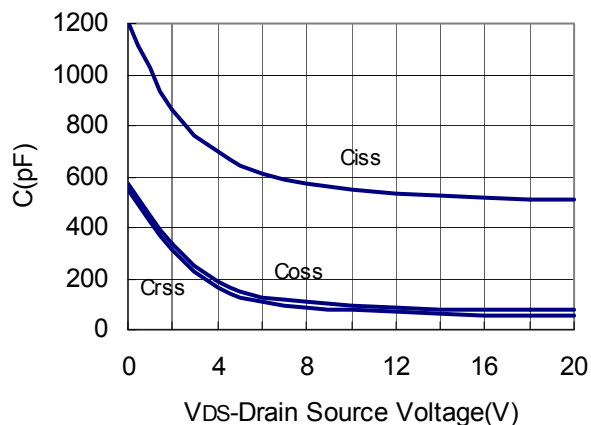


Drain Source On Resistance

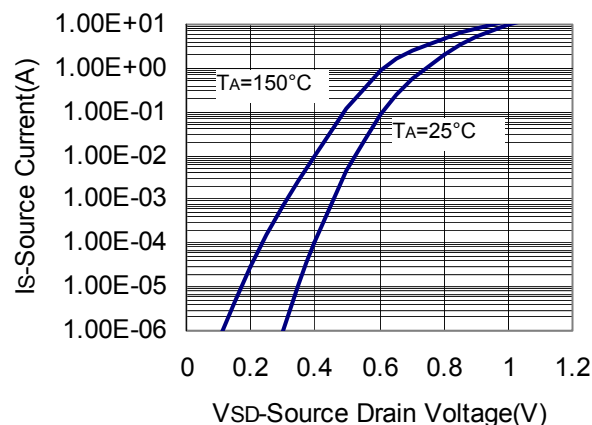


TYPICAL CHARACTERISTICS (25°C Unless Note)

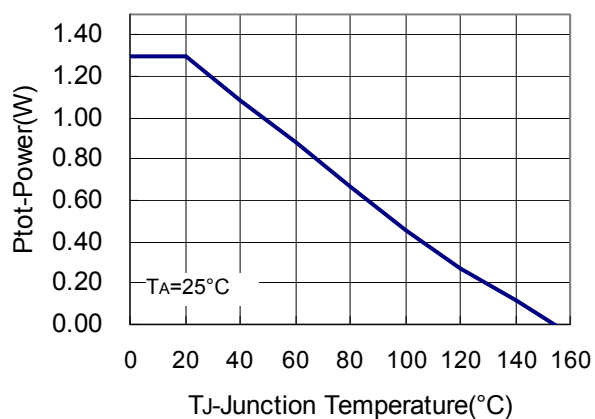
Capacitance



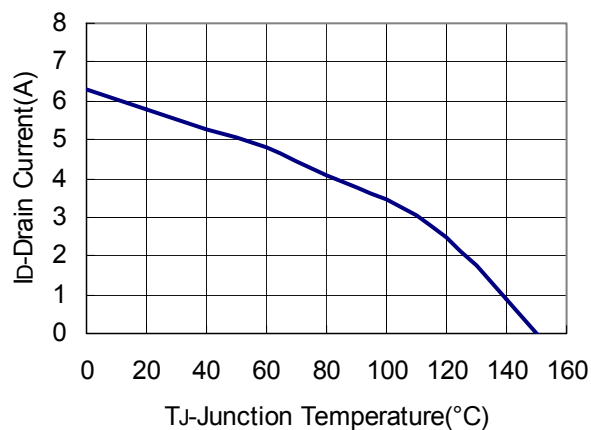
Source Drain Diode Forward



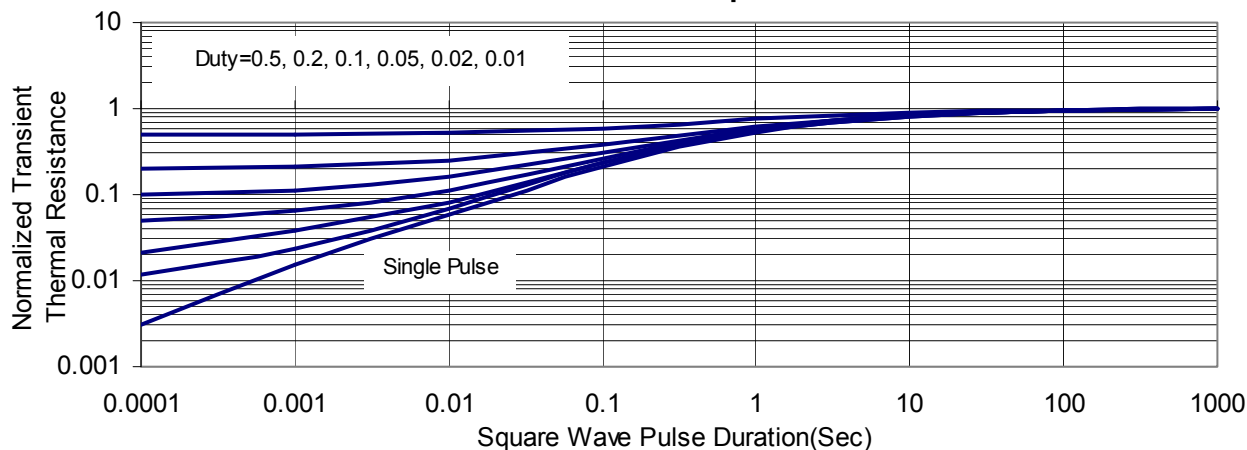
Power Dissipation



Drain Current



Thermal Transient Impedance



■ SOT-89 PACKAGE DIMENSIONS

Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.400	1.600	0.055	0.063
b	0.320	0.520	0.130	0.197
b1	0.400	0.580	0.016	0.023
c	0.350	0.440	0.014	0.017
D	4.400	4.600	0.173	0.181
D1	1.550 BSC		0.061 BSC	
E	2.300	2.600	0.091	0.102
E1	3.940	4.250	0.155	0.167
e	1.550 BSC		0.060 BSC	
e1	3.000 BSC		0.118 BSC	
L	0.900	1.200	0.035	0.047

