

Single N-Channel MOSFET

■ DESCRIPTION

SMC3206H is the N-Channel enhancement mode power field effect transistors, provide superior fast switching performance and withstand high energy pulse in the avalanche and commutation mode.

■ PART NUMBER INFORMATION

SMC 3206 H - TR G

a	b	c	d	e
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a : Company name.

b : Product Serial number.

c : Package code H:TO-252

d : Handling code TR:Tape&Reel

e : Green produce code G:RoHS Compliant

■ FEATURES

V_{DS}=30V, I_D=100A

R_{DS(ON)}=2.7mΩ(Typ.)@V_{GS}=10V

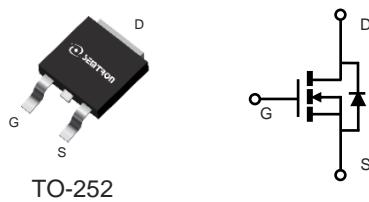
R_{DS(ON)}=3.4mΩ(Typ.)@V_{GS}=4.5V

- ◆ High power and current handling capability
- ◆ 100% EAS Guaranteed

■ APPLICATIONS

◆ DC/DC Converter

◆ Power Management



■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C Unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DSS}	Drain-Source Voltage	30	V
V _{GSS}	Gate-Source Voltage	±20	V
I _D	Continuous Drain Current ^A	100	A
	T _C =25°C	100	A
	T _C =100°C	72.5	A
I _{DM}	Pulsed Drain Current ^B	280	A
I _D	Continuous Drain Current	22	A
	T _A =25°C	22	A
	T _A =70°C	17.5	A
P _D	Power Dissipation ^A	2.5	W
	T _A =25°C	2.5	W
	T _A =70°C	1.6	W
I _{AS}	Single Pulse Avalanche Current ^B	45	A
E _{AS}	Single Pulse Avalanche energy L=0.1mH ^{BE}	101	mJ
P _D	Power Dissipation ^C	69	W
	T _C =25°C	69	W
	T _C =100°C	28	W
T _J	Operation Junction Temperature	-55/150	°C
T _{STG}	Storage Temperature Range	-55/150	°C

■ THERMAL RESISTANCE

Symbol	Parameter	Typ	Max	Units
R _{θJA}	Thermal Resistance Junction to Ambient ^A	t≤10s	20	°C/W
	Thermal Resistance Junction to Ambient ^{AC}	Steady-State	50	
R _{θJC}	Thermal Resistance Junction to Case		1.8	

ELECTRICAL CHARACTERISTICS($T_A=25^\circ\text{C}$ Unless otherwise noted)

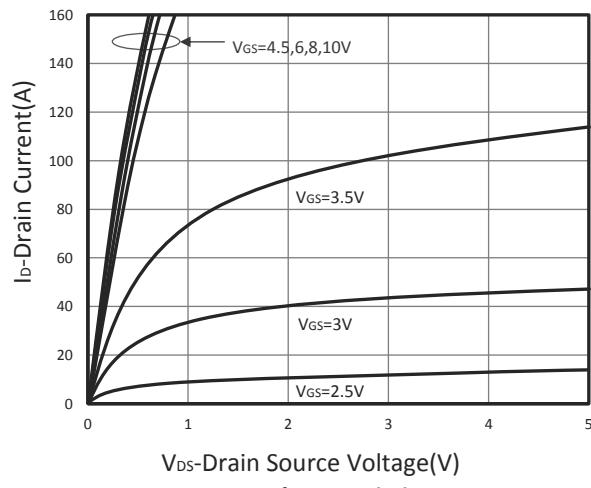
Symbol	Parameter	Condition	Min	Typ	Max	Unit	
Static Parameters							
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	30			V	
$V_{\text{GS}(\text{th})}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1.2	1.6	2.5	V	
I_{GSS}	Gate Leakage Current	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 20\text{V}$			± 100	nA	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=30\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$		-1		μA	
		$V_{\text{DS}}=24\text{V}, V_{\text{GS}}=0\text{V}, T_J=75^\circ\text{C}$		-10			
$R_{\text{DS(ON)}}$	Drain-source On-Resistance ^D	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=20\text{A}$		2.7	3.2	$\text{m}\Omega$	
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=15\text{A}$		3.4	4.2		
G_{fs}	Forward Transconductance	$V_{\text{DS}}=5\text{V}, I_{\text{D}}=20\text{A}$	72			S	
Diode Characteristics							
V_{SD}	Diode Forward Voltage ^D	$I_{\text{S}}=1\text{A}, V_{\text{GS}}=0\text{V}$			1	V	
I_{S}	Diode Continuous Forward Current				80	A	
t_{rr}	Reverse Recovery Time	$I_{\text{S}}=20\text{A}, dI/dt=100\text{A}/\mu\text{s}$	34			ns	
Q_{rr}	Reverse Recovery Charge		25			nC	
Dynamic and Switching Parameters ^F							
Q_{g}	Total Gate Charge	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=10\text{V}, I_{\text{D}}=20\text{A}$		37.5	52.5	nC	
Q_{g}	Total Gate Charge (4.5V)			17.5	24.5		
Q_{gs}	Gate-Source Charge			5.4	7.6		
Q_{gd}	Gate-Drain Charge			6.7	9.4		
C_{iss}	Input Capacitance	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	2250			pF	
C_{oss}	Output Capacitance		315				
C_{rss}	Reverse Transfer Capacitance		168				
R_{g}	Gate Resistance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, f=1\text{MHz}$	2			Ω	
$t_{\text{d(on)}}$	Turn-On Time	$V_{\text{DD}}=15\text{V}, V_{\text{GS}}=10\text{V}, R_{\text{G}}=3\Omega$ $I_{\text{D}}=1\text{A}$	8			nS	
			5				
$t_{\text{d(off)}}$	Turn-Off Time		23				
			9				

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

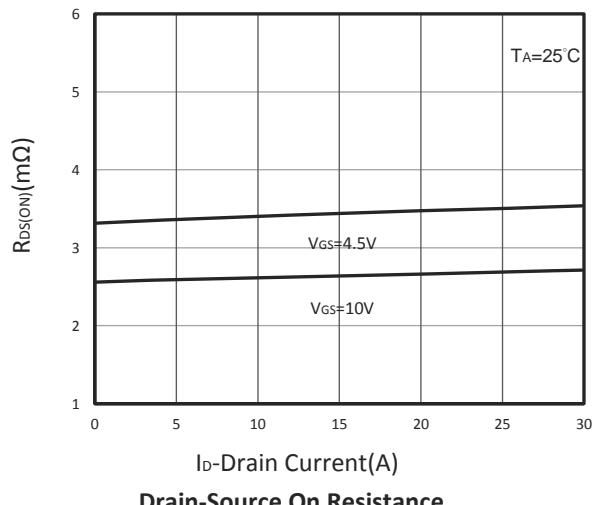
- A. Surface mounted on FR4 board using 1 in² pad size.
- B. Pulsed width limited by maximum junction temperature, $T_{\text{J(MAX)}}=150^\circ\text{C}$ (initial temperature $T_{\text{J}}=25^\circ\text{C}$).
- C. Using $\leq 10\text{s}$ junction-to-ambient thermal resistance is base on $T_{\text{J(MAX)}}=150^\circ\text{C}$.
- D. Pulse test width $\leq 300\mu\text{s}$ and duty cycle $\leq 2\%$.
- E. The EAS data shows maximum, The test condition is $V_{\text{DD}}=25\text{V}, V_{\text{GS}}=10\text{V}, L=0.1\text{mH}, I_{\text{AS}}=45\text{A}$
- F. Guaranteed by design, not subject to production testing.
- *. The maximum rating current limited by package.

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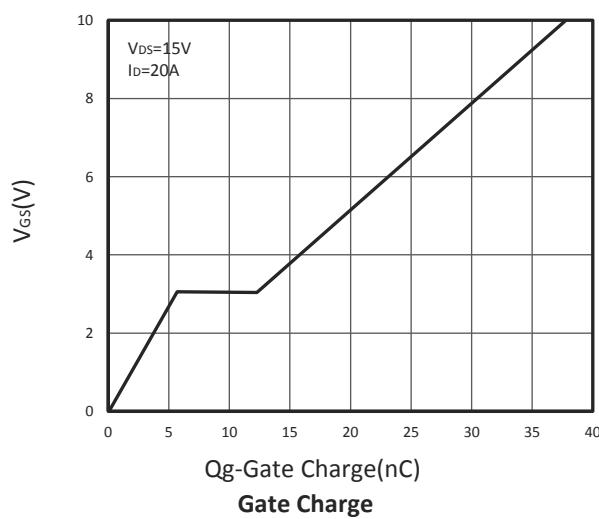
■ TYPICAL CHARACTERISTICS



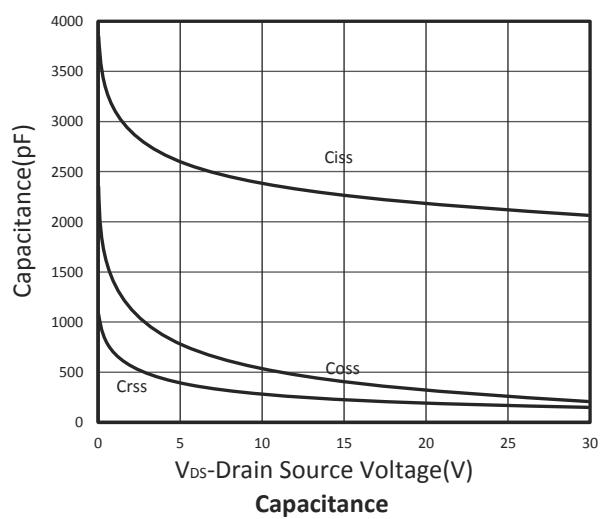
Output Characteristics



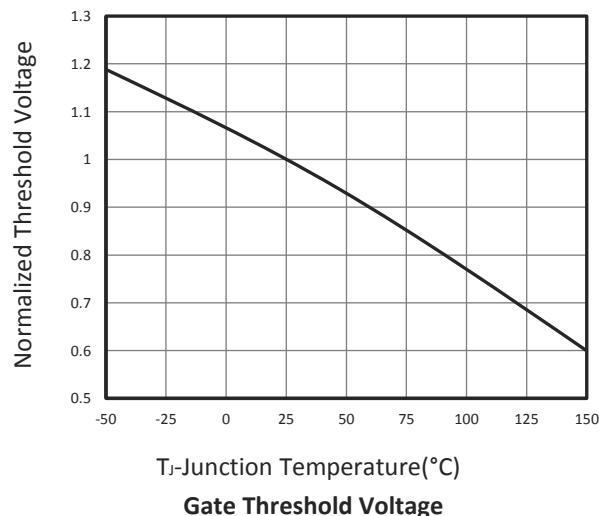
Drain-Source On Resistance



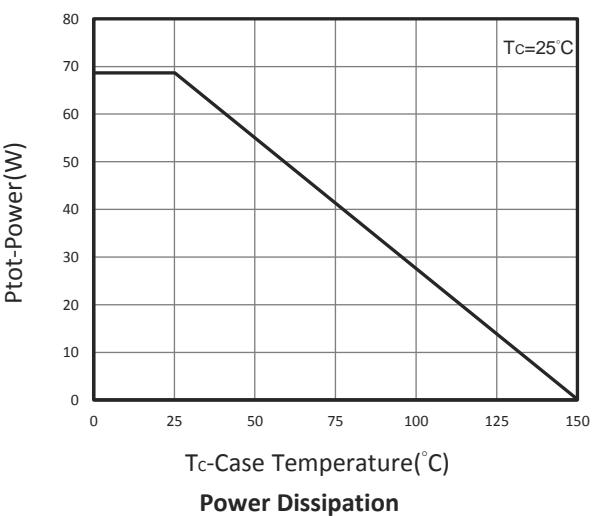
Gate Charge



Capacitance

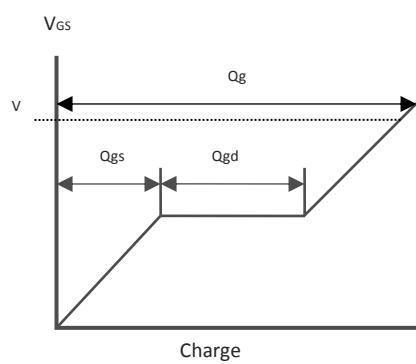
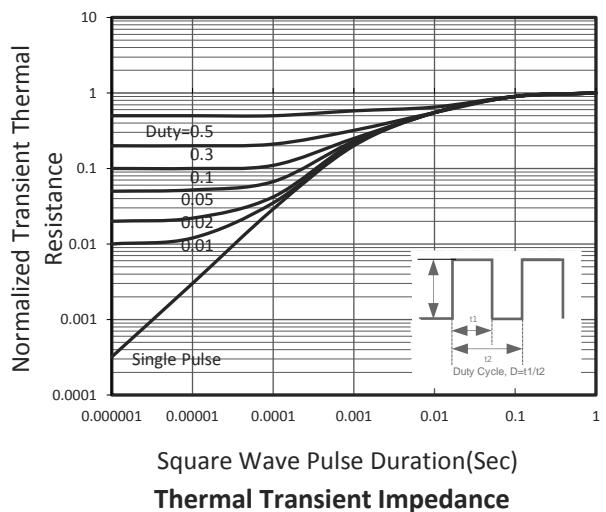
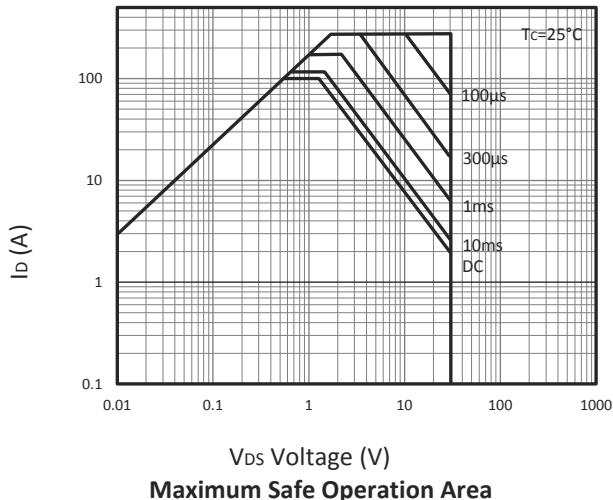
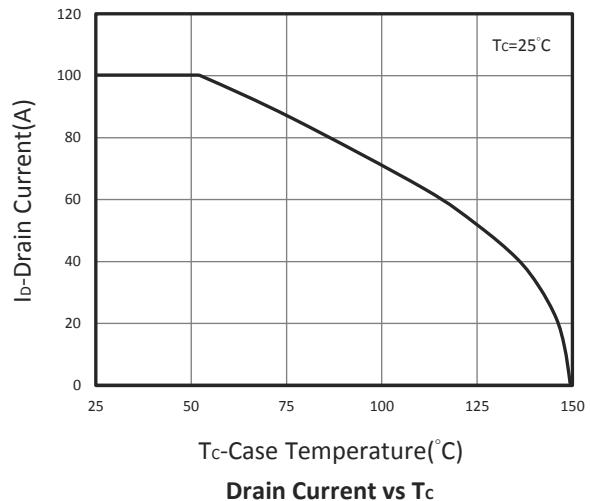
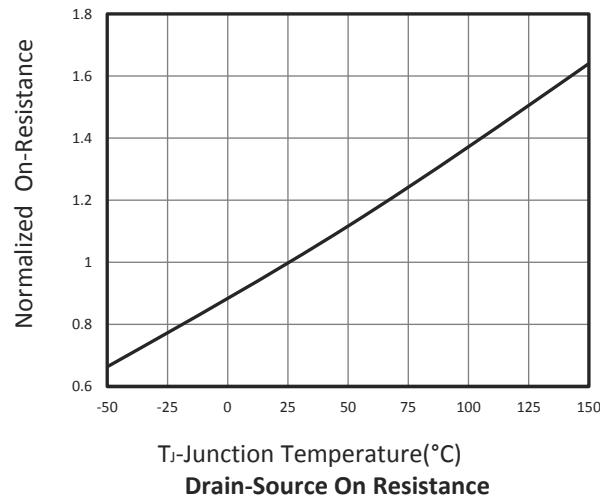


Gate Threshold Voltage

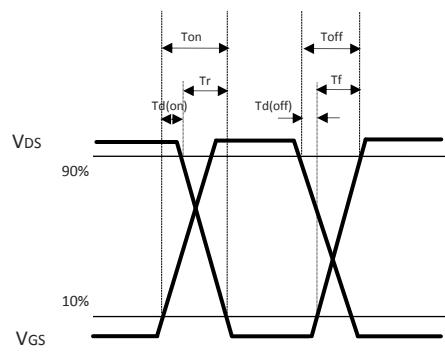


Power Dissipation

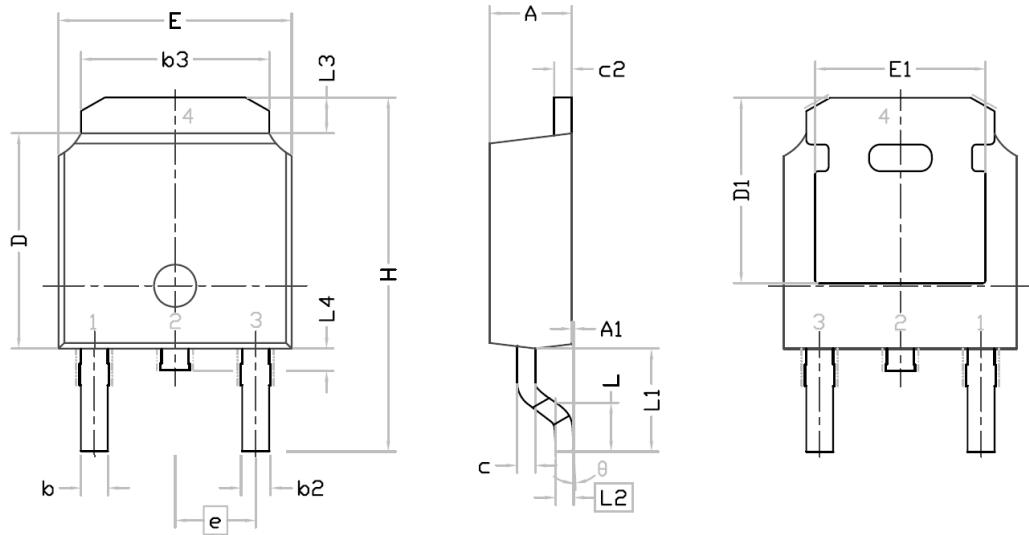
■ TYPICAL CHARACTERISTICS



Gate Charge Waveform



Switching Time Waveform

TO-252 PACKAGE DIMENSIONS


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.380	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.640	0.880	0.025	0.035
b2	0.770	1.140	0.030	0.045
b3	5.210	5.460	0.205	0.215
c	0.460	0.600	0.018	0.024
c2	0.460	0.580	0.018	0.023
D	6.000	6.223	0.236	0.245
D1	5.210	-	0.205	-
E	6.400	6.731	0.252	0.265
E1	4.400	-	0.173	-
e	2.286 BSC.		0.090 BSC.	
H	9.400	10.40	0.370	0.409
L	1.400	1.770	0.055	0.070
L1	2.743 REF.		0.108 REF.	
L2	0.508 BSC.		0.020 BSC.	
L3	0.890	1.270	0.035	0.050
L4	0.640	1.010	0.025	0.040
Θ	0°	10°	0°	10°

Recommended Land Pattern

