

Single N-Channel MOSFET

DESCRIPTION

SMC3240H is the N-Channel enhancement mode power field effect transistors, provide superior fast switching performance and withstand high energy pulse in the avalanche and commutation mode.

PART NUMBER INFORMATION

SMC 3240 H - TR G
 a b c d e

a : Company name.
 b : Product Serial number.
 c : Package code H:TO-252
 d : Handling code TR:Tape&Reel
 e : Green produce code G:RoHS Compliant

FEATURES

$V_{DS}=30V$, $I_D=53A$

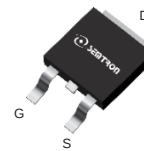
$R_{DS(ON)}=7m\Omega(Typ.)@V_{GS}=10V$

$R_{DS(ON)}=9m\Omega(Typ.)@V_{GS}=4.5V$

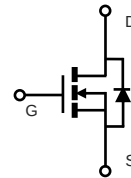
- ◆ High power and current handling capability
- ◆ 100% EAS Guaranteed

APPLICATIONS

- ◆ DC/DC Converter
- ◆ Power Management



TO-252



ABSOLUTE MAXIMUM RATINGS ($T_A=25^{\circ}C$ Unless otherwise noted)

Symbol	Parameter		Rating	Units
V _{DSS}	Drain-Source Voltage		30	V
V _{GSS}	Gate-Source Voltage		±20	V
I _D	Continuous Drain Current [*]	T _C =25°C	53	A
		T _C =100°C	33.4	A
I _{DM}	Pulsed Drain Current ^B		160	A
I _D	Continuous Drain Current	T _A =25°C	21	A
		T _A =70°C	17	A
P _D	Power Dissipation ^A	T _A =25°C	6.3	W
		T _A =70°C	4	W
I _{AS}	Single Pulse Avalanche Current ^B		30	A
E _{AS}	Single Pulse Avalanche energy L=0.1mH ^{BE}		45	mJ
P _D	Power Dissipation ^C	T _C =25°C	39	W
		T _C =100°C	15.6	W
T _J	Operation Junction Temperature		-55/150	°C
T _{STG}	Storage Temperature Range		-55/150	°C

THERMAL RESISTANCE

Symbol	Parameter	Typ	Max	Units
$R_{\theta JA}$	Thermal Resistance Junction to Ambient ^A	$t \leq 10s$	20	$^{\circ}C/W$
	Thermal Resistance Junction to Ambient ^{AC}	Steady-State	50	
$R_{\theta JC}$	Thermal Resistance Junction to Case		3.2	

ELECTRICAL CHARACTERISTICS (T_A=25°C Unless otherwise noted)

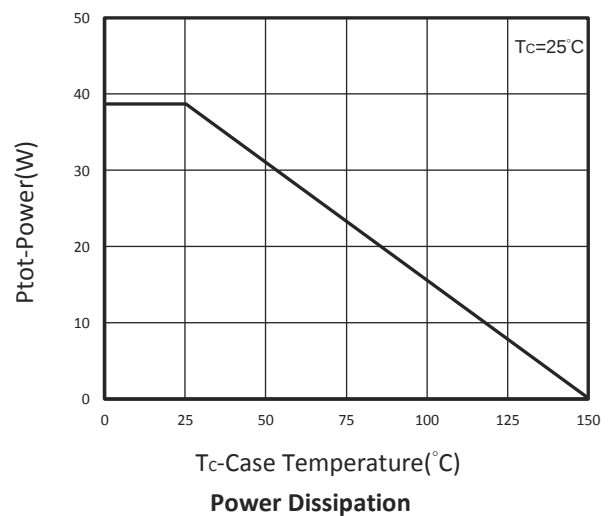
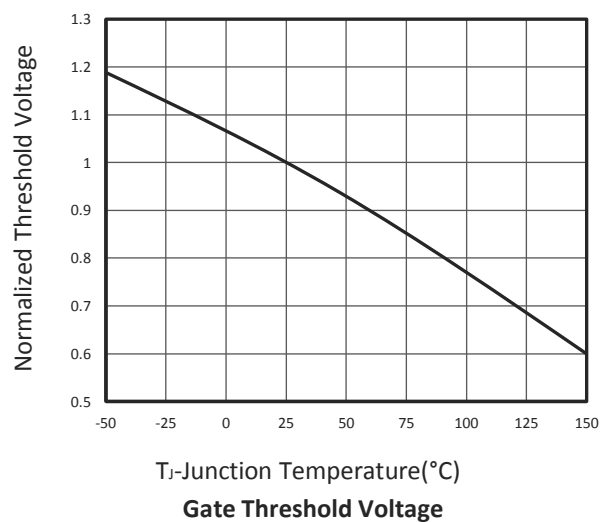
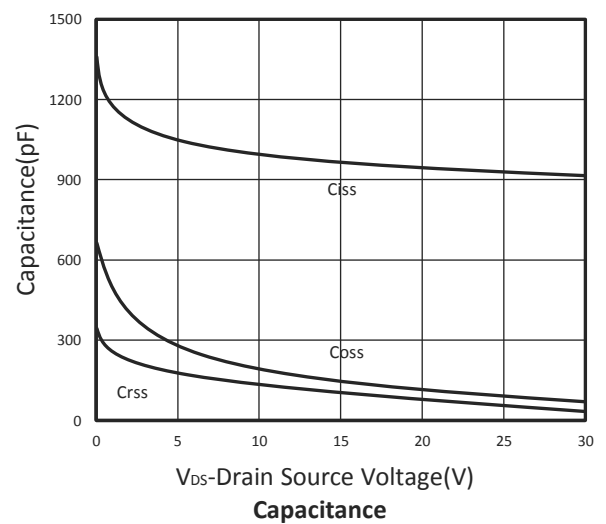
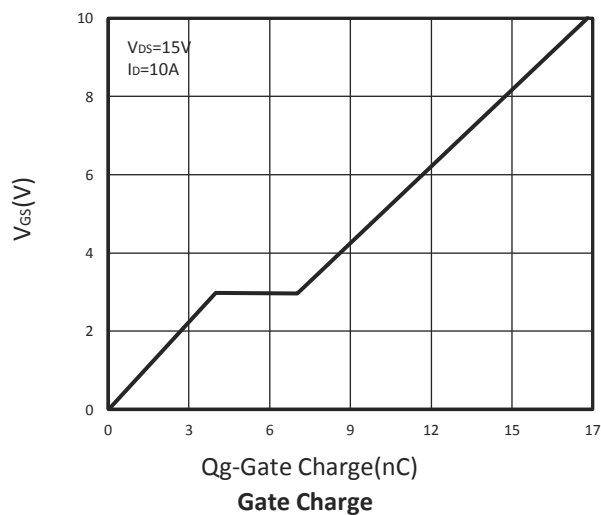
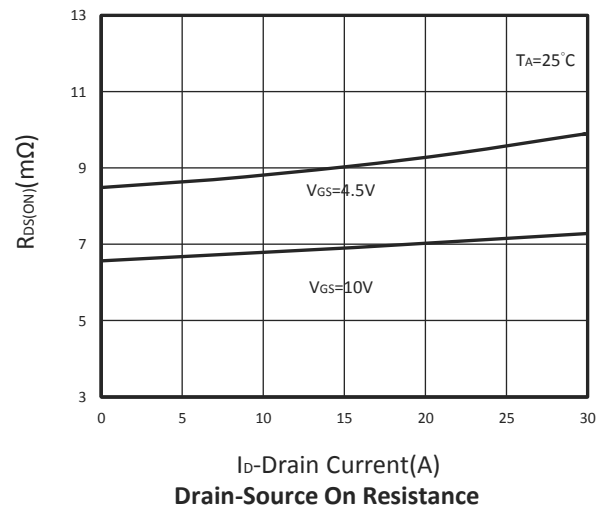
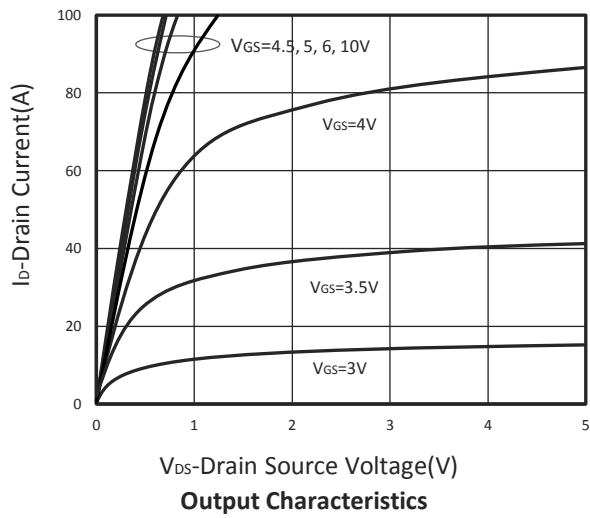
Symbol	Parameter	Condition	Min	Typ	Max	Unit
Static Parameters						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	30			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	1.2	1.5	2.5	V
I _{GSS}	Gate Leakage Current	V _{DS} =0V, V _{GS} =±20V			±100	nA
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =30V, V _{GS} =0V, T _J =25°C			1	μA
		V _{DS} =24V, V _{GS} =0V, T _J =75°C			10	
R _{DS(ON)}	Drain-source On-Resistance [Ⓟ]	V _{GS} =10V, I _D =20A V _{GS} =4.5V, I _D =15A		7 9	8.5 12	mΩ
G _{fs}	Forward Transconductance	V _{DS} =5V, I _D =10A		14		S
Diode Characteristics						
V _{SD}	Diode Forward Voltage [Ⓟ]	I _S =1A, V _{GS} =0V			1	V
I _S	Diode Continuous Forward Current				53	A
t _{rr}	Reverse Recovery Time	I _S =10A, dI/dt=100A/μs		9		ns
Q _{rr}	Reverse Recovery Charge			3.2		nC
Dynamic and Switching Parameters [Ⓕ]						
Q _g	Total Gate Charge	V _{DS} =15V, V _{GS} =10V, I _D =10A		16.9		nC
Q _g	Total Gate Charge (4.5V)			7.9		
Q _{gs}	Gate-Source Charge			3.7		
Q _{gd}	Gate-Drain Charge			3.2		
C _{iSS}	Input Capacitance	V _{DS} =15V, V _{GS} =0V, f=1MHz		955		pF
C _{oSS}	Output Capacitance			138		
C _{rSS}	Reverse Transfer Capacitance			110		
R _g	Gate Resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		2.5		Ω
t _{d(on)}	Turn-On Time	V _{DD} =15V, V _{GS} =10V, R _G =3.3Ω I _D =1A		5	8.7	nS
t _r				9.6	18	
t _{d(off)}	Turn-Off Time			28	53	
t _f				8	15	

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

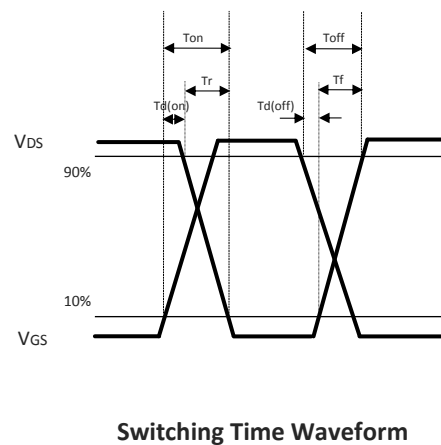
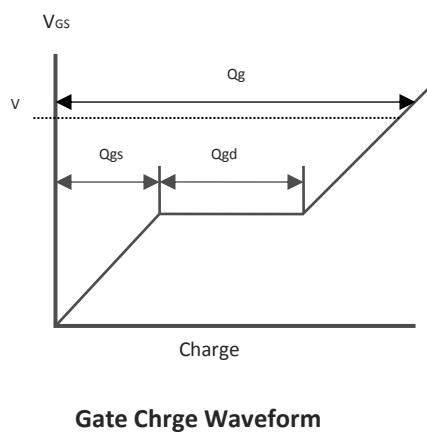
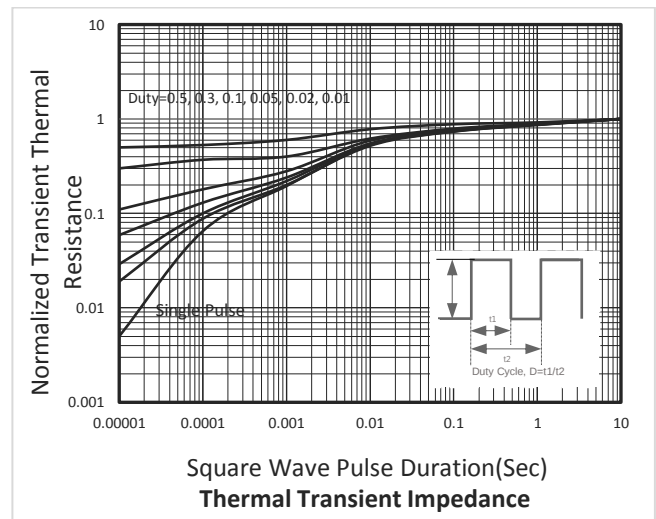
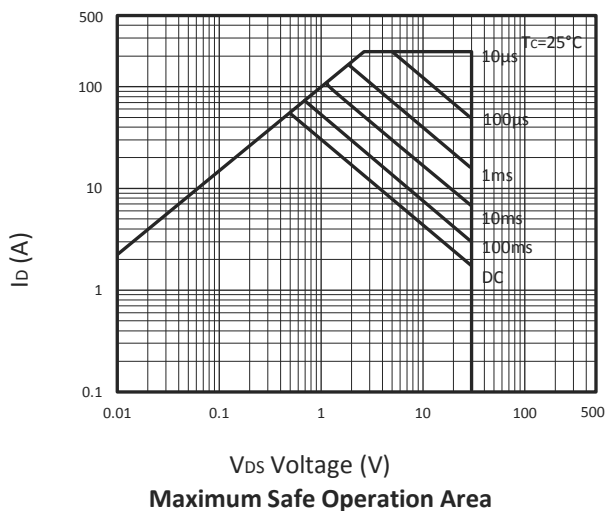
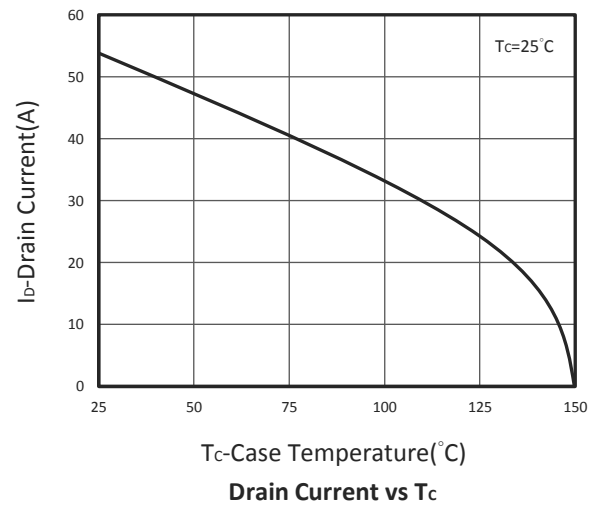
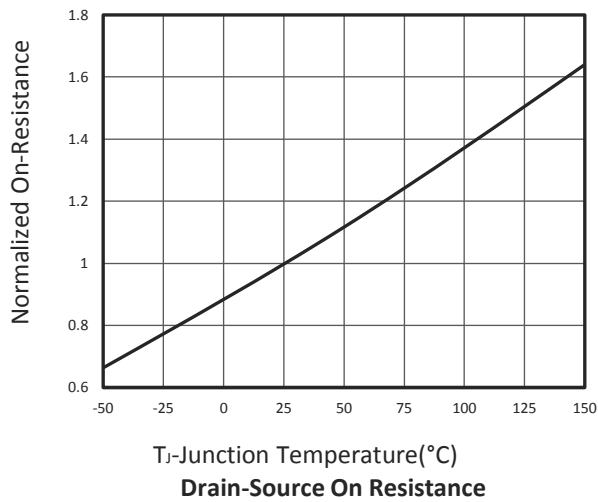
- A. Surface mounted on FR4 board using 1 in² pad size.
- B. Pulsed width limited by maximum junction temperature, T_{J(MAX)}=150°C (initial temperature T_J=25°C).
- C. Using ≤ 10s junction-to-ambient thermal resistance is base on T_{J(MAX)}=150°C.
- D. Pulse test width ≤300μs and duty cycle ≤ 2%.
- E. The EAS data shows maximum, The test condition is V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=30A
- F. Guaranteed by design, not subject to production testing.
- *. The maximum rating current limited by package.

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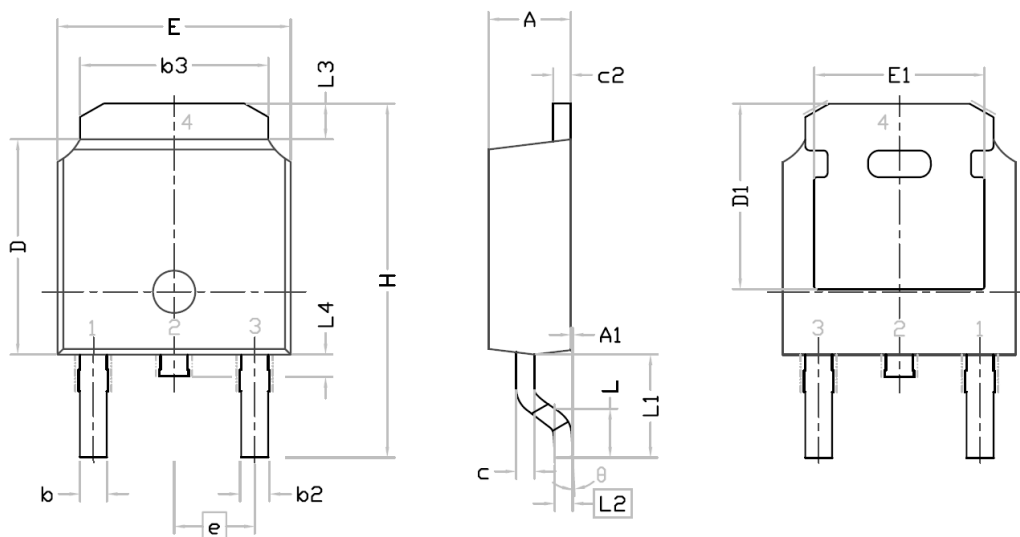
TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS



TO-252 PACKAGE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.200	2.380	0.087	0.094
A1	0.000	0.127	0.000	0.005
b	0.640	0.880	0.025	0.035
b2	0.770	1.140	0.030	0.045
b3	5.210	5.460	0.205	0.215
c	0.460	0.600	0.018	0.024
c2	0.460	0.580	0.018	0.023
D	6.000	6.223	0.236	0.245
D1	5.210	-	0.205	-
E	6.400	6.731	0.252	0.265
E1	4.400	-	0.173	-
e	2.286 BSC.		0.090 BSC.	
H	9.400	10.40	0.370	0.409
L	1.400	1.770	0.055	0.070
L1	2.743 REF.		0.108 REF.	
L2	0.508 BSC.		0.020 BSC.	
L3	0.890	1.270	0.035	0.050
L4	0.640	1.010	0.025	0.040
Θ	0°	10°	0°	10°

Recommended Land Pattern

