

Single P-Channel MOSFET

■ DESCRIPTION

SMC5225 is the P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior, fast switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

■ PART NUMBER INFORMATION

SMC 5225 H - TR G

a : Company name.

b : Product Serial number.

c : Package code H:TO-252

d : Handling code TR:Tape&Reel

e : Green produce code G:*RoHS Compliant*

■ FEATURES

$V_{DS} = -30V, I_D = -22A$

$R_{DS(ON)}=38m\Omega(\text{Typ.}) @ V_{GS}=-10V$

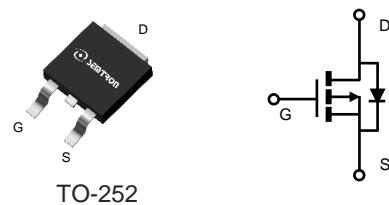
$R_{DS(ON)}=50m\Omega(\text{Typ.}) @ V_{GS}=-4.5V$

◆ High power and current handling capability

■ APPLICATIONS

◆ LED Application

◆ Power Management



■ ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless otherwise noted)

| Symbol | Parameter | Rating | Units |
|-----------|---|-------------------------|------------------|
| V_{DSS} | Drain-Source Voltage | -30 | V |
| V_{GSS} | Gate-Source Voltage | ± 20 | V |
| I_D | Continuous Drain Current | $T_c=25^\circ\text{C}$ | -22 |
| | | $T_c=100^\circ\text{C}$ | -14 |
| I_{DM} | Pulsed Drain Current ^A | -80 | A |
| I_D | Continuous Drain Current | $T_A=25^\circ\text{C}$ | -9.2 |
| | | $T_A=70^\circ\text{C}$ | -7.3 |
| P_D | Power Dissipation ^B | $T_A=25^\circ\text{C}$ | 2.5 |
| | | $T_A=70^\circ\text{C}$ | 1.6 |
| I_{AS} | Avalanche Current ^A | -15 | A |
| E_{AS} | Single Pulse Avalanche energy L=0.3mH ^{AF} | 33.5 | mJ |
| P_D | Power Dissipation ^C | $T_c=25^\circ\text{C}$ | 35 |
| | | $T_c=100^\circ\text{C}$ | 14 |
| T_J | Operation Junction Temperature | -55/150 | $^\circ\text{C}$ |
| T_{STG} | Storage Temperature Range | -55/150 | $^\circ\text{C}$ |

■ THERMAL RESISTANCE

| Symbol | Parameter | Typ | Max | Units |
|-----------------|--|---------------------|-----|--------------------|
| $R_{\theta JA}$ | Thermal Resistance Junction to Ambient ^B | $t \leq 10\text{s}$ | 20 | $^\circ\text{C/W}$ |
| | Thermal Resistance Junction to Ambient ^{BD} | Steady-State | 50 | |
| $R_{\theta JC}$ | Thermal Resistance Junction to Case | | 3.5 | |

ELECTRICAL CHARACTERISTICS($T_A = 25^\circ\text{C}$ Unless otherwise noted)

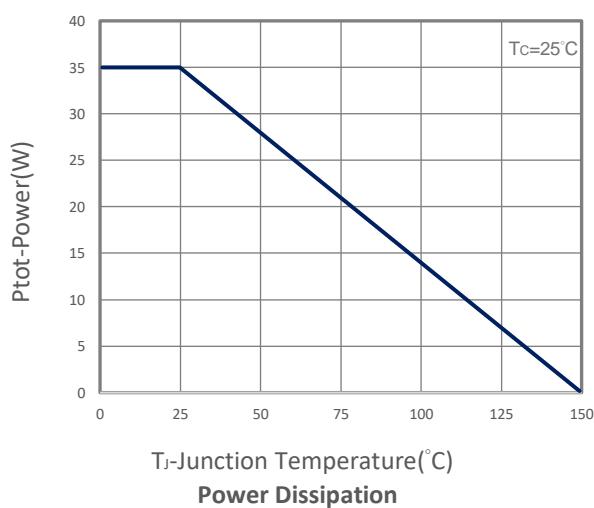
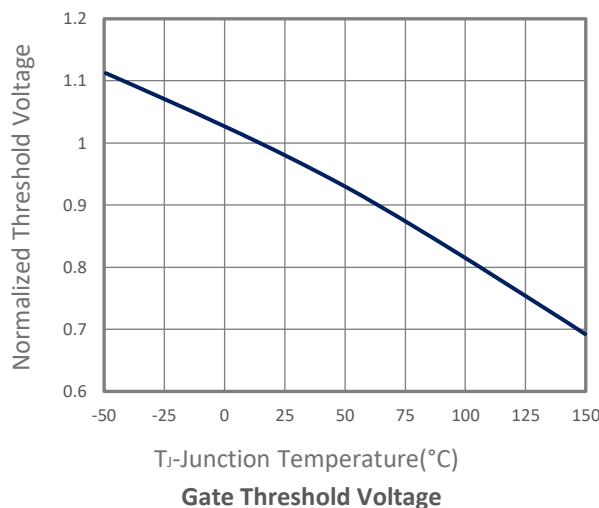
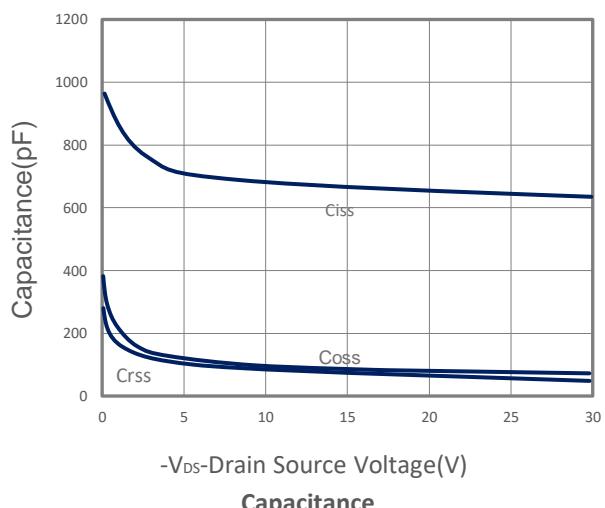
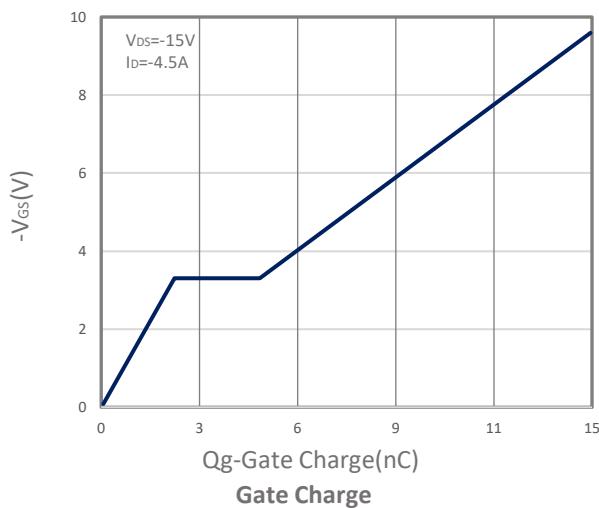
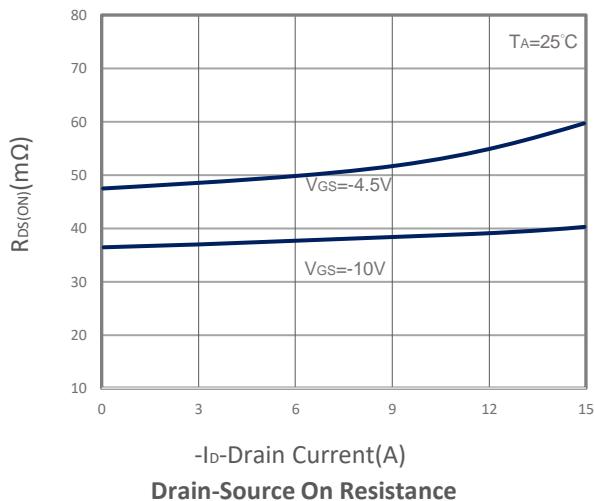
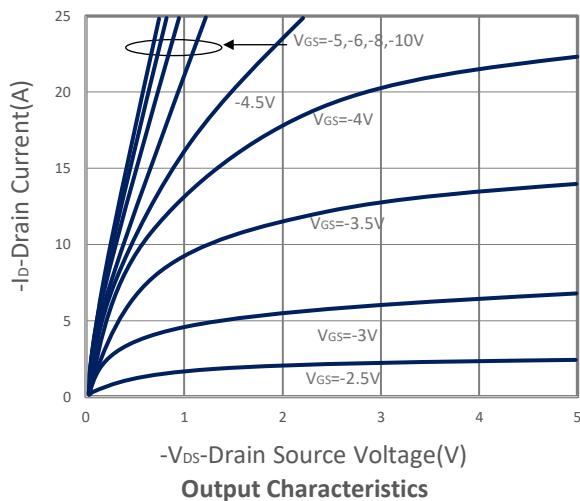
| Symbol | Parameter | Condition | Min | Typ | Max | Unit | |
|---|---|--|-----|------|-----------|------------------|--|
| Static Parameters | | | | | | | |
| BV_{DSS} | Drain-Source Breakdown Voltage | $V_{GS}=0\text{V}, I_D=-250\mu\text{A}$ | -30 | | | V | |
| $V_{GS(\text{th})}$ | Gate Threshold Voltage | $V_{DS}=V_{GS}, I_D=-250\mu\text{A}$ | -1 | -1.5 | -2.5 | V | |
| I_{GSS} | Gate Leakage Current | $V_{DS}=0\text{V}, V_{GS}=\pm 20\text{V}$ | | | ± 100 | nA | |
| I_{DSS} | Zero Gate Voltage Drain Current | $V_{DS}=-30\text{V}, V_{GS}=0\text{V}, T_J=25^\circ\text{C}$ | | | -1 | μA | |
| | | $V_{DS}=-24\text{V}, V_{GS}=0\text{V}, T_J=75^\circ\text{C}$ | | | -10 | | |
| $R_{DS(\text{ON})}$ | Drain-source On-Resistance ^E | $V_{GS}=-10\text{V}, I_D=-9.2\text{A}$ | | 38 | 45 | $\text{m}\Omega$ | |
| | | $V_{GS}=-4.5\text{V}, I_D=-7\text{A}$ | | 50 | 60 | | |
| G_f | Forward Transconductance | $V_{DS}=-10\text{V}, I_D=-6.6\text{A}$ | | 22 | | S | |
| Diode Characteristics | | | | | | | |
| V_{SD} | Diode Forward Voltage ^E | $I_S=-1\text{A}, V_{GS}=0\text{V}$ | | -0.7 | -1 | V | |
| I_S | Continuous Source Current | | | | -4.6 | A | |
| Dynamic and Switching Parameters | | | | | | | |
| Q_g | Total Gate Charge (10V) | $V_{DS}=-15\text{V}, V_{GS}=-10\text{V}, I_D=-4.5\text{A}$ | | 15.8 | 22.1 | nC | |
| Q_g | Total Gate Charge (4.5V) | | | 7.7 | 10.8 | | |
| Q_{gs} | Gate-Source Charge | | | 2.3 | 3.2 | | |
| Q_{gd} | Gate-Drain Charge | | | 2.6 | 3.6 | | |
| C_{iss} | Input Capacitance | $V_{DS}=-15\text{V}, V_{GS}=0\text{V}, f=1\text{MHz}$ | | 645 | | pF | |
| C_{oss} | Output Capacitance | | | 63 | | | |
| C_{rss} | Reverse Transfer Capacitance | | | 52 | | | |
| $t_{d(on)}$ | Turn-On Time | $V_{DD}=-15\text{V}, V_{GEN}=-10\text{V}, R_G=3.3\Omega, I_D=-1\text{A}$ | | 6 | 11 | nS | |
| t_r | | | | 12 | 23 | | |
| $t_{d(off)}$ | Turn-Off Time | | | 25.5 | 48 | | |
| t_f | | | | 7 | 13 | | |

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

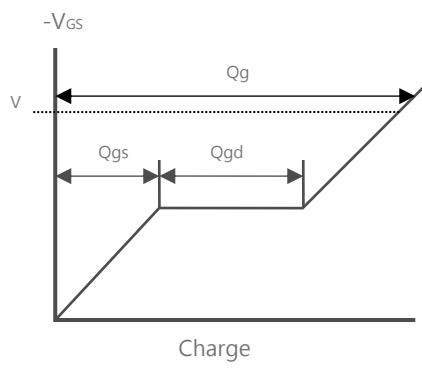
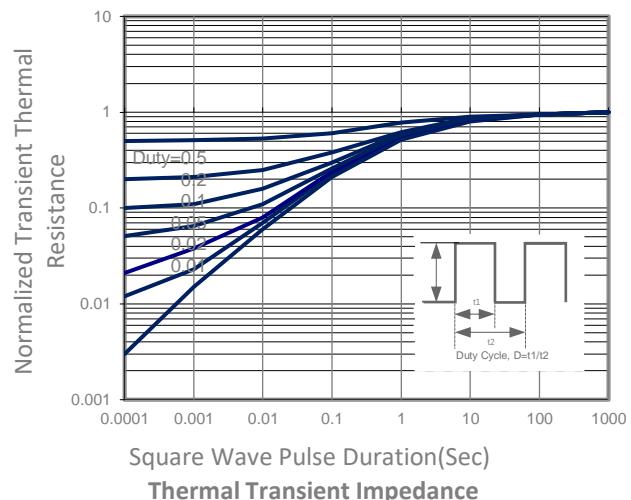
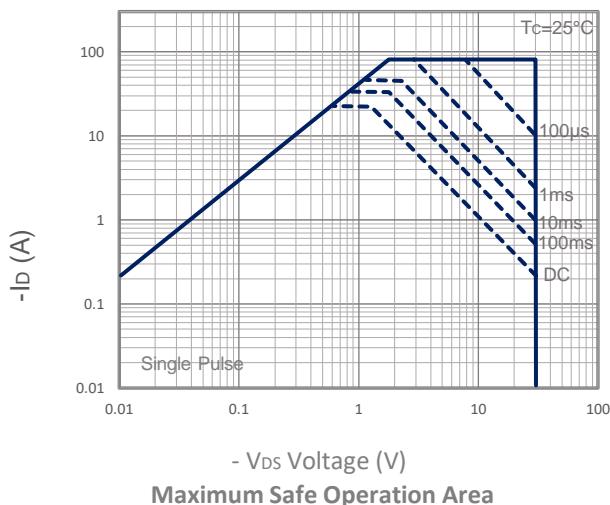
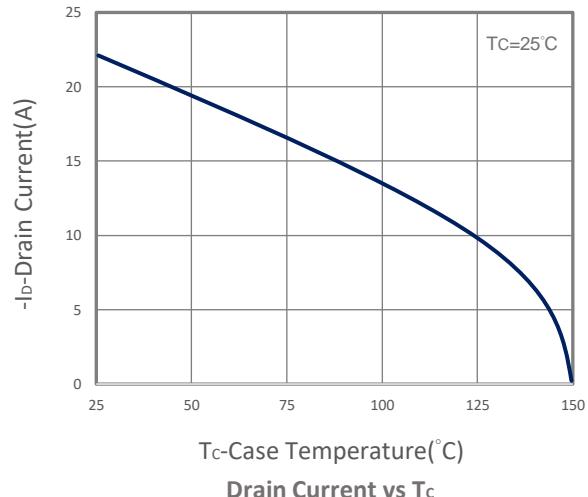
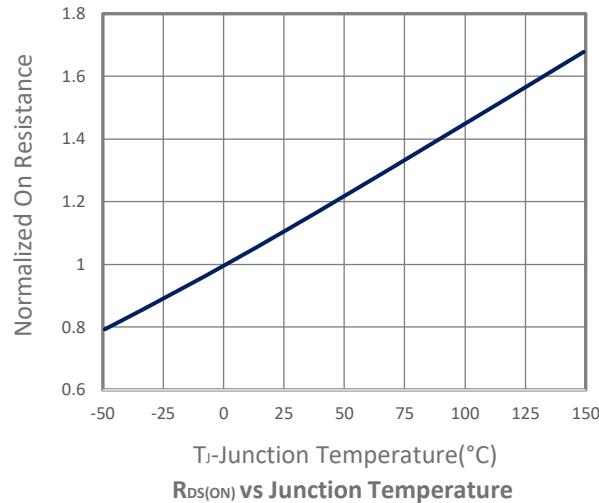
- A. Pulsed width limited by maximum junction temperature, $T_J(\text{MAX})=150^\circ\text{C}$.
- B. The value of $R_{\theta JA}$ is measured with the device mounted on 1in2 FR-4 board in a still air environment with maximum junction temperature $T_J(\text{MAX})=150^\circ\text{C}$ (initial temperature $T_A=25^\circ\text{C}$).
- C. $T_J(\text{MAX})=150^\circ\text{C}$, using junction-to-ambient thermal resistance, $t \leq 10\text{sec}$.
- D. $T_J(\text{MAX})=150^\circ\text{C}$, using junction-to-case thermal resistance ($R_{\theta JC}$) is more useful in additional heat sinking is used.
- E. The data tested by pulsed, pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
- F. The EAS data shows Max, tested and pulse width limited by $T_J(\text{MAX})=150^\circ\text{C}$ (initial temperature $T_J=25^\circ\text{C}$).

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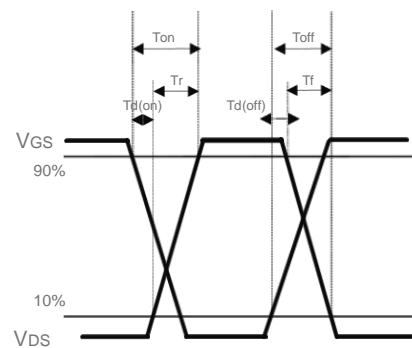
■ TYPICAL CHARACTERISTICS



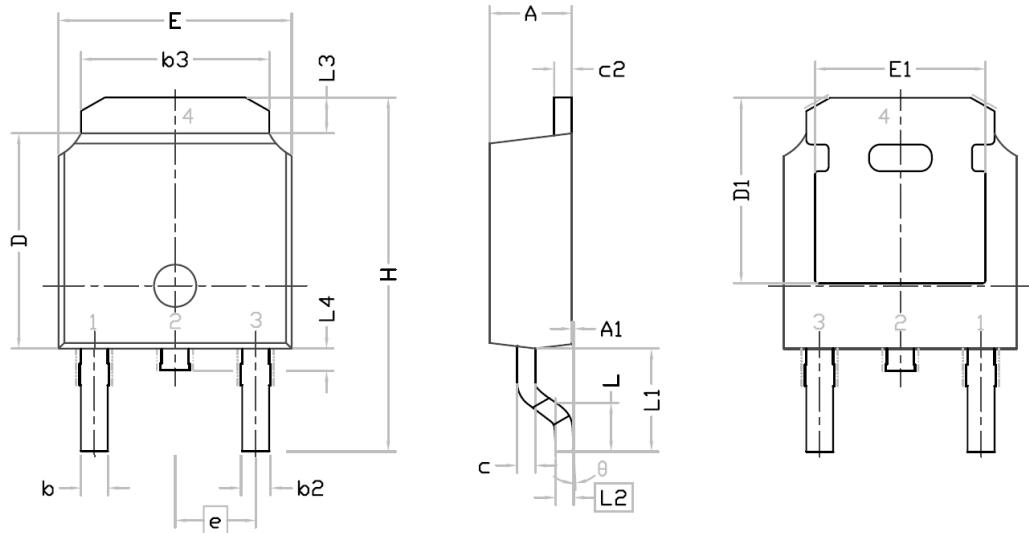
TYPICAL CHARACTERISTICS



Gate Charge Waveform



Switching Time Waveform

TO-252 PACKAGE DIMENSIONS


| Symbol | Dimensions In Millimeters | | Dimensions In Inches | |
|--------|---------------------------|-------|----------------------|-------|
| | Min. | Max. | Min. | Max. |
| A | 2.200 | 2.380 | 0.087 | 0.094 |
| A1 | 0.000 | 0.127 | 0.000 | 0.005 |
| b | 0.640 | 0.880 | 0.025 | 0.035 |
| b2 | 0.770 | 1.140 | 0.030 | 0.045 |
| b3 | 5.210 | 5.460 | 0.205 | 0.215 |
| c | 0.460 | 0.600 | 0.018 | 0.024 |
| c2 | 0.460 | 0.580 | 0.018 | 0.023 |
| D | 6.000 | 6.223 | 0.236 | 0.245 |
| D1 | 5.210 | - | 0.205 | - |
| E | 6.400 | 6.731 | 0.252 | 0.265 |
| E1 | 4.400 | - | 0.173 | - |
| e | 2.286 BSC. | | 0.090 BSC. | |
| H | 9.400 | 10.40 | 0.370 | 0.409 |
| L | 1.400 | 1.770 | 0.055 | 0.070 |
| L1 | 2.743 REF. | | 0.108 REF. | |
| L2 | 0.508 BSC. | | 0.020 BSC. | |
| L3 | 0.890 | 1.270 | 0.035 | 0.050 |
| L4 | 0.640 | 1.010 | 0.025 | 0.040 |
| θ | 0° | 10° | 0° | 10° |

Recommended Land Pattern

