

■ DESCRIPTION

SMC6276LESN used trench technology are well suited for high efficiency fast switching applications, this MOSFET has been designed to minimize the on-state resistance and yet maintain superior switching performance, this devices are well suited for applications in the small surface mount package.

■ PART NUMBER INFORMATION

SMC 6276L E SN - TR G

a	b	c	d	e	f
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a : Company name.
 b : Product Serial number.
 c : ESD Protection
 d : Package code SN: SOT-23
 e : Handling code TR: Tape&Reel
 f : Green produce code G: RoHS Compliant

■ FEATURES

V_{DS}=60V, I_D=0.45A

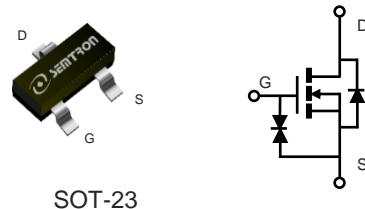
R_{DS(ON)}=1.6Ω(Typ.)@V_{GS}=10V

R_{DS(ON)}=2.0Ω(Typ.)@V_{GS}=4.5V

- ◆ Extremely Low Threshold Voltage
- ◆ Low Input Capacitance
- ◆ ESD Protection >±2KV

■ APPLICATIONS

- ◆ Portable Appliances
- ◆ High speed switch
- ◆ Battery management



■ ABSOLUTE MAXIMUM RATINGS (T_A=25°C Unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DSS}	Drain-Source Voltage	60	V
V _{GSS}	Gate-Source Voltage	±20	V
I _D	Continuous Drain Current	0.45	A
	T _A =25°C	0.45	
	T _A =70°C	0.36	
I _{DM}	Pulsed Drain Current ^B	1.8	A
P _D	Power Dissipation ^A	0.83	W
	T _A =25°C	0.83	
	T _A =70°C	0.53	
T _J	Operation Junction Temperature	-55/150	°C
T _{STG}	Storage Temperature Range	-55/150	°C

■ THERMAL RESISTANCE

Symbol	Parameter	Typ	Max	Units
R _{θJA}	Thermal Resistance Junction to Ambient ^A	t≤10s	150	°C/W
	Thermal Resistance Junction to Ambient ^{AC}	Steady-State	300	°C/W

ELECTRICAL CHARACTERISTICS($T_A=25^\circ\text{C}$ Unless otherwise noted)

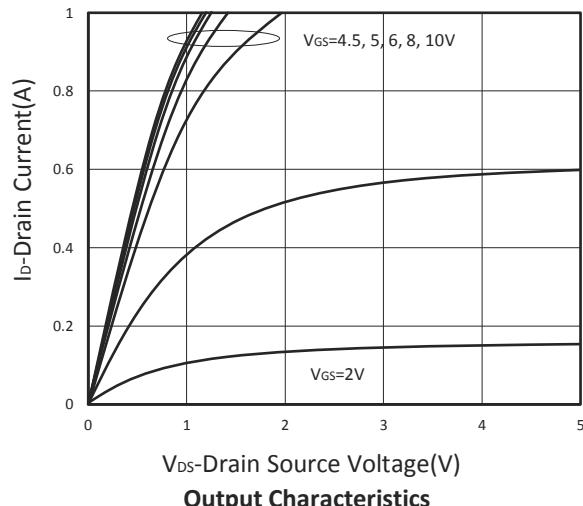
Symbol	Parameter	Condition	Min	Typ	Max	Unit	
Static Parameters							
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$, $I_D=250\mu\text{A}$	60			V	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_D=250\mu\text{A}$	0.7	1	1.2	V	
I_{GSS}	Gate Leakage Current	$V_{DS}=0\text{V}$, $V_{GS}=\pm 20\text{V}$			± 10	μA	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS}=60\text{V}$, $V_{GS}=0\text{V}$, $T_J=25^\circ\text{C}$			1	μA	
		$V_{DS}=48\text{V}$, $V_{GS}=0\text{V}$, $T_J=85^\circ\text{C}$			30		
$R_{DS(\text{ON})}$	Drain-source On-Resistance ^D	$V_{GS}=10\text{V}$, $I_D=0.45\text{A}$		1.6	2.0	Ω	
		$V_{GS}=4.5\text{V}$, $I_D=0.3\text{A}$		2.0	2.5		
		$V_{GS}=2.5\text{V}$, $I_D=0.1\text{A}$		4.0	5.5		
Diode Characteristics							
V_{SD}	Diode Forward Voltage ^D	$I_S=0.2\text{A}$, $V_{GS}=0\text{V}$			1.3	V	
I_S	Diode Continuous Forward Current				0.23	A	
Dynamic and Switching Parameters ^E							
Q_g	Total Gate Charge	$V_{DS}=30\text{V}$, $V_{GS}=10\text{V}$ $I_D=0.4\text{A}$		1.28		nC	
Q_{gs}	Gate-Source Charge			0.22			
Q_{gd}	Gate-Drain Charge			0.1			
C_{iss}	Input Capacitance	$V_{DS}=30\text{V}$, $V_{GS}=0\text{V}$, $f=1\text{MHz}$		28		pF	
C_{oss}	Output Capacitance			7.6			
C_{rss}	Reverse Transfer Capacitance			6.4			
$t_{d(on)}$	Turn-On Time	$V_{DD}=30\text{V}$, $V_{GS}=10\text{V}$, $R_G=3.9\Omega$, $I_D=0.4$		3.3	6.3	nS	
t_r				3.9	7.4		
$t_{d(off)}$	Turn-Off Time			8.8	16.7		
t_f				12	22.8		

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged.

- A. Surface mounted on FR4 board using 1 in² pad size.
- B. Pulsed width limited by maximum junction temperature, $T_J(\text{MAX})=150^\circ\text{C}$.
- C. Using $\leq 10\text{s}$ junction-to-ambient thermal resistance is base on $T_J(\text{MAX})=150^\circ\text{C}$.
- D. Pulse test width $\leq 300\mu\text{s}$ and duty cycle $\leq 2\%$.
- E. Guaranteed by design, not subject to production testing.

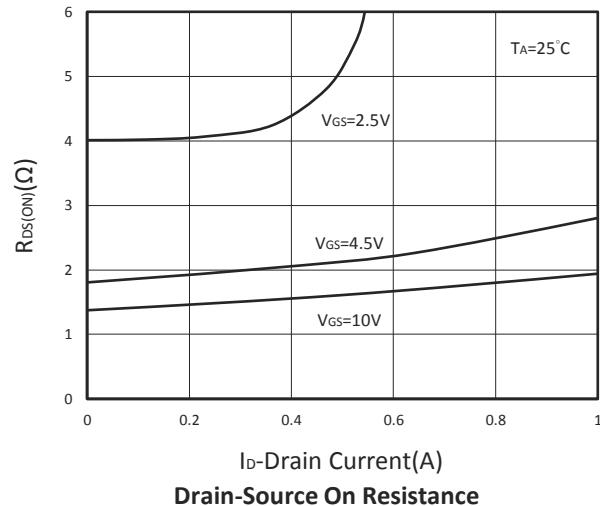
The products and product specifications contained herein are subject to change without notice to improve performance characteristics. Consult us, or our representatives before use, to confirm that the information in this datasheet is up to date. We assume no responsibility for any infringement of patents, patent rights, or other rights arising from the use of any information and circuitry in this datasheet.

■ TYPICAL CHARACTERISTICS



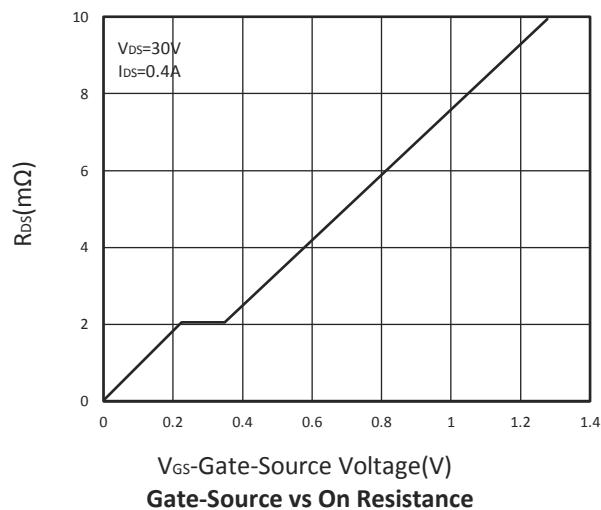
V_{DS} -Drain Source Voltage(V)

Output Characteristics



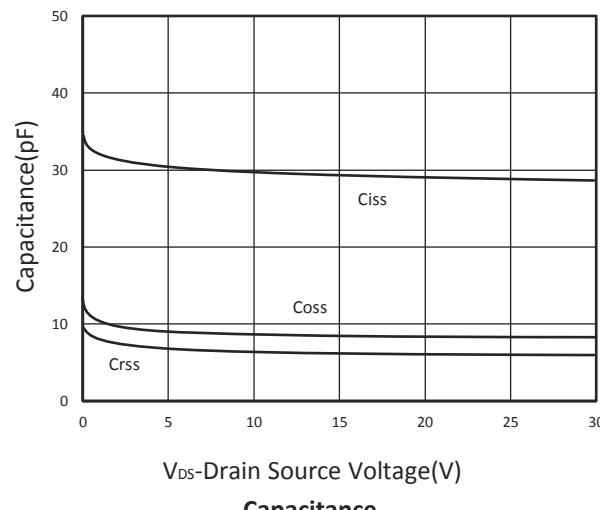
I_D -Drain Current(A)

Drain-Source On Resistance



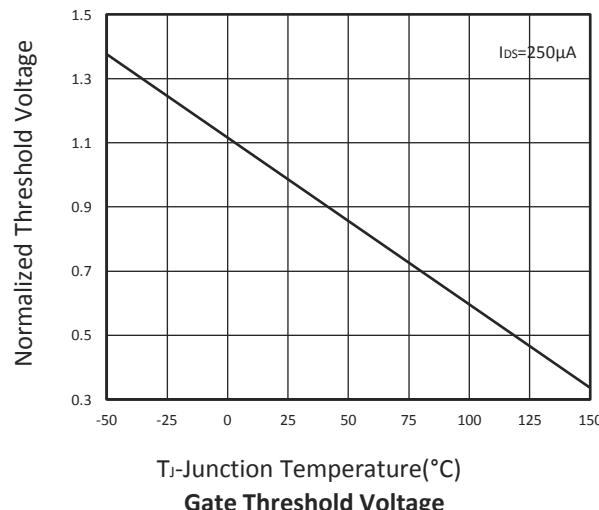
V_{GS} -Gate-Source Voltage(V)

Gate-Source vs On Resistance



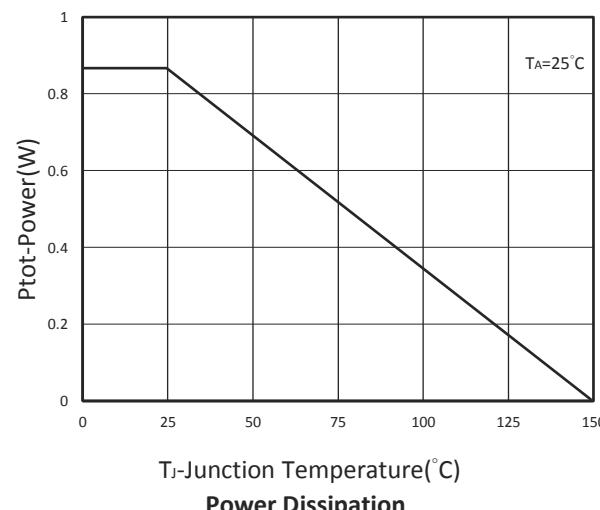
V_{DS} -Drain Source Voltage(V)

Capacitance



T_J -Junction Temperature(°C)

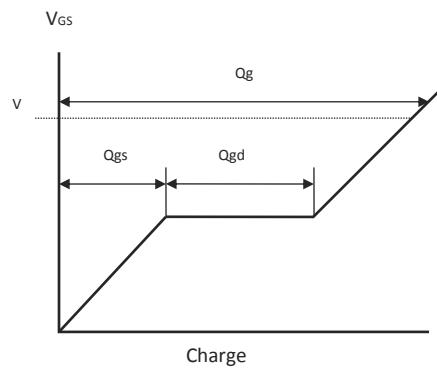
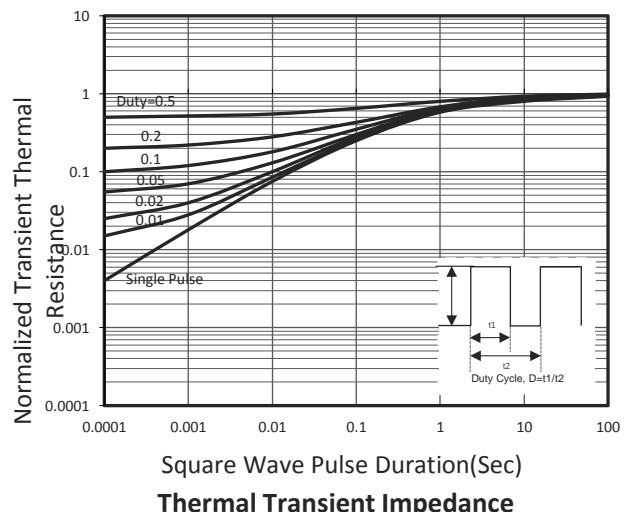
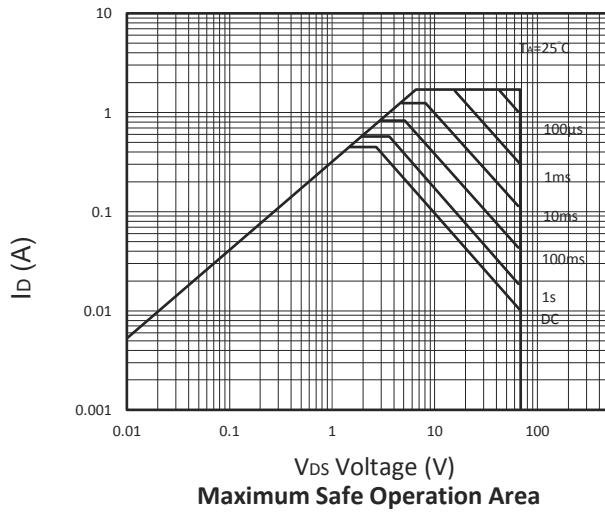
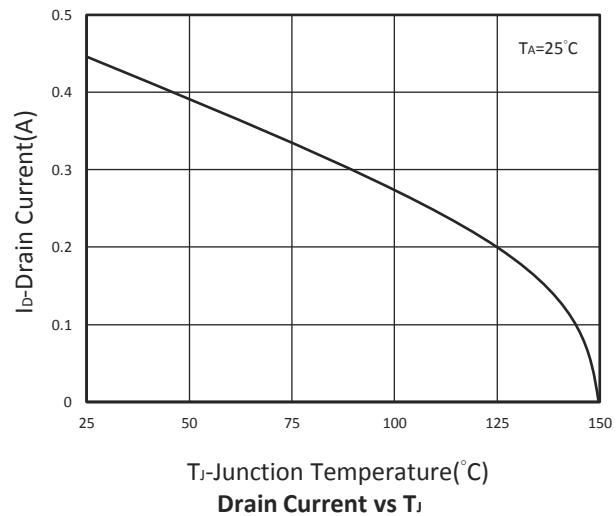
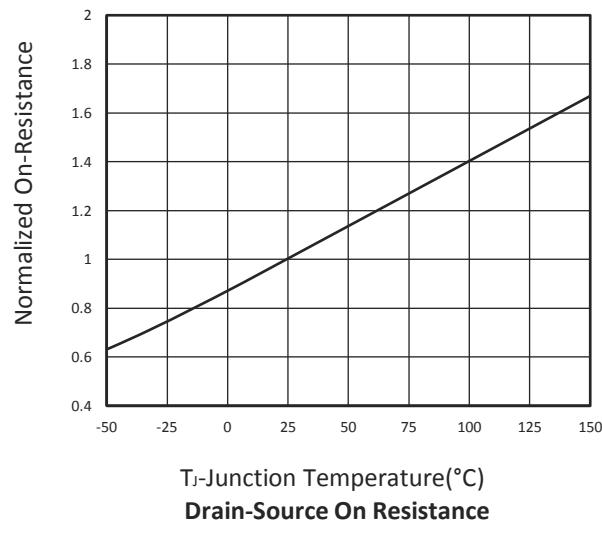
Gate Threshold Voltage



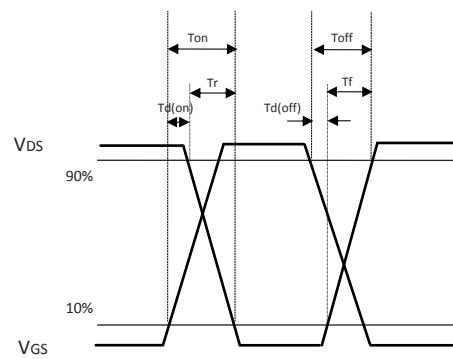
T_J -Junction Temperature(°C)

Power Dissipation

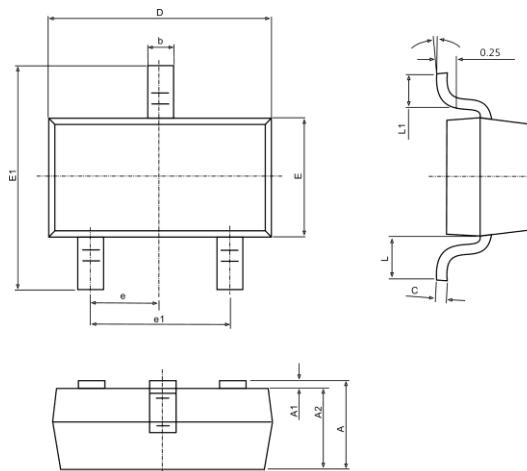
TYPICAL CHARACTERISTICS



Gate Charge Waveform

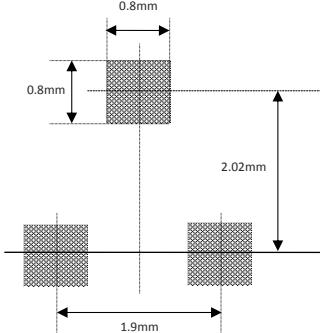


Switching Time Waveform

SOT-23 PACKAGE DIMENSIONS


1.

Recommended Land Pattern



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 BSC.		0.037 BSC	
e1	1.800	2.000	0.071	0.079
L	0.550 BSC		0.022 BSC	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°