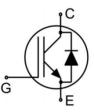


Main Product Characteristics:

VCES	1250V
lc	40A
V _{CE(sat)}	1.8V





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Schematic Diagram

Features and Benefits:

- Trench FS technology offering
- High speed switching
- Low gate charge and V_{CE(sat)}
- High ruggedness, temperature stable behavior
- Maximum junction temperature 175°C



Applications:

- Solar Inverters
- Uninterruptible power supplies
- Motor drives
- Air condition

Absolute Max Rating:

Symbol	Parameter	Value	Units
Vces	Collector-Emitter Voltage	1250	V
V _{GES}	Gate- Emitter Voltage	±30	V
	Collector Current	80	
lc	Collector Current @T _c = 100 °C	40	
I _{Cpuls}	Pulsed Collector Current, t _p limited by T _{jmax}	160	
-	Turn off safe operating area, V_{CE} =1200V, T _J =175°C	160	- A
lF	Diode Continuous Forward Current @Tc = 100 °C	40	
Іғм	Diode Maximum Forward Current	160	
0	Power Dissipation @ T _c = 25°C	468	W
PD	Power Dissipation @ T _c = 100°C	234	W
T _J T _{STG}	Operating Junction and Storage Temperature Range	-55 to +175	°C
T∟	Maximum Temperature for Soldering	260	°C



Thermal Resistance

Symbol	Characterizes	Тур.	Max.	Units
	Thermal Resistance, Junction-to-case for IGBT	_	0.32	°C/W
R _{ejc}	Thermal Resistance, Junction-to-case for Diode		0.61	°C/W
R _{0JA}	Thermal Resistance, Junction-to-ambient		40	°C/W

Electrical Characteristics @T_A=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions	
V(BR)CES	Collector-Emitter Breakdown Voltage	1250		_	V	Vge=0V,Ice=1mA	
M	Collector Erritter Coturation Valterra	_	1.8	2		Ic=40A ,Vge=15V @Tj=25°C	
VCE(sat)	Collector-Emitter Saturation Voltage	_	2.25	_	V	Ic=40A ,Vge=15V @Tj=175°C	
VGE(th)	Gate Threshold Voltage	4.5	_	6	V	Ic=1.9mA,Vce=Vge	
Ices	Collector-Emitter Leakage Current	—	—	200	μA	Vge =0V,Vce=1200V	
lana	Coto to Emitter Deverse Lookege	—	—	200	nA	VGE=25V,VCE =0V	
Iges	Gate to Emitter Reverse Leakage		_	-200	nA	VGE=-25V,VCE =0V	
Cies	Input capacitance	—	4700	—		V _{GS} = 0V	
Coes	Output capacitance	—	106	—	pF	V _{DS} = 50V	
Cres	Reverse transfer capacitance	—	66	—		f = 1MHz	
t _{d(on)}	Turn-on delay time	_	40			V _{CC} =600V,	
tr	Rise time		23	_	-	V_{GE} =0.0/15.0V, R _G =10.0Ω, L _σ =70nH, C _σ =67pF	
t _{d(off)}	Turn-Off delay time		350		ns		
t _f	Fall time		50		-		
Eon	Turn-On Switching Loss		2.2			V _{cc} =600V,	
	· · · · · · · · · · · · · · · · · · ·				-	V _{GE} =0.0/15.0V,	
Eoff	Turn-Off Switching Loss	_	1.8	_	mJ	R _G =10.0Ω,	
					-	L₀=70nH,	
Ets	Total Switching Loss	—	4			C _σ =67pF	
Qg	Total Gate Charge	_	238	_		Vcc=480V, Ic=40A,	
Qge	Gate to Emitter Charge	—	40	—	nC	VCC-460V, IC-40A, VGE=15V	
Qgc	Gate to Collector Charge	_	135	_		VGE-IJV	

Electrical Characteristics of the Diode@T_A=25°C unless otherwise specified

Symbol	Parameter	Min.	Тур.	Max.	Units	Conditions
Vfm	Diode Forward Voltage		2.3	3	V	I _F =40A,V _{GE} =0V
t _{rr}	Reverse Recovery Time		320	_	ns	
Qrr	Reverse Recovery Charge		2.6	_	μC	T」= 25°C, I⊧ =40A, di/dt =
Irrm	Diode Peak Reverse Recovery		19	_	А	700A/µs
	Current					



Typical Electrical and Thermal Characteristics

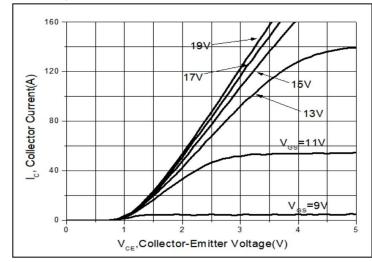


Figure1. Typical Output Characteristics

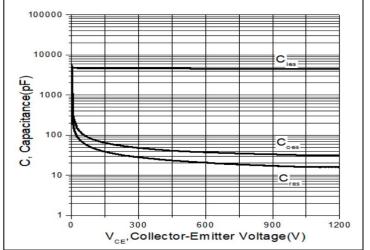


Figure3.Typical Capacitance

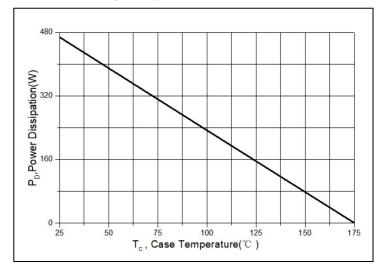


Figure 5. Power Dissipation vs. Case Temperature

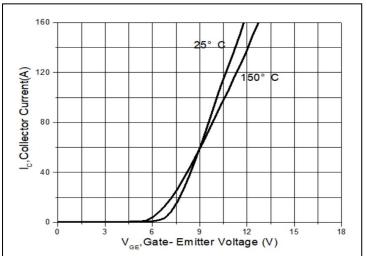


Figure 2. Typical Transfer Characteristics

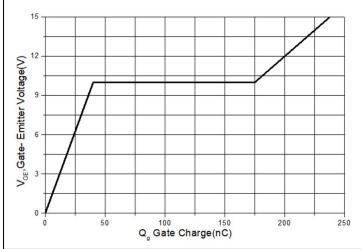
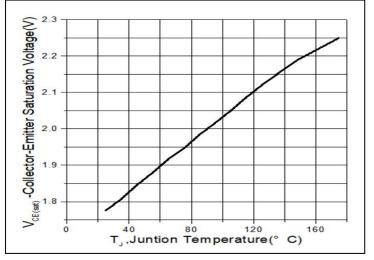
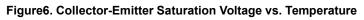


Figure4. Typical Gate Charge







Typical Electrical and Thermal Characteristics

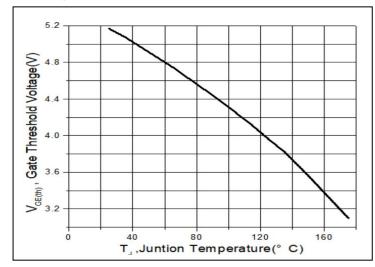
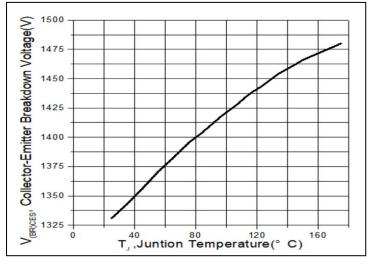
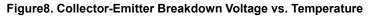


Figure7. Gate Threshold Voltage vs. Temperature

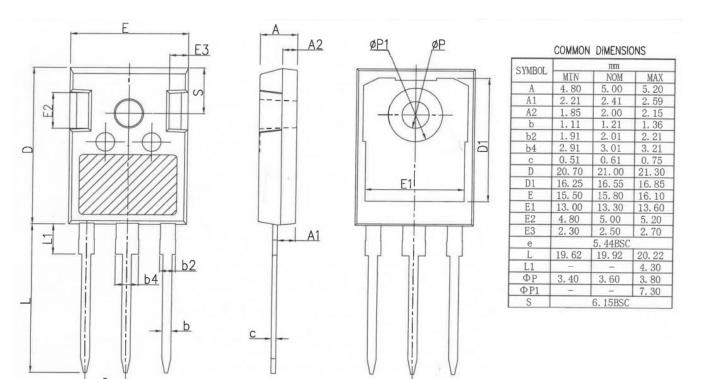






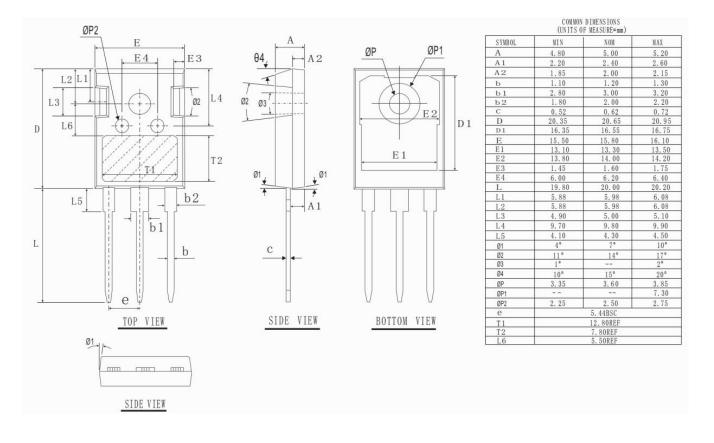
Mechanical Data:

Option1:





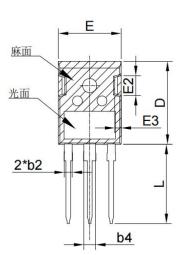
Option2:

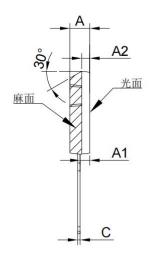


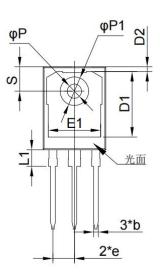


Option3:

Unit:mm







	Min	Тур	Max		Min	Тур	Max
Α	4.7	5.00	5.20	E1	13.2	13.5	13.8
A1	2.30	2.40	2.50	E2	4.90	5.00	5.10
A2	1.90	2.00	2.10	E3	1.50	1.60	1.70
b	1.10	1.20	1.30	е	5.34	5.44	5.54
b2	1.80	2.00	2.20	L	19.80	20.00	20.32
b4	2.80	3.00	3.20	L1		4.17	4.50
С	0.5	0.6	0.7	Р	3.50	3.60	3.70
D	20.8	20.95	21.1	P1	7.00	7.19	7.40
D1	16.25	16.55	16.85	S	6.04	6.15	6.3
D2	0.95	1.17	1.35				
E	15.48	15.88	16.28				





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