

SMG1332E

600mA, 20V, R_{DS(ON)}600mΩ

N-Channel Enhancement Mode Power Mos.FET

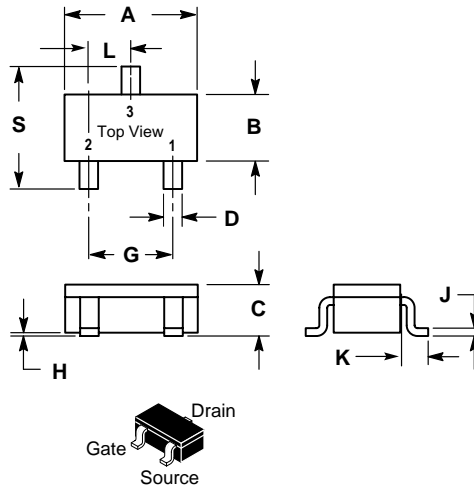
RoHS Compliant Product

Description

The SMG1332E provide the designer with best combination of fast switching, low on-resistance and cost-effectiveness.

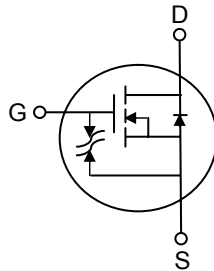
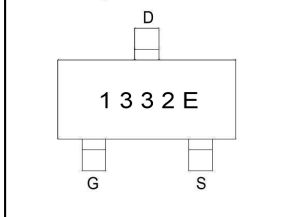
Features

- * Simple Gate Drive
- * 2KV ESD Rating (Per MIL-STD-883D)
- * Small Package Outline



SC-59		
Dim	Min	Max
A	2.70	3.10
B	1.40	1.60
C	1.00	1.30
D	0.35	0.50
G	1.70	2.10
H	0.00	0.10
J	0.10	0.26
K	0.20	0.60
L	0.85	1.15
S	2.40	2.80
All Dimension in mm		

Marking :



Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V _{DS}	20	V
Gate-Source Voltage	V _{GS}	±5	V
Continuous Drain Current ³	I _D @T _A =25°C	600	mA
Continuous Drain Current ³	I _D @T _A =70°C	470	mA
Pulsed Drain Current ^{1,2}	I _{DM}	2.5	A
Total Power Dissipation	P _D @T _A =25°C	1.0	W
Linear Derating Factor		0.008	W/°C
Operating Junction and Storage Temperature Range	T _j , T _{stg}	-55~+150	°C

Thermal Data

Parameter	Symbol	Ratings	Unit
Thermal Resistance Junction-ambient ³	R _{thj-a}	125	°C/W

Electrical Characteristics(T_j=25°C Unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Drain-Source Breakdown Voltage	BV _{DSS}	20	-	-	V	V _{GS} =0V, I _D =250uA
Breakdown Voltage Temp. Coefficient	ΔBV _{DSS} /ΔT _j	-	0.02	-	V/°C	Reference to 25°C, I _D =1mA
Gate Threshold Voltage	V _{GS(th)}	0.5	-	1.2	V	V _{DS} =V _{GS} , I _D =250uA
Gate-Source Leakage Current	I _{GSS}	-	-	±10	uA	V _{GS} =±5V
Drain-Source Leakage Current (T _j =25°C)	I _{DSS}	-	-	1	uA	V _{DS} =20V, V _{GS} =0
Drain-Source Leakage Current (T _j =70°C)		-	-	10	uA	V _{DS} =16V, V _{GS} =0
Static Drain-Source On-Resistance	R _{DS(ON)}	-	-	600	Ω	V _{GS} =4.5V, I _D =600mA
		-	-	1200		V _{GS} =2.5V, I _D =400mA
Total Gate Charge ²	Q _g	-	1.3	2	nC	I _D =600mA V _{DS} =16V V _{GS} =4.5V
Gate-Source Charge	Q _{gs}	-	0.3	-		
Gate-Drain ("Miller") Charge	Q _{gd}	-	0.5	-		
Turn-on Delay Time ²	T _{d(ON)}	-	4	-	nS	V _{DD} =10V I _D =600mA V _{GS} =10V R _G =3.3Ω R _D =16.7Ω
Rise Time	T _r	-	10	-		
Turn-off Delay Time	T _{d(OFF)}	-	15	-		
Fall Time	T _f	-	2	-		
Input Capacitance	C _{iss}	-	38	60	pF	V _{GS} =0V V _{DS} =10V f=1.0MHz
Output Capacitance	C _{oss}	-	17	-		
Reverse Transfer Capacitance	C _{rss}	-	12	-		
Forward Transconductance	G _{fs}	-	1	-	S	V _{DS} =5V, I _D =600mA

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Condition
Forward On Voltage ²	V _{DS}	-	-	1.2	V	I _S =300mA, V _{GS} =0V.

Notes: 1.Pulse width limited by Max. junction temperature.

2.Pulse width ≤300us, dutycycle ≤2%.

3.Surface mounted on FR4 board, t ≤10sec.

Characteristics Curve

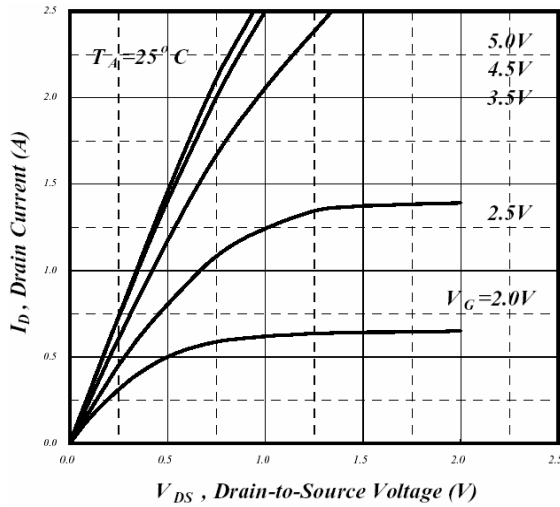


Fig 1. Typical Output Characteristics

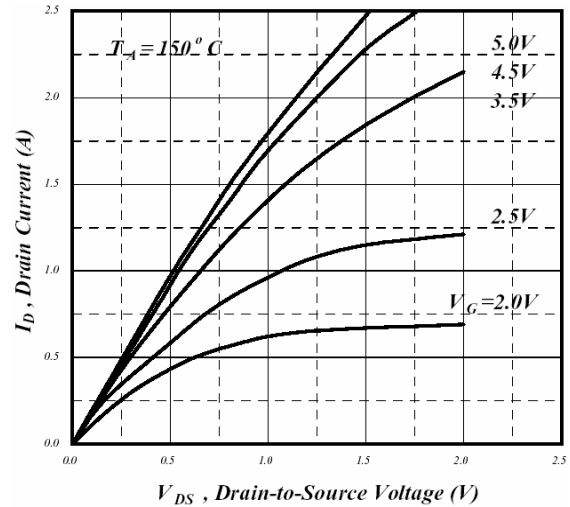


Fig 2. Typical Output Characteristics

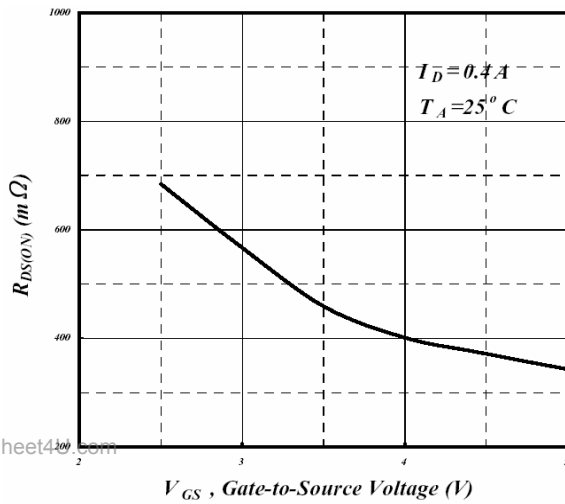


Fig 3. On-Resistance v.s. Gate Voltage

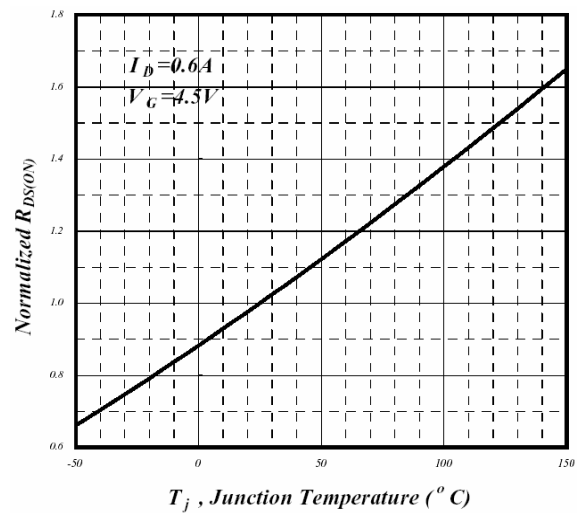


Fig 4. Normalized On-Resistance v.s. Junction Temperature

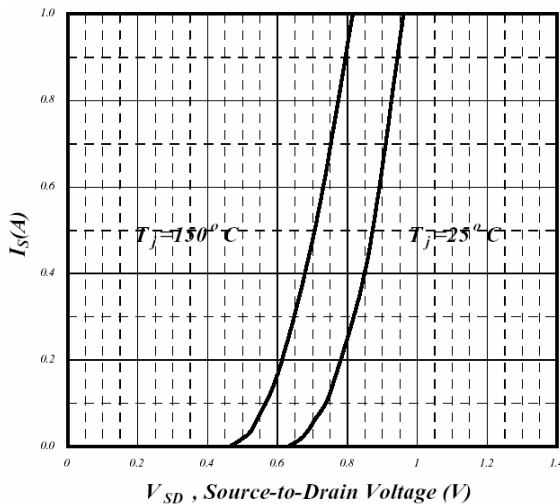


Fig 5. Forward Characteristics of Reverse Diode

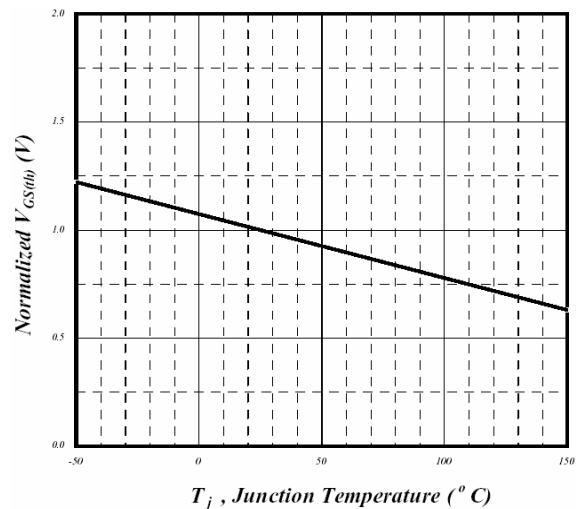


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

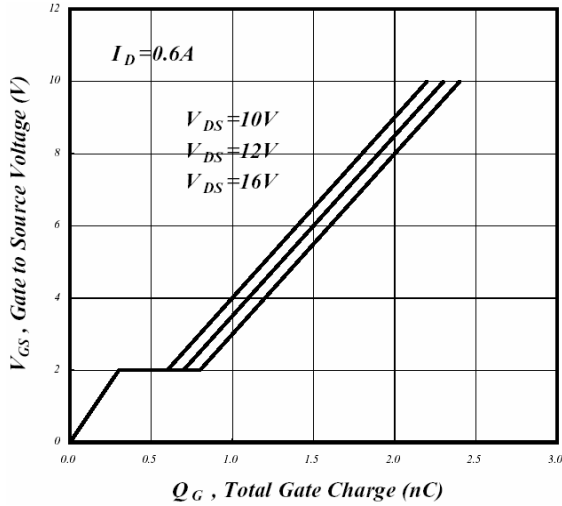


Fig 7. Gate Charge Characteristics

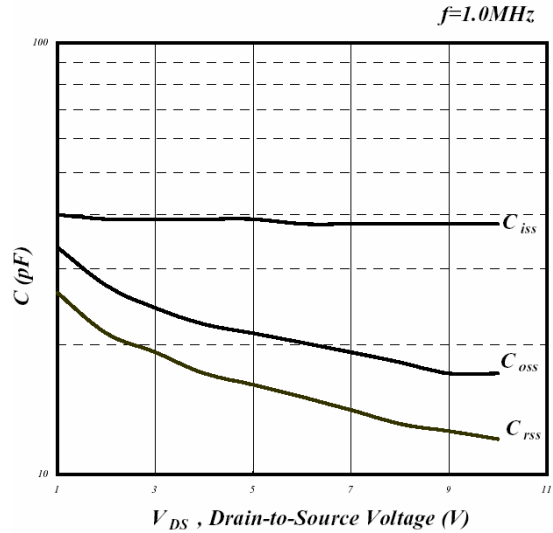


Fig 8. Typical Capacitance Characteristics

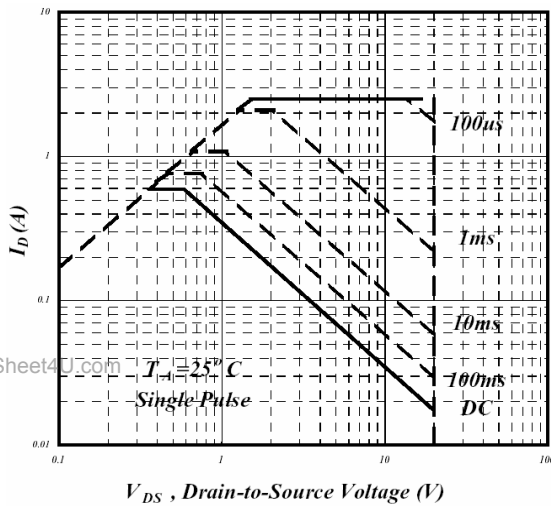


Fig 9. Maximum Safe Operating Area

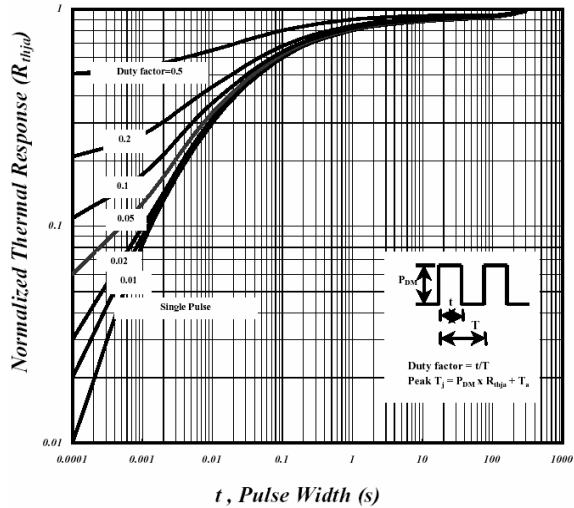


Fig 10. Effective Transient Thermal Impedance

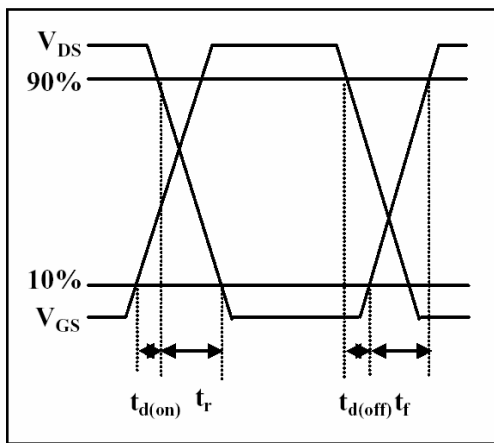


Fig 11. Switching Time Waveform

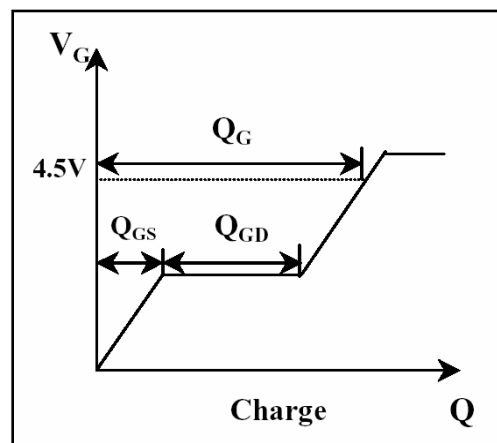


Fig 12. Gate Charge Waveform