

## SWITCHING REGULATOR APPLICATIONS

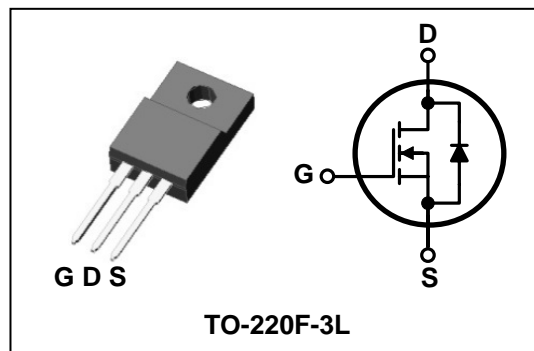
### Features

- High Voltage:  $BV_{DSS}=650V(\text{Min.})$
- Low  $C_{rSS}$  :  $C_{rSS}=10.2pF(\text{Typ.})$
- Low gate charge :  $Qg=23nC(\text{Typ.})$
- Low  $R_{DS(on)}$  :  $R_{DS(on)}=1.4\Omega(\text{Max.})$

### Ordering Information

Type No.	Marking	Package Code
SMK0765F	SMK0765	TO-220F-3L

### PIN Connection



### Absolute maximum ratings ( $T_c=25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Rating	Unit	
Drain-source voltage	$V_{DSS}$	650	V	
Gate-source voltage	$V_{GSS}$	$\pm 30$	V	
Drain current (DC) *	$I_D$	( $T_c=25^\circ\text{C}$ )	7	A
		( $T_c=100^\circ\text{C}$ )	3.4	A
Drain current (Pulsed) *	$I_{DM}$	28	A	
Drain power dissipation	$P_D$	40	W	
Avalanche current (Single) ②	$I_{AS}$	7	A	
Single pulsed avalanche energy ②	$E_{AS}$	201	mJ	
Avalanche current (Repetitive) ①	$I_{AR}$	7	A	
Repetitive avalanche energy ①	$E_{AR}$	14.7	mJ	
Junction temperature	$T_J$	150	$^\circ\text{C}$	
Storage temperature range	$T_{stg}$	-55~150		

\* Limited by maximum junction temperature

Characteristic	Symbol	Typ.	Max	Unit	
Thermal resistance	Junction-case	$R_{th(J-C)}$	-	3.12	$^\circ\text{C}/\text{W}$
	Junction-ambient	$R_{th(J-a)}$	-	62.5	

## Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit	
Drain-source breakdown voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0	650	-	-	V	
Gate threshold voltage	V <sub>GS(th)</sub>	I <sub>D</sub> =250μA, V <sub>DS</sub> =V <sub>GS</sub>	2.0	-	4.0	V	
Drain-source cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V	-	-	1	μA	
Gate leakage current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±30V	-	-	±100	nA	
Drain-source on-resistance ④	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =3.5A	-	1.2	1.4	Ω	
Forward transfer conductance ④	g <sub>fs</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =3.5A	-	8.1	-	S	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V f=1MHz	-	1006	1258	pF	
Output capacitance	C <sub>oss</sub>		-	98	123		
Reverse transfer capacitance	C <sub>rss</sub>		-	10	15		
Turn-on delay time	t <sub>d(on)</sub>	V <sub>DD</sub> =300V, I <sub>D</sub> =7A R <sub>G</sub> =25Ω	-	18	-	ns	
Rise time	t <sub>r</sub>		-	19	-		
Turn-off delay time	t <sub>d(off)</sub>		③④	-	72		-
Fall time	t <sub>f</sub>		-	28	-		
Total gate charge	Q <sub>g</sub>	V <sub>DS</sub> =520V, V <sub>GS</sub> =10V I <sub>D</sub> =7A	-	21	27	nC	
Gate-source charge	Q <sub>gs</sub>		-	6	-		
Gate-drain charge	Q <sub>gd</sub>		③④	-	5		-

## Source-Drain Diode Ratings and Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

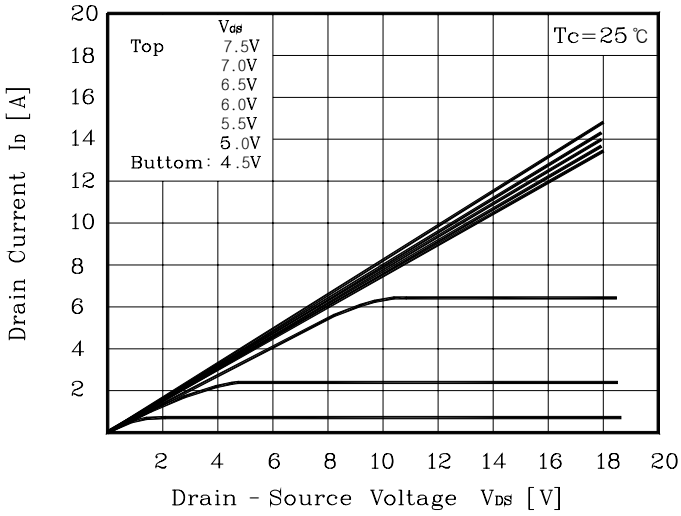
Characteristic	Symbol	Test Condition	Min	Typ	Max	Unit
Source current (DC)	I <sub>S</sub>	Integral reverse diode in the MOSFET	-	-	7	A
Source current (Pulsed) ①	I <sub>SM</sub>		-	-	28	
Forward voltage ④	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =7A	-	-	1.4	V
Reverse recovery time	t <sub>rr</sub>	I <sub>S</sub> =7A, V <sub>GS</sub> =0, di <sub>s</sub> /dt=100A/ us	-	365	-	ns
Reverse recovery charge	Q <sub>rr</sub>		-	3.4	-	uC

Note ;

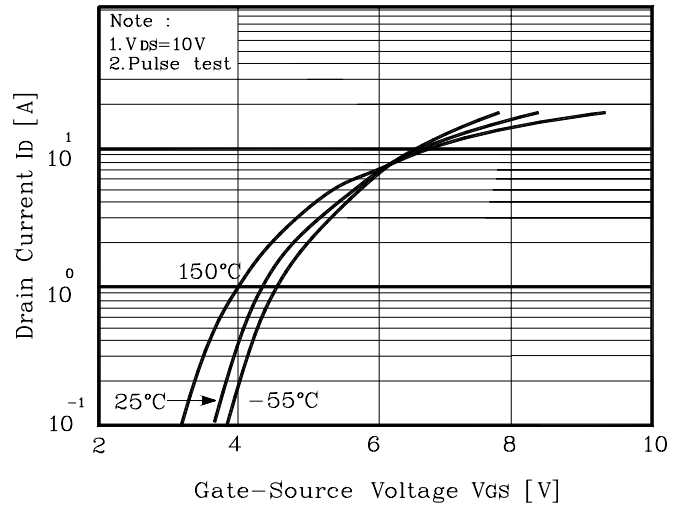
- ① Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature
- ② L=7.6mH, I<sub>AS</sub>=7A, V<sub>DD</sub>=50V, R<sub>G</sub>=27Ω
- ③ Pulse Test : Pulse Width < 300us, Duty cycle ≤ 2%
- ④ Essentially independent of operating temperature

## Electrical Characteristic Curves

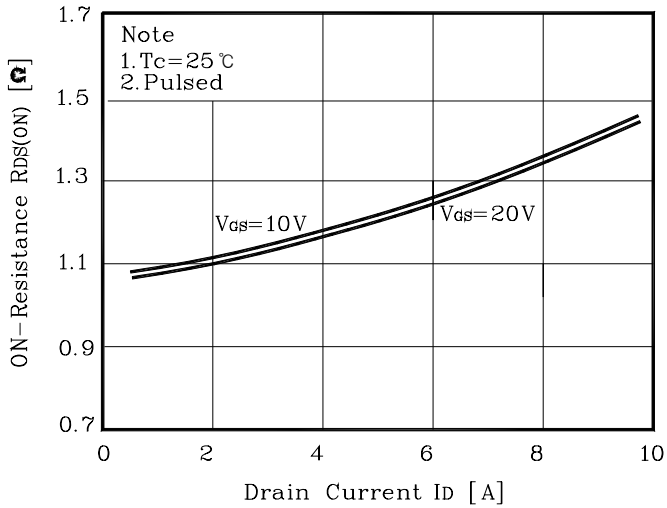
**Fig. 1  $I_D - V_{DS}$**



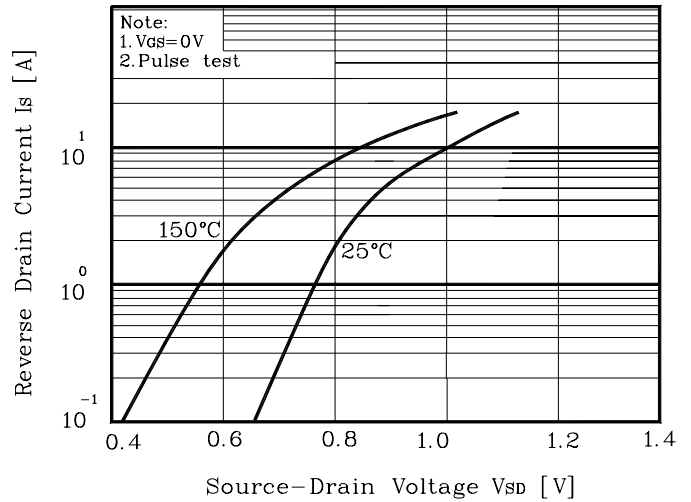
**Fig. 2  $I_D - V_{GS}$**



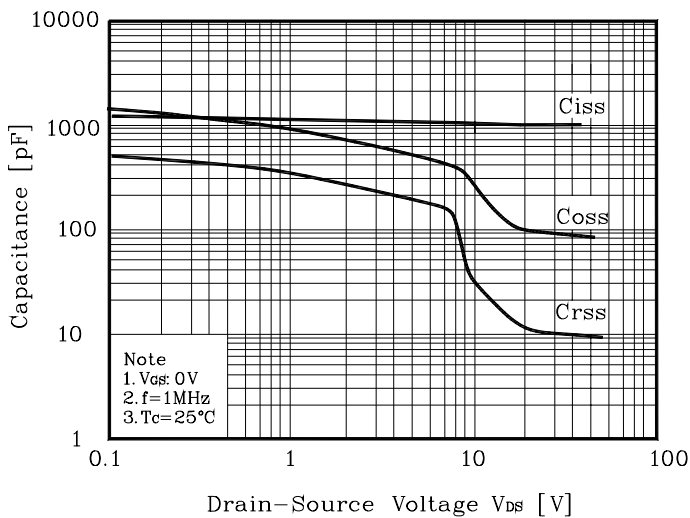
**Fig. 3  $R_{DS(on)} - I_D$**



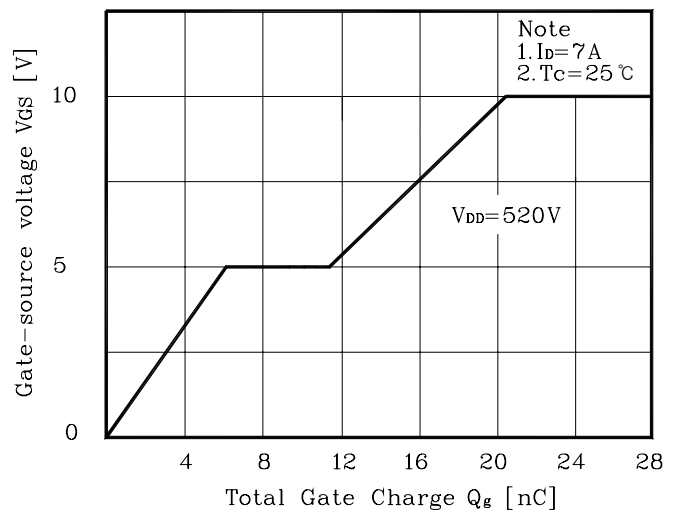
**Fig. 4  $I_S - V_{SD}$**



**Fig. 5 Capacitance -  $V_{DS}$**

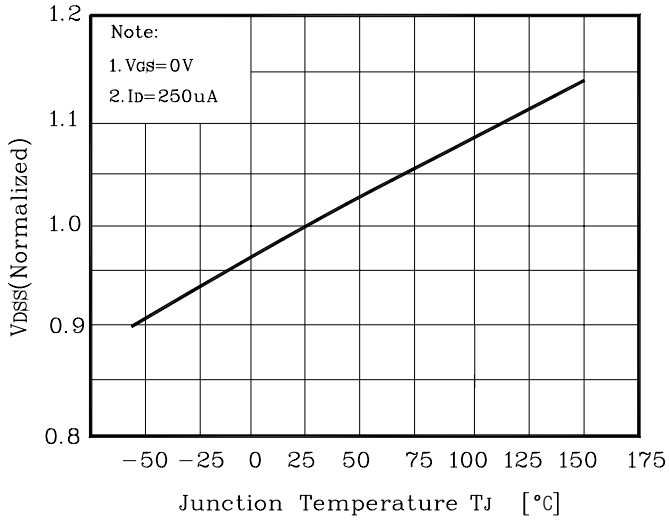


**Fig. 6  $V_{GS} - Q_g$**

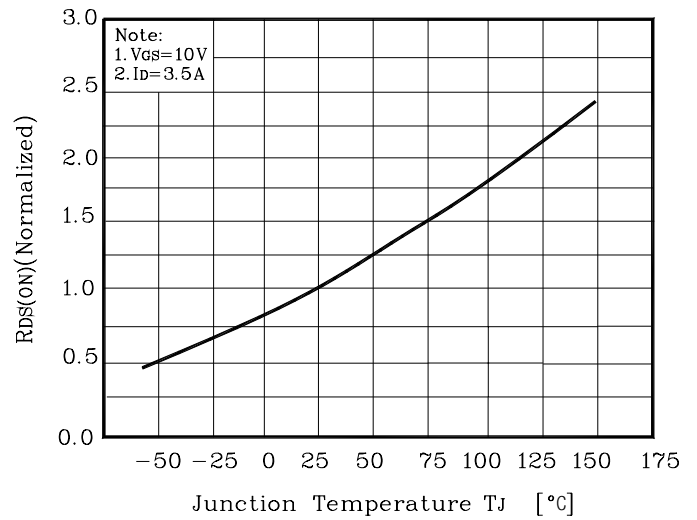


## Electrical Characteristic Curves

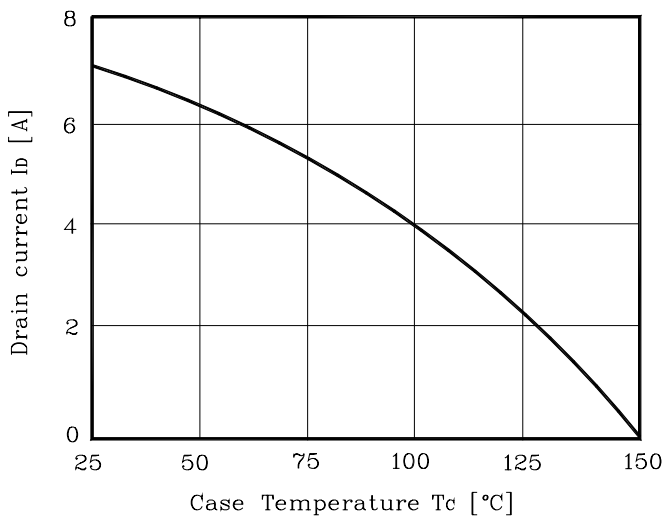
**Fig. 7  $V_{DSS} - T_J$**



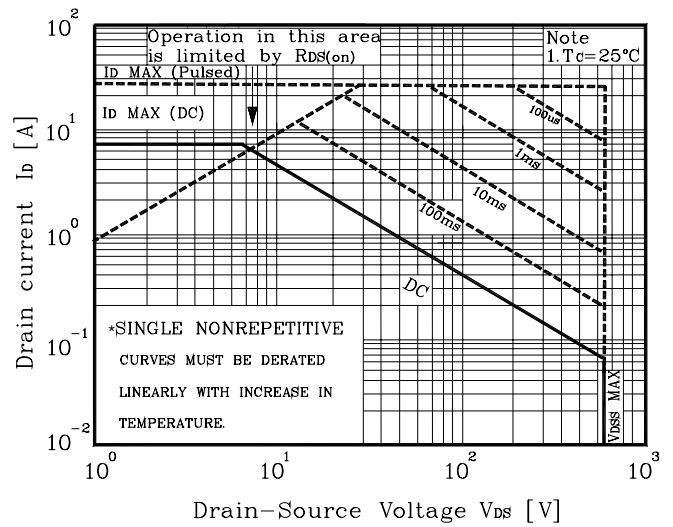
**Fig. 8  $R_{DS(on)} - T_J$**



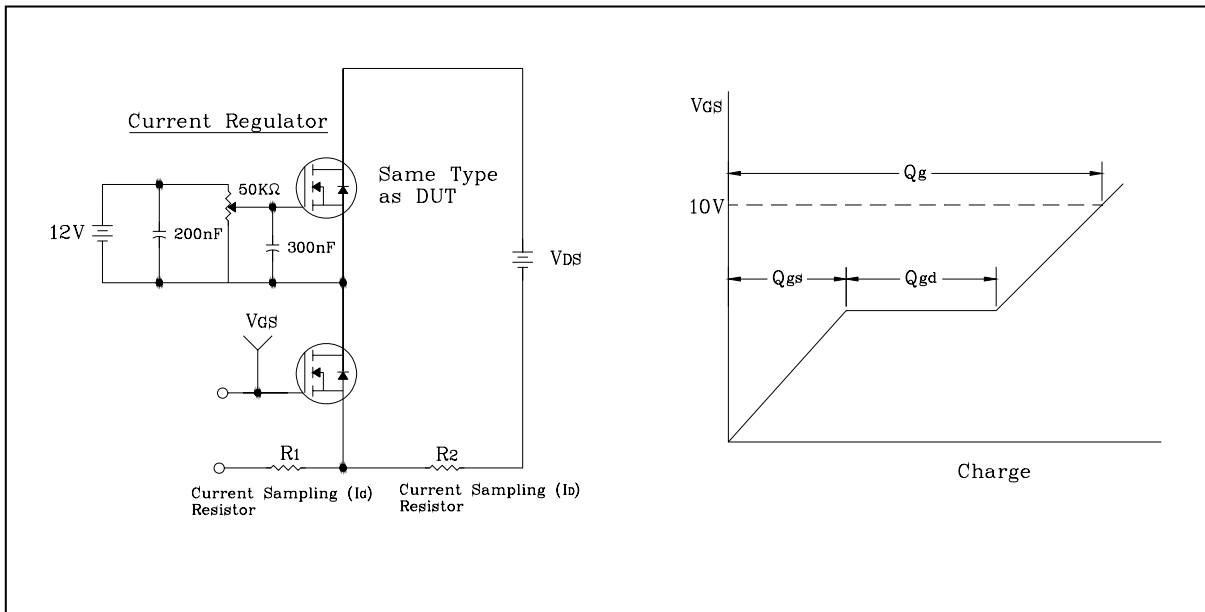
**Fig. 9  $I_D - T_C$**



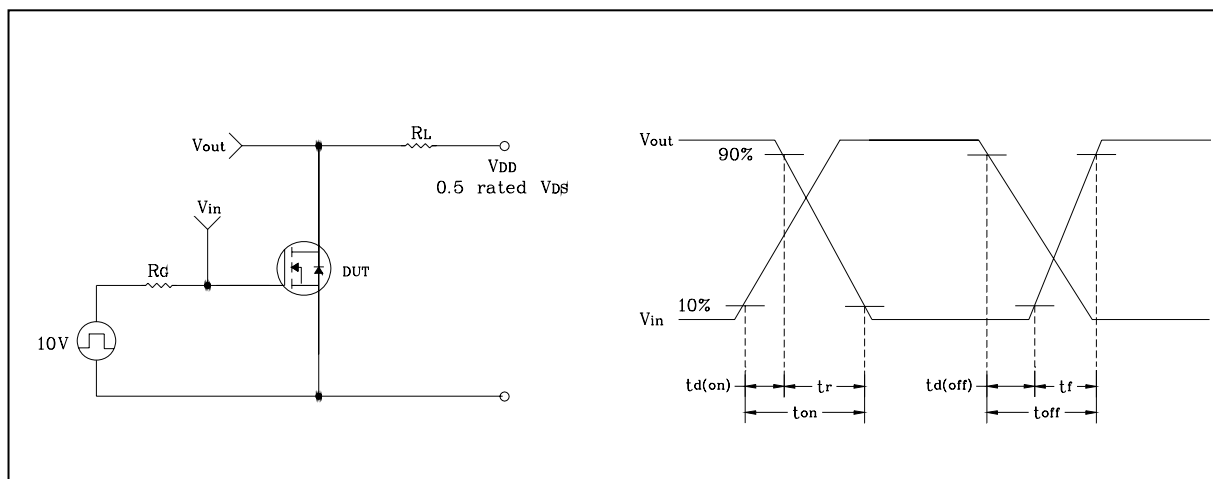
**Fig. 10 Safe Operating Area**



**Fig. 10 Gate Charge Test Circuit & Waveform**



**Fig. 11 Resistive Switching Test Circuit & Waveform**



**Fig. 12 E<sub>AS</sub> Test Circuit & Waveform**

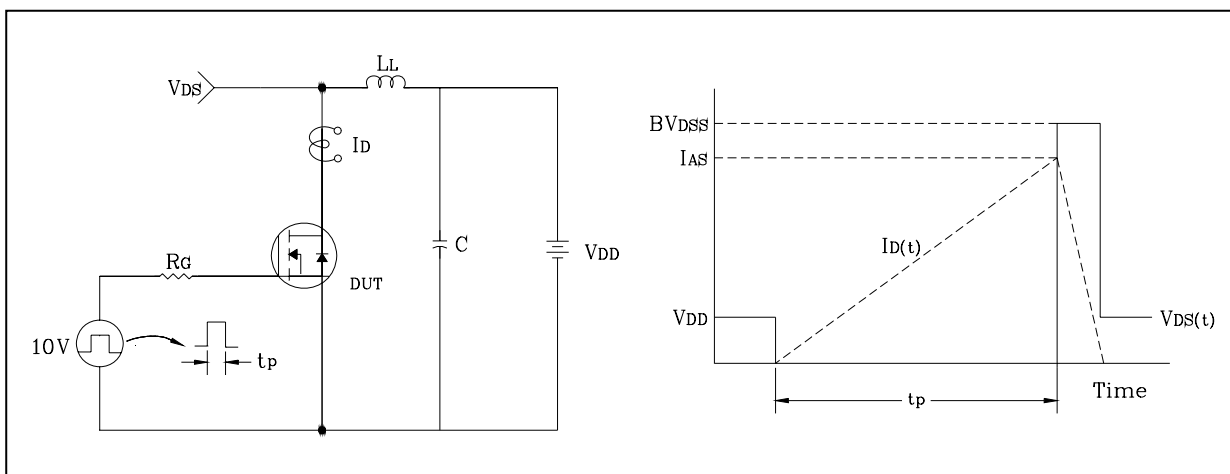
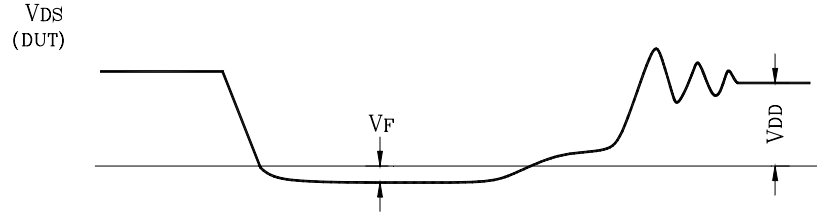
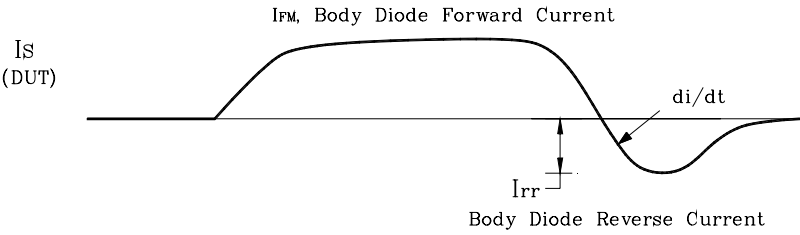
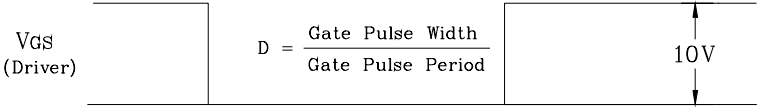
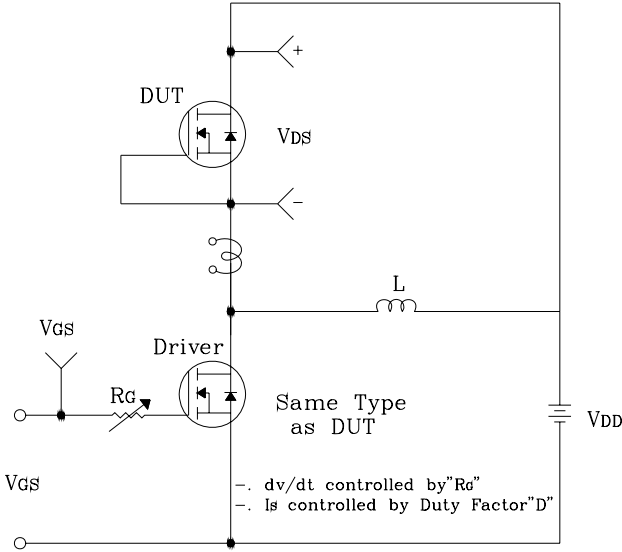
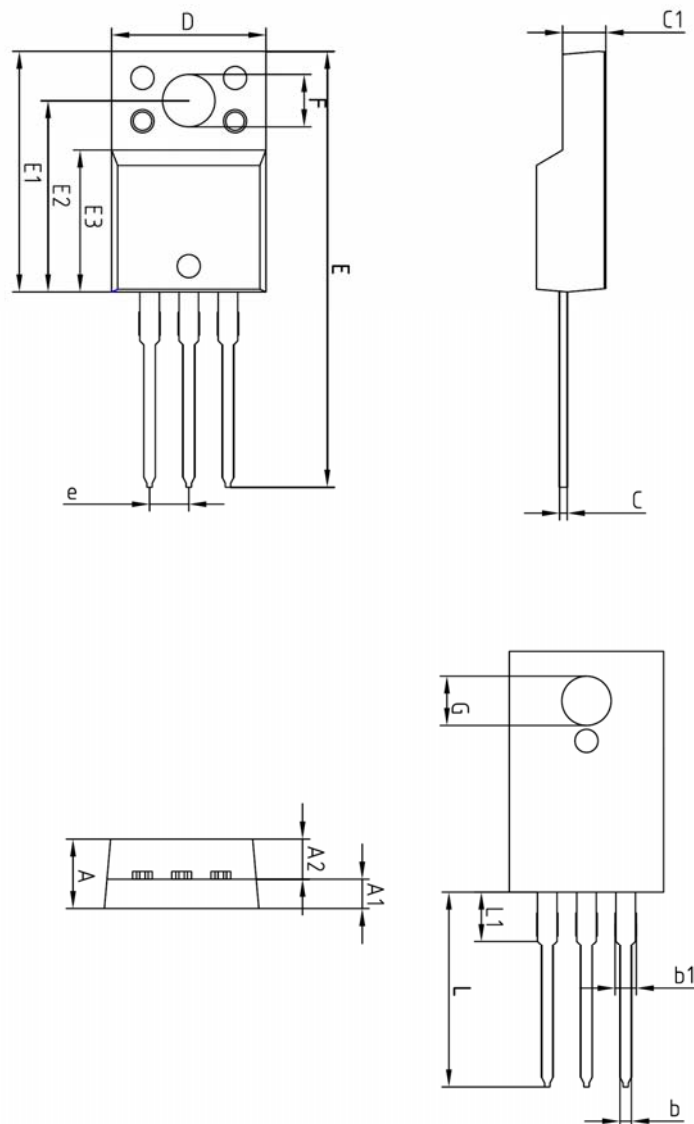


Fig. 13 Diode Reverse Recovery Time Test Circuit & Waveform



## Outline Dimensions



SYMBOL	MILLIMETERS			NOTE
	MINIMUM	NOMINAL	MAXIMUM	
A	-	-	4.60	
A1	2.45	2.50	2.55	
A2	1.95	2.00	2.05	
b	0.65	0.75	0.85	
b1	1.07	1.27	1.47	
C	0.40	0.50	0.60	
C1	2.70	2.80	2.90	
D	9.90	10.00	10.10	
E	28.00	-	28.60	
E1	15.50	15.60	15.70	
E2	12.30	12.40	12.50	
E3	9.15	9.20	9.25	
F	3.30	3.40	3.50	
G	3.10	3.20	3.30	
e	2.54 BSC			
L	12.40	-	13.00	
L1	3.46 BSC			

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**Please make sure that you consult with us before you use these AUK Corp. products in equipments which require high quality and / or reliability, and in equipments which could have major impact to the welfare of human life(atomic energy control, airplane, spaceship, transportation, combustion control, all types of safety device, etc.). AUK Corp. cannot accept liability to any damage which may occur in case these AUK Corp. products were used in the mentioned equipments without prior consultation with AUK Corp..**

**Specifications mentioned in this publication are subject to change without notice.**



## DC-DC CONVERTER APPLICATION HIGH VOLTAGE SWITCHING APPLICATIONS

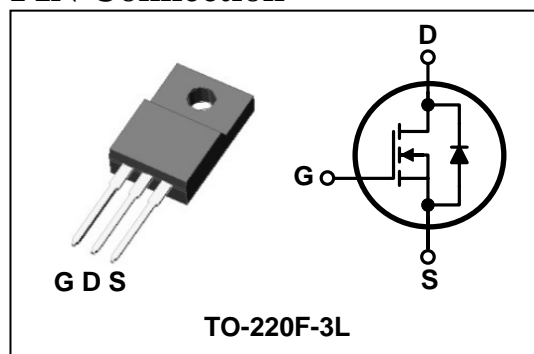
### Features

- High Voltage:  $BV_{DSS}=250V(\text{Min.})$
- Low  $C_{rSS}$  :  $C_{rSS}=33pF(\text{Typ.})$
- Low gate charge :  $Q_g=14.5nC(\text{Typ.})$
- Low  $R_{DS(on)}$  :  $R_{DS(on)}=0.43\Omega(\text{Max.})$

### Ordering Information

Type NO.	Marking	Package Code
SMK0825F	SMK0825	TO-220F-3L

### PIN Connection



### Absolute maximum ratings ( $T_C=25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Rating	Unit
Drain-source voltage	$V_{DSS}$	250	V
Gate-source voltage	$V_{GSS}$	$\pm 30$	V
Drain current (DC)	$I_D$	( $T_C=25^\circ\text{C}$ )	8
		( $T_C=100^\circ\text{C}$ )	5.2
Drain current (Pulsed) *	$I_{DM}$	32	A
Drain power dissipation	$P_D$	29	W
Avalanche current (Single) ②	$I_{AS}$	8	A
Single pulsed avalanche energy ②	$E_{AS}$	356	mJ
Avalanche current (Repetitive) ①	$I_{AR}$	8	A
Repetitive avalanche energy ①	$E_{AR}$	7.4	mJ
Junction temperature	$T_J$	150	$^\circ\text{C}$
Storage temperature range	$T_{stg}$	-55~150	

\* Limited by maximum junction temperature

Characteristic	Symbol	Typ.	Max	Unit
Thermal resistance	Junction-case	-	4.31	$^\circ\text{C}/\text{W}$
	Junction-ambient	-	62.5	

## Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit	
Drain-source breakdown voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0	250	-	-	V	
Gate threshold voltage	V <sub>GS(th)</sub>	I <sub>D</sub> =250μA, V <sub>DS</sub> =V <sub>GS</sub>	2.0	-	4.0	V	
Drain-source cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> =250V, V <sub>GS</sub> =0V	-	-	1	μA	
Gate leakage current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±30V	-	-	±100	nA	
Drain-source on-resistance ④	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =4.0A	-	0.35	0.43	Ω	
Forward transfer conductance ④	g <sub>fs</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =4.0A	-	7.0	-	S	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1MHz	-	619	773	pF	
Output capacitance	C <sub>oss</sub>		-	141	176		
Reverse transfer capacitance	C <sub>rss</sub>		-	33	41		
Turn-on delay time	t <sub>d(on)</sub>	V <sub>DD</sub> =125V, I <sub>D</sub> =8A R <sub>G</sub> =25Ω	-	15	-	ns	
Rise time	t <sub>r</sub>		-	85	-		
Turn-off delay time	t <sub>d(off)</sub>		③④	-	90		-
Fall time	t <sub>f</sub>		-	65	-		
Total gate charge	Q <sub>g</sub>	V <sub>DS</sub> =200V, V <sub>GS</sub> =10V I <sub>D</sub> =8A	-	14.5	18.2	nC	
Gate-source charge	Q <sub>gs</sub>		-	4.0	-		
Gate-drain charge	Q <sub>gd</sub>		③④	-	4.5		-

## Source-Drain Diode Ratings and Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

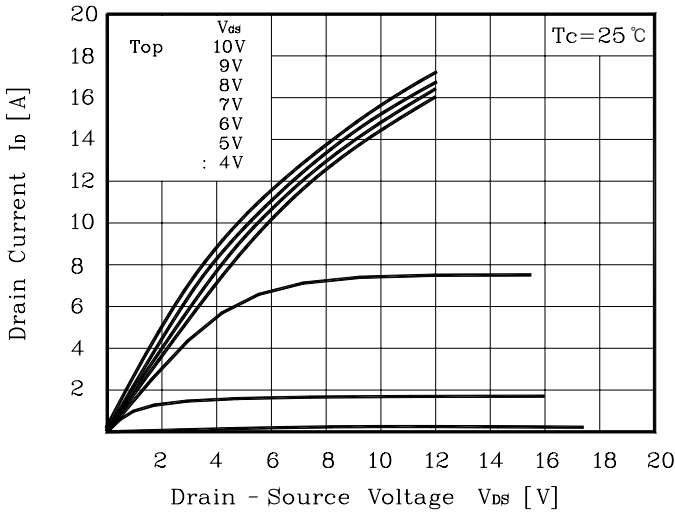
Characteristic	Symbol	Test Condition	Min	Typ	Max	Unit
Source current	I <sub>S</sub>	Integral reverse diode in the MOSFET	-	-	8	A
Source current(Plused) ①	I <sub>SM</sub>		-	-	32	
Forward voltage ④	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =8A	-	-	1.4	V
Reverse recovery time	t <sub>rr</sub>	I <sub>S</sub> =8A, V <sub>GS</sub> =0, di <sub>S</sub> /dt=100A/us	-	178	-	ns
Reverse recovery charge	Q <sub>rr</sub>		-	1.16	-	uC

Note ;

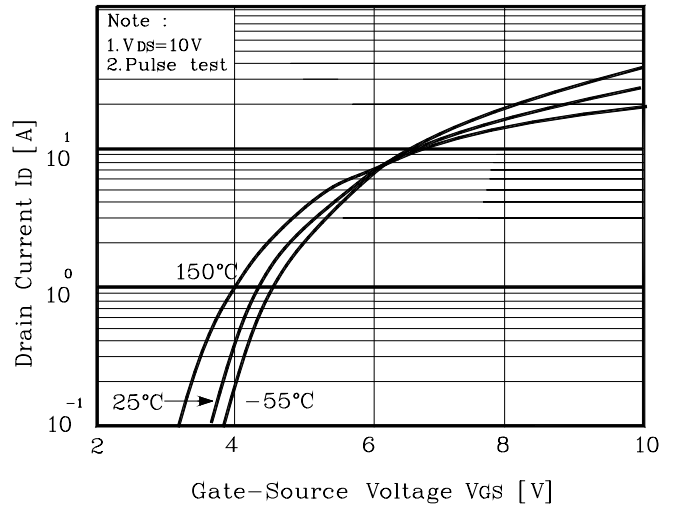
- ① Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature
- ② L=8.9mH, I<sub>AS</sub>=8A, V<sub>DD</sub>=50V, R<sub>G</sub>=27Ω
- ③ Pulse Test : Pulse Width < 300us, Duty cycle ≤ 2%
- ④ Essentially independent of operating temperature

## Electrical Characteristic Curves

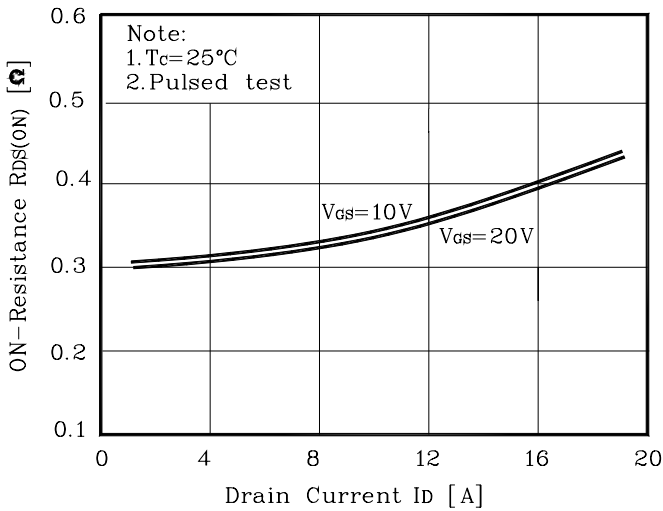
**Fig. 1  $I_D - V_{DS}$**



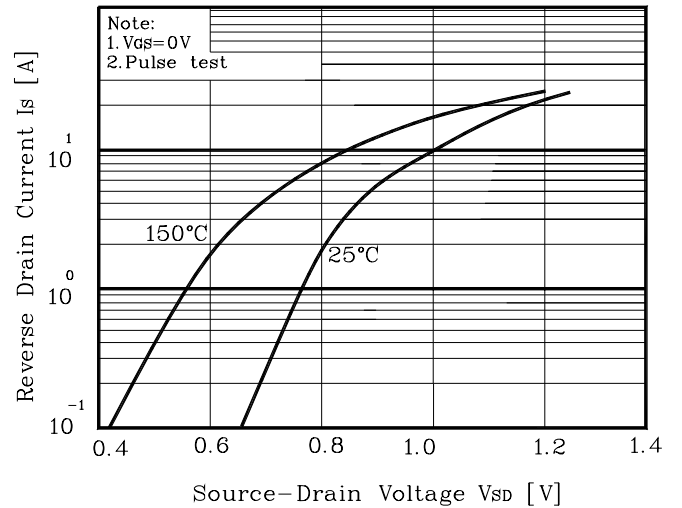
**Fig. 2  $I_D - V_{GS}$**



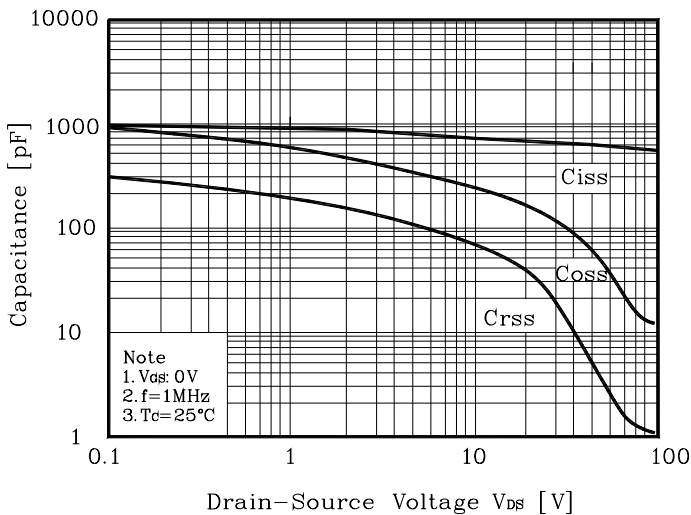
**Fig. 3  $R_{DS(on)} - I_D$**



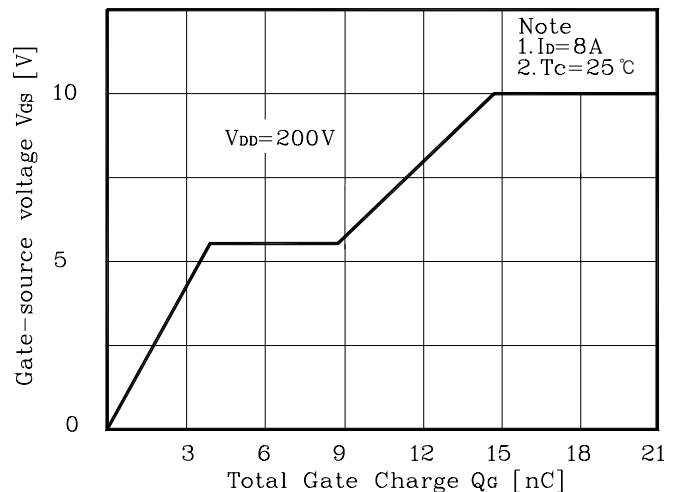
**Fig. 4  $I_S - V_{SD}$**



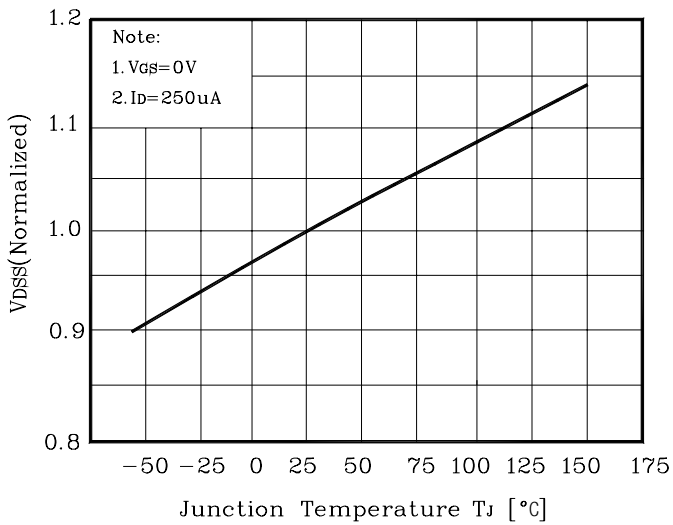
**Fig. 5 Capacitance -  $V_{DS}$**



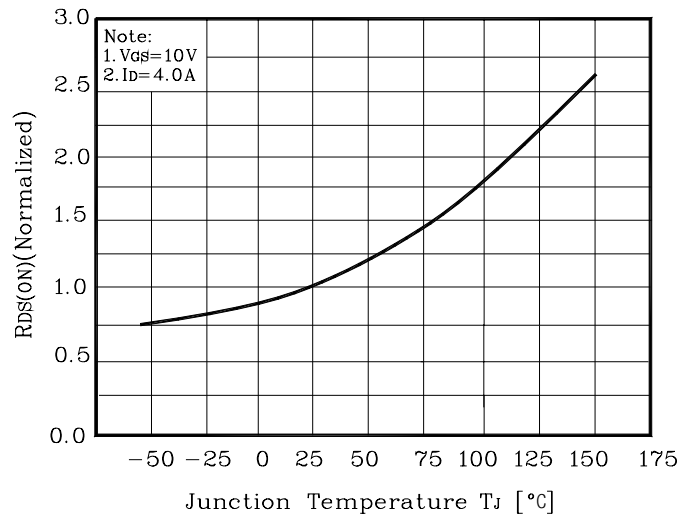
**Fig. 6  $V_{GS} - Q_G$**



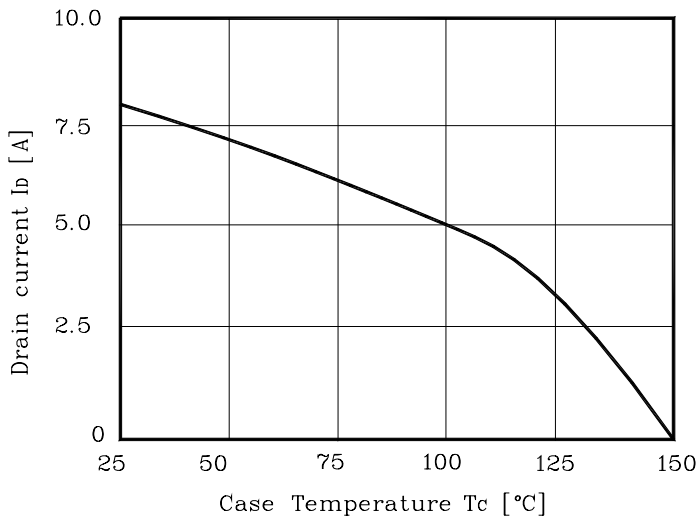
**Fig. 7  $V_{DSS} - T_J$**



**Fig. 8  $R_{DS(on)} - T_J$**



**Fig. 9  $I_D - T_C$**



**Fig. 10 Safe Operating Area**

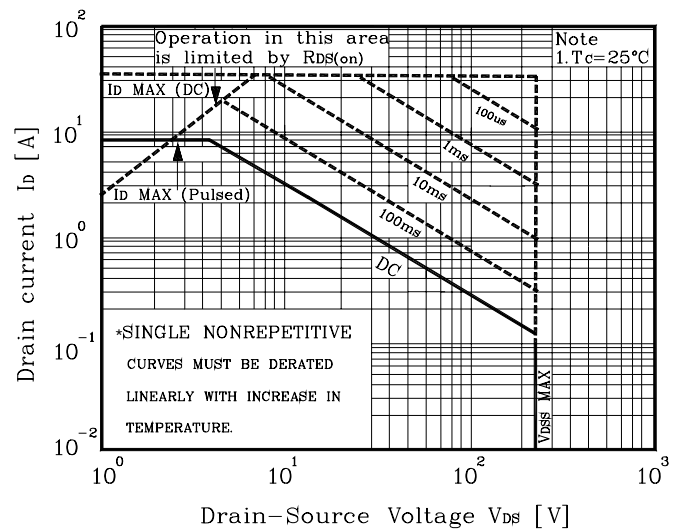


Fig. 11 Gate Charge Test Circuit & Waveform

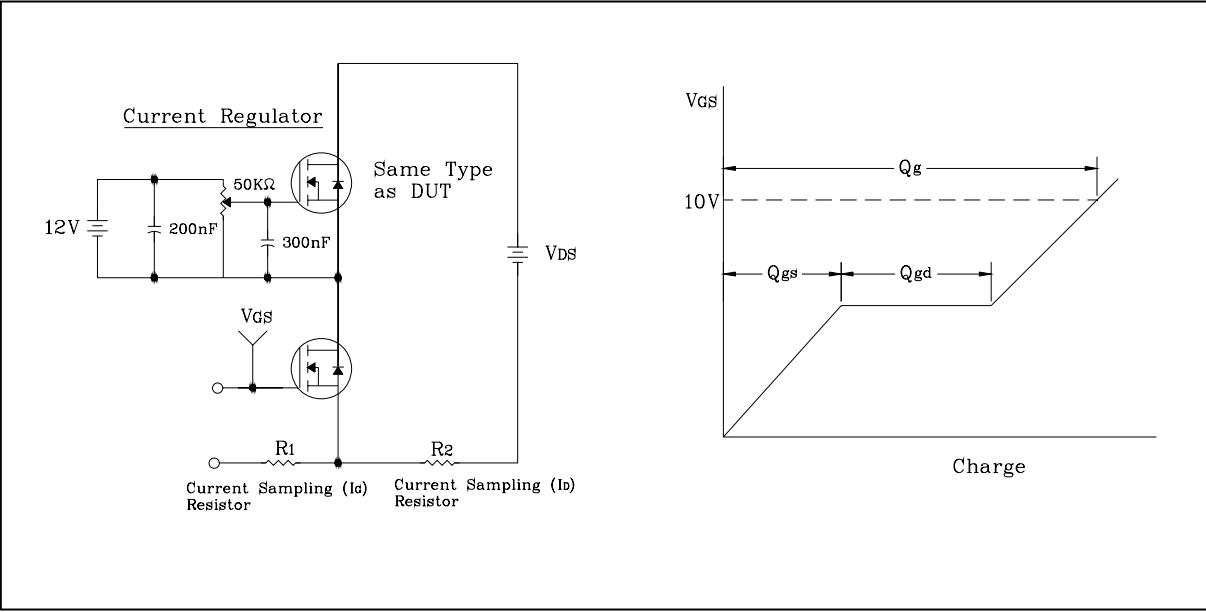


Fig. 12 Resistive Switching Test Circuit & Waveform

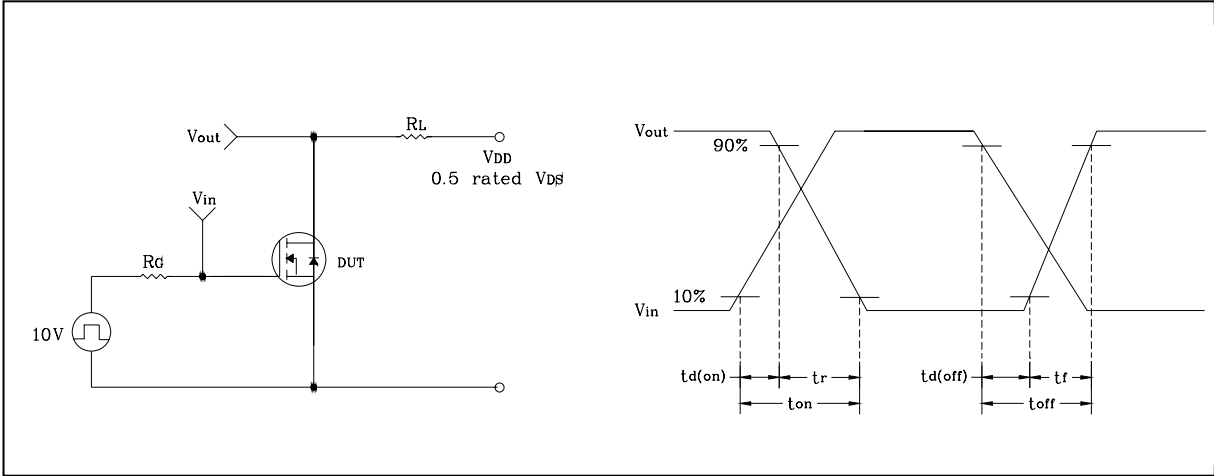


Fig. 13 E<sub>AS</sub> Test Circuit & Waveform

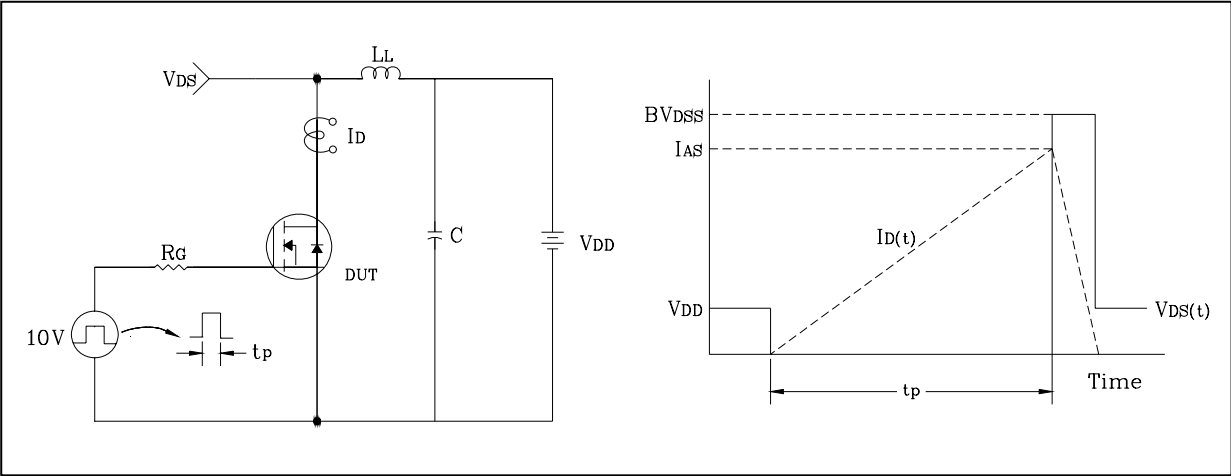
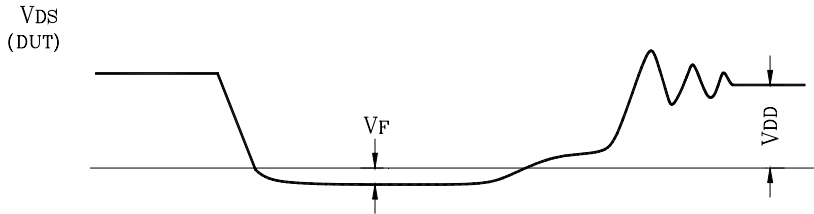
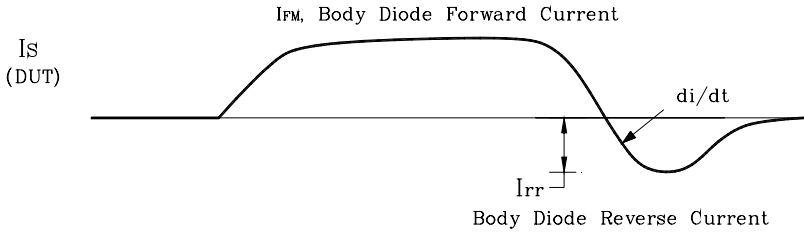
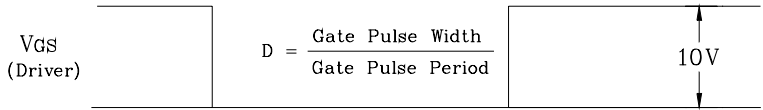
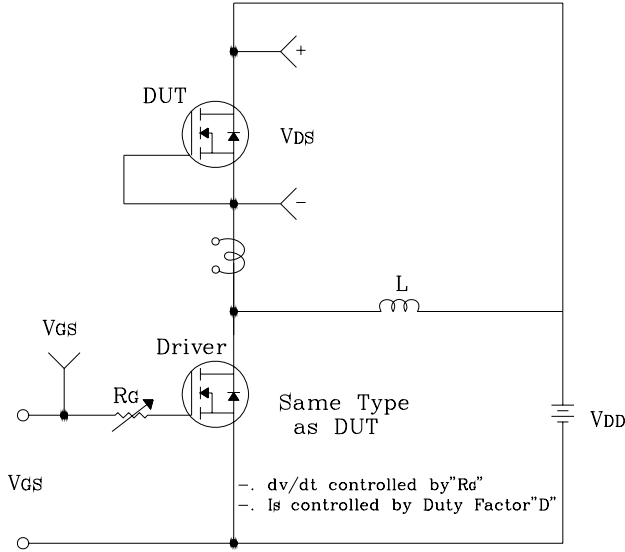
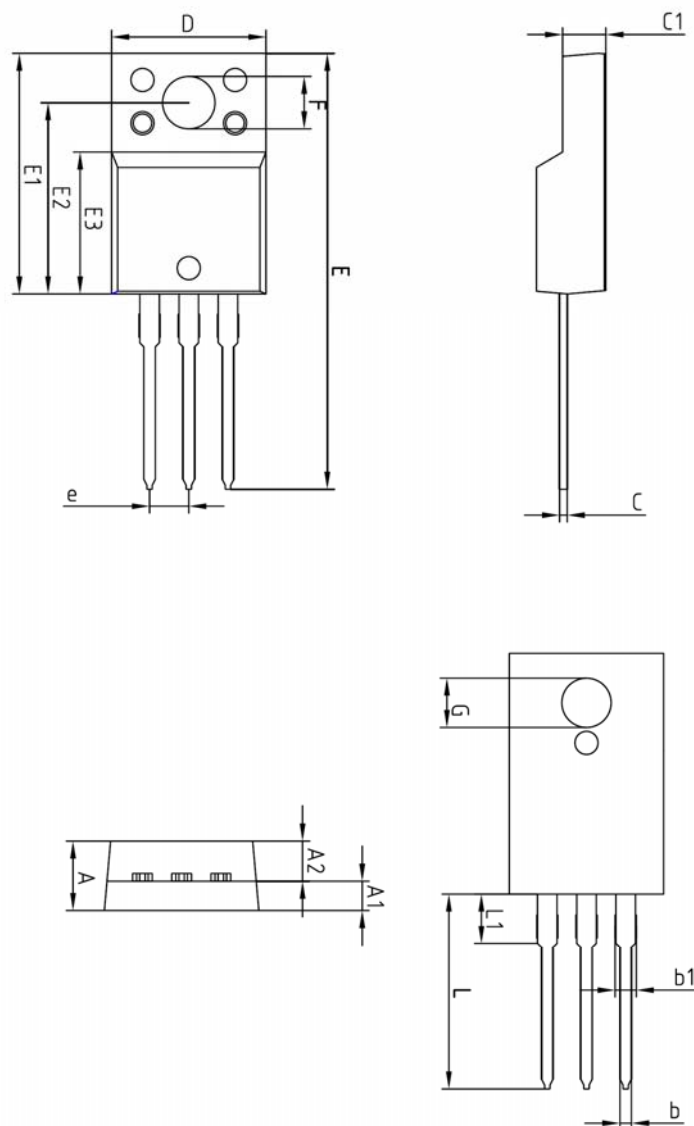


Fig. 14 Diode Reverse Recovery Time Test Circuit & Waveform



## Outline Dimension



SYMBOL	MILLIMETERS			NOTE
	MINIMUM	NOMINAL	MAXIMUM	
A	-	-	4.60	
A1	2.45	2.50	2.55	
A2	1.95	2.00	2.05	
b	0.65	0.75	0.85	
b1	1.07	1.27	1.47	
C	0.40	0.50	0.60	
C1	2.70	2.80	2.90	
D	9.90	10.00	10.10	
E	28.00	-	28.60	
E1	15.50	15.60	15.70	
E2	12.30	12.40	12.50	
E3	9.15	9.20	9.25	
F	3.30	3.40	3.50	
G	3.10	3.20	3.30	
e	2.54 BSC			
L	12.40	-	13.00	
L1	3.46 BSC			

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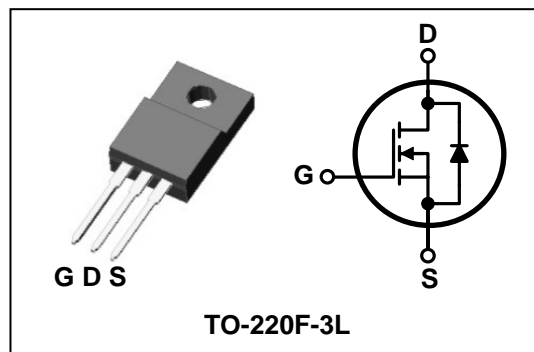
### Features

- High Voltage:  $BV_{DSS}=600V(\text{Min.})$
- Low  $C_{rSS}$  :  $C_{rSS}=5.8pF(\text{Typ.})$
- Low gate charge :  $Qg=13nC(\text{Typ.})$
- Low  $R_{DS(on)}$  :  $R_{DS(on)}=2.5\Omega(\text{Max.})$

### Ordering Information

Type No.	Marking	Package Code
SMK0465F	SMK0465	TO-220F-3L

### PIN Connection



### Absolute maximum ratings ( $T_c=25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Rating	Unit
Drain-source voltage	$V_{DSS}$	650	V
Gate-source voltage	$V_{GSS}$	$\pm 30$	V
Drain current (DC)	$I_D$	( $T_c=25^\circ\text{C}$ )	4
		( $T_c=100^\circ\text{C}$ )	2.0
Drain current (Pulsed) *	$I_{DM}$	16	A
Drain Power dissipation	$P_D$	30	W
Avalanche current (Single) ②	$I_{AS}$	4	A
Single pulsed avalanche energy ②	$E_{AS}$	81.5	mJ
Avalanche current (Repetitive) ①	$I_{AR}$	4	A
Repetitive avalanche energy ①	$E_{AR}$	3.4	mJ
Junction temperature	$T_J$	150	$^\circ\text{C}$
Storage temperature range	$T_{stg}$	-55~150	

\* Limited by maximum junction temperature

Characteristic	Symbol	Typ.	Max	Unit	
Thermal resistance	Junction-case	$R_{th(J-C)}$	-	4.16	$^\circ\text{C}/\text{W}$
	Junction-ambient	$R_{th(J-a)}$	-	62.5	

## Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit	
Drain-source breakdown voltage	BV <sub>DSS</sub>	I <sub>D</sub> =250μA, V <sub>GS</sub> =0	650	-	-	V	
Gate-threshold voltage	V <sub>GS(th)</sub>	I <sub>D</sub> =250μA, V <sub>DS</sub> =V <sub>GS</sub>	2.0	-	4.0	V	
Drain-source leakage current	I <sub>DSS</sub>	V <sub>DS</sub> =650V, V <sub>GS</sub> =0V	-	-	1	μA	
Gate-source leakage	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±30V	-	-	±100	nA	
Drain-Source on-resistance ④	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =2.0A	-	2.4	3.0	Ω	
Forward transfer admittance ④	g <sub>fs</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =2.0A	-	4.0	-	S	
Input capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =25V, f=1MHz	-	609	761	pF	
Output capacitance	C <sub>oss</sub>		-	50.4	63.0		
Reverse transfer capacitance	C <sub>rss</sub>		-	5.3	6.7		
Turn-on delay time	t <sub>d(on)</sub>	V <sub>DD</sub> =300V, I <sub>D</sub> =4A R <sub>G</sub> =25Ω	-	10	-	ns	
Rise time	t <sub>r</sub>		-	42	-		
Turn-off delay time	t <sub>d(off)</sub>		③④	-	38		-
Fall time	t <sub>f</sub>		-	46	-		
Total gate charge	Q <sub>g</sub>	V <sub>DS</sub> =480V, V <sub>GS</sub> =10V I <sub>D</sub> =4A	-	13	17	nC	
Gate-source charge	Q <sub>gs</sub>		-	4	-		
Gate-drain charge	Q <sub>gd</sub>		③④	-	3		-

## Source-Drain Diode Ratings and Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

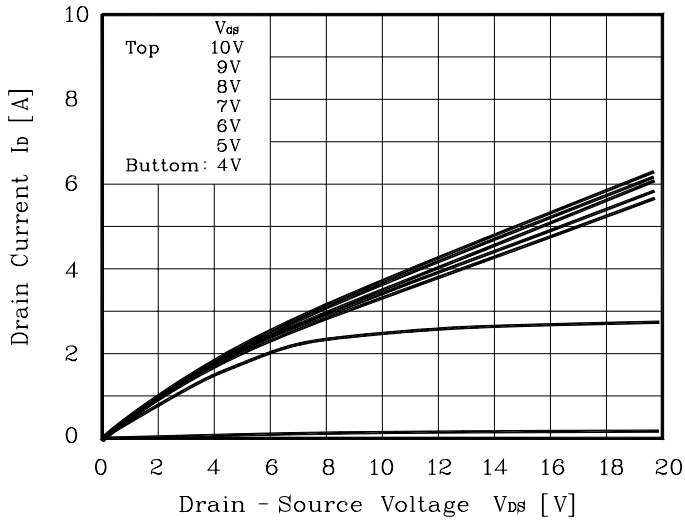
Characteristic	Symbol	Test Condition	Min	Typ	Max	Unit
Continuous source current	I <sub>S</sub>	Integral reverse diode in the MOSFET	-	-	4	A
Source current (Pulsed) ①	I <sub>SM</sub>		-	-	16	
Forward voltage ④	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =4A	-	-	1.4	V
Reverse recovery time	t <sub>rr</sub>	I <sub>S</sub> =4A di <sub>s</sub> /dt=100A/us	-	300	-	ns
Reverse recovery charge	Q <sub>rr</sub>		-	2.2	-	uC

Note ;

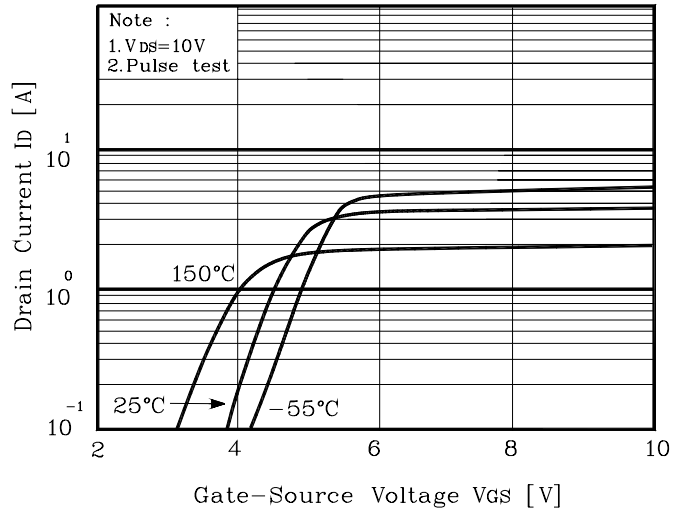
- ① Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature
- ② L=9.4mH, I<sub>AS</sub>=4A, V<sub>DD</sub>=50V, R<sub>G</sub>=27Ω , Starting T<sub>J</sub> = 25 °C
- ③ Pulse Test : Pulse Width < 300us, Duty cycle ≤ 2%
- ④ Essentially independent of operating temperature

## Electrical Characteristic Curves

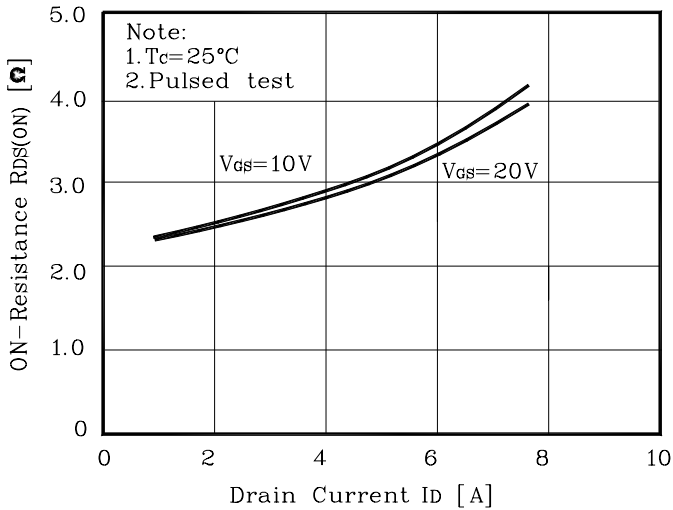
**Fig. 1  $I_D - V_{DS}$**



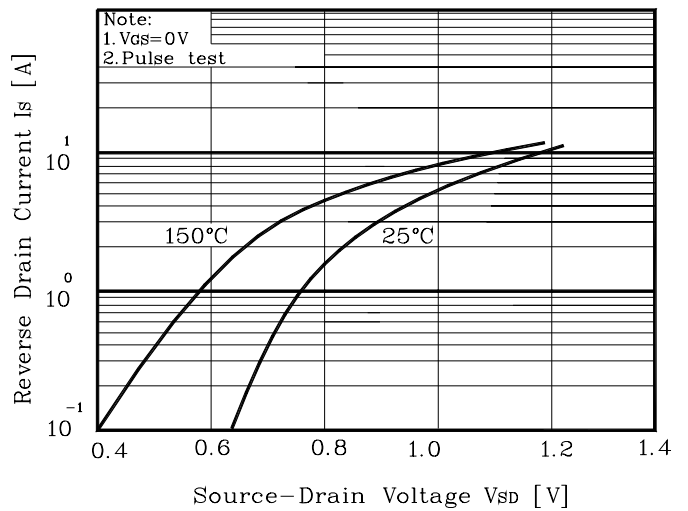
**Fig. 2  $I_D - V_{GS}$**



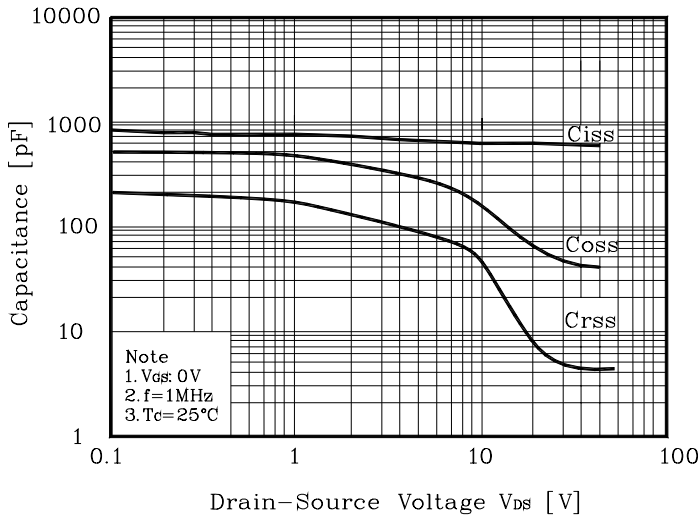
**Fig. 3  $R_{DS(on)} - I_D$**



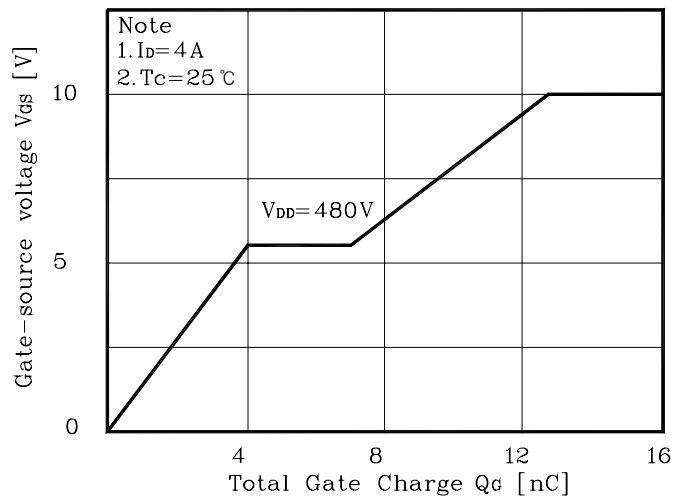
**Fig. 4  $I_S - V_{SD}$**



**Fig. 5 Capacitance -  $V_{DS}$**

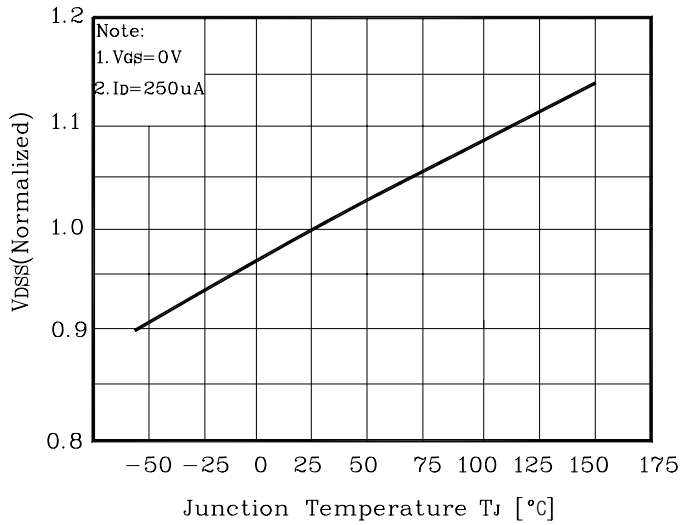


**Fig. 6  $V_{GS} - Q_G$**

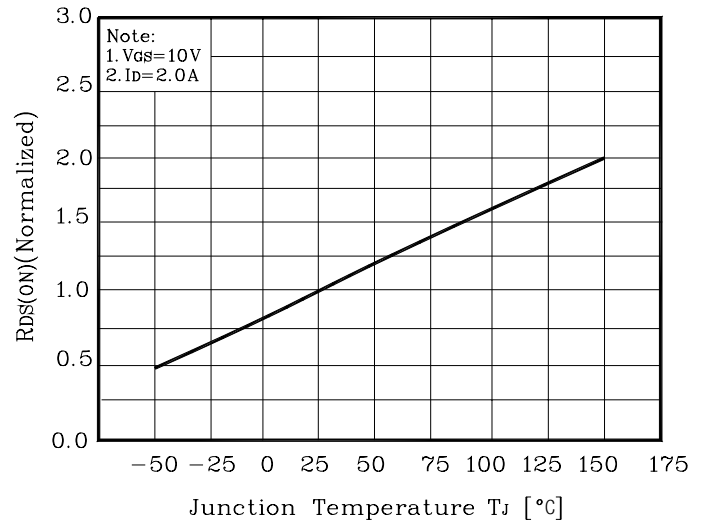


## Electrical Characteristic Curves

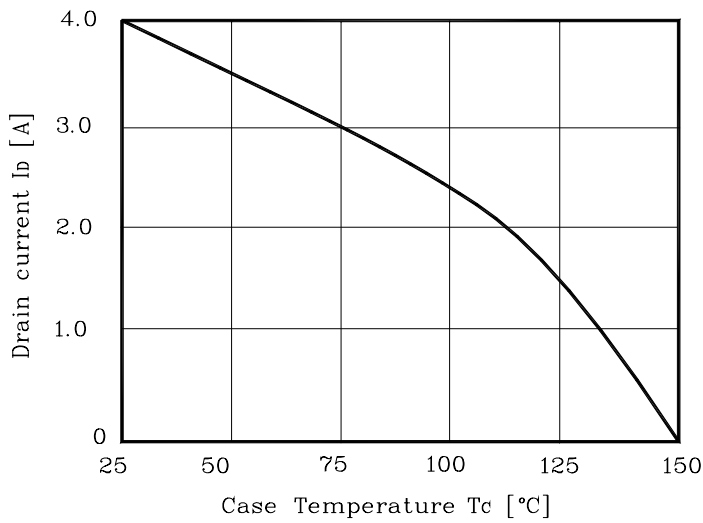
**Fig. 7**  $V_{DSS} - T_J$



**Fig. 8**  $R_{DS(on)} - T_J$



**Fig. 9**  $I_D - T_C$



**Fig. 10** Safe Operating Area

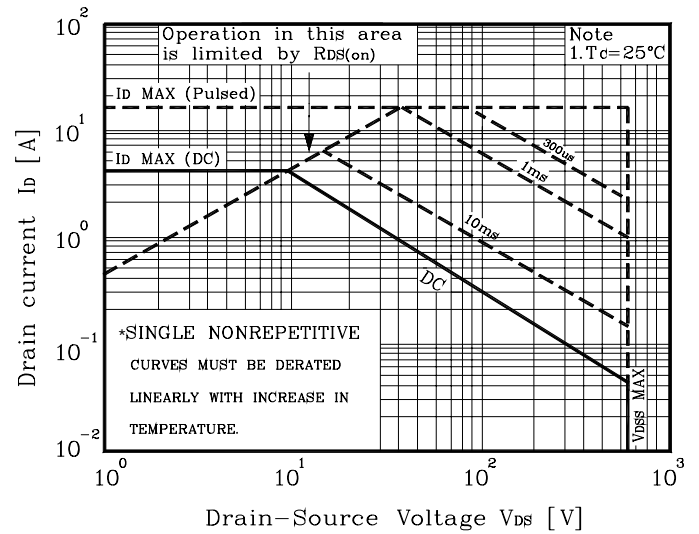


Fig. 11 Gate Charge Test Circuit & Waveform

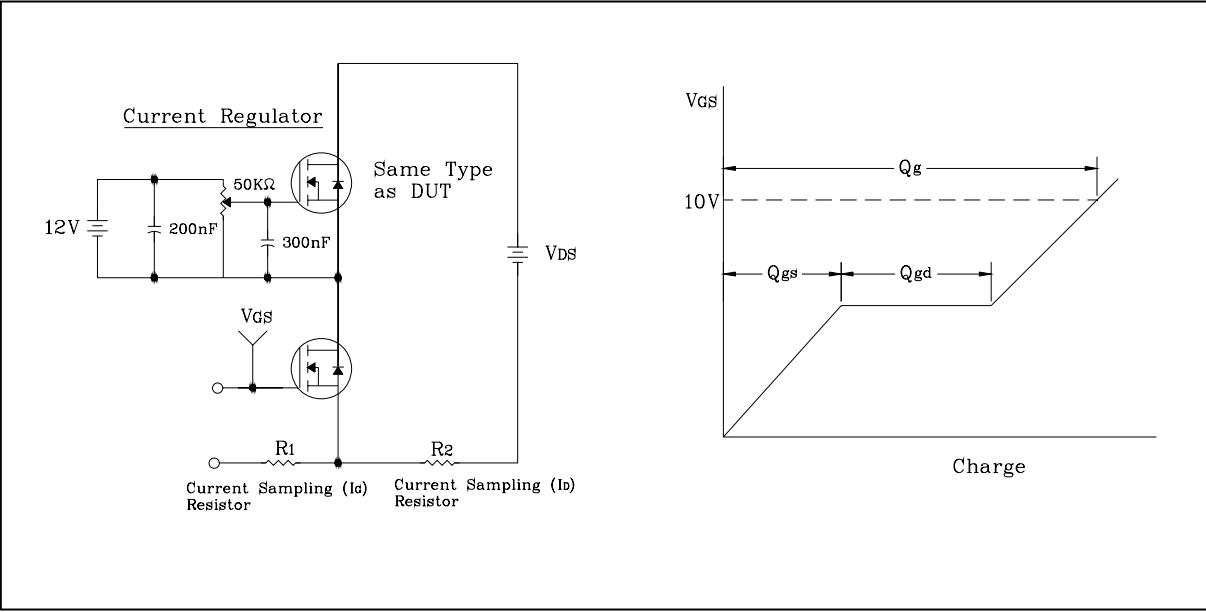


Fig. 12 Resistive Switching Test Circuit & Waveform

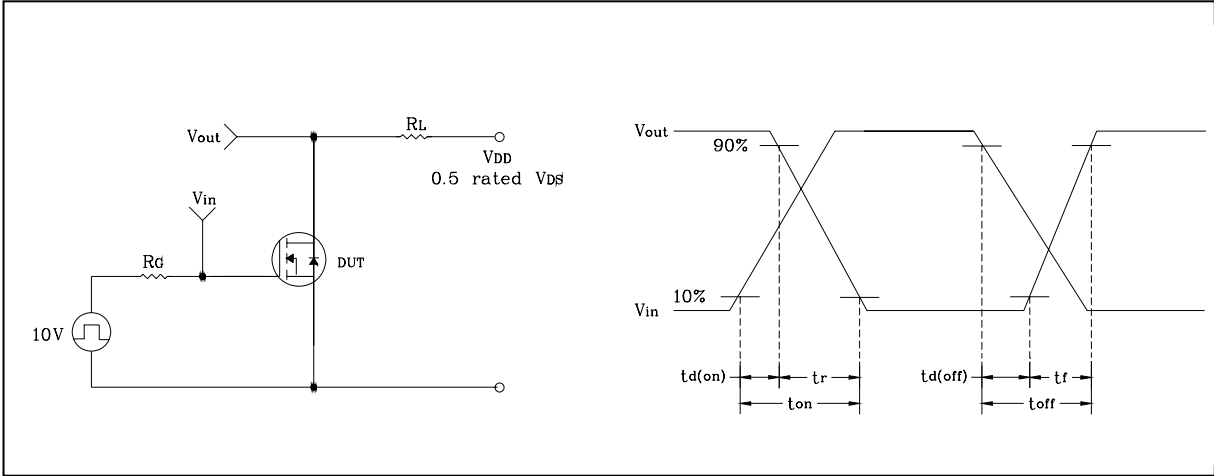


Fig. 13 E<sub>AS</sub> Test Circuit & Waveform

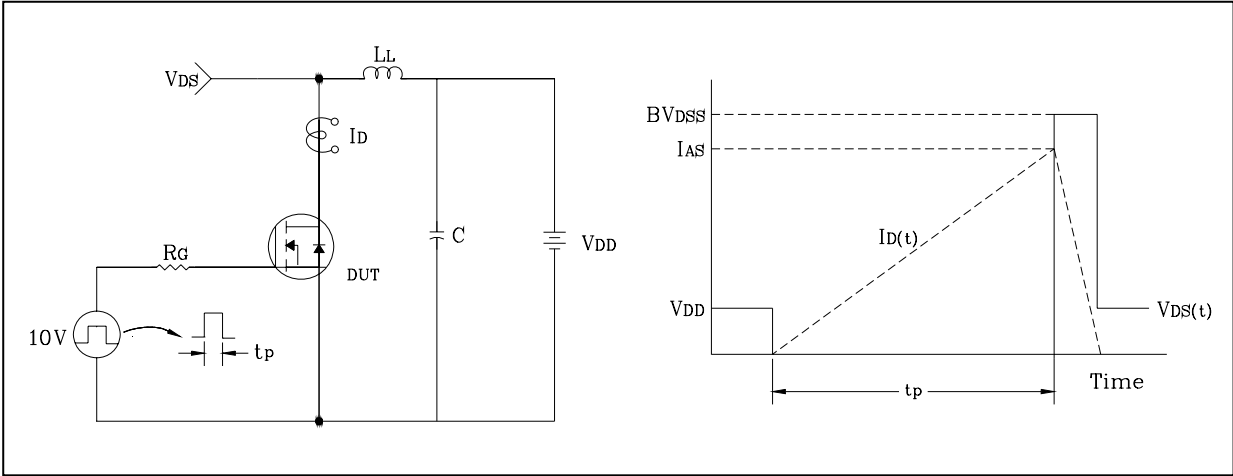
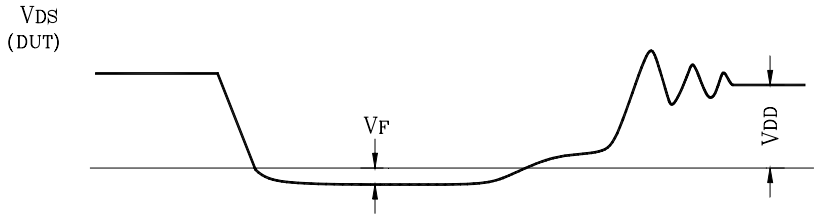
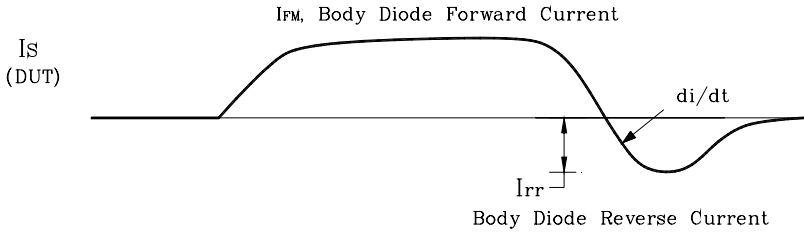
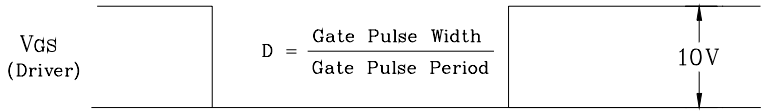
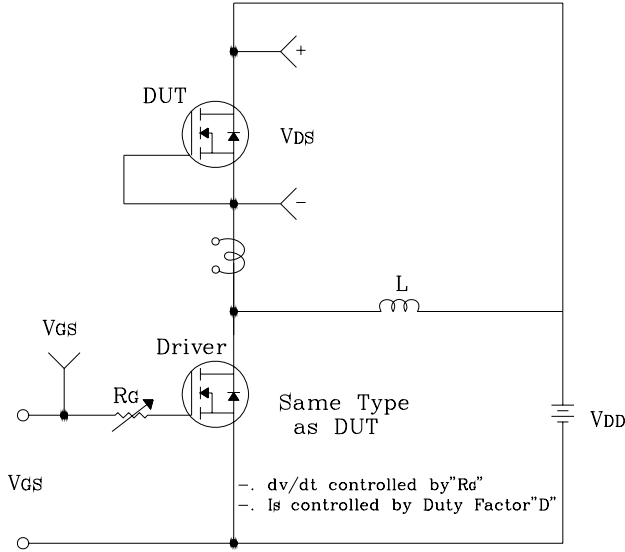
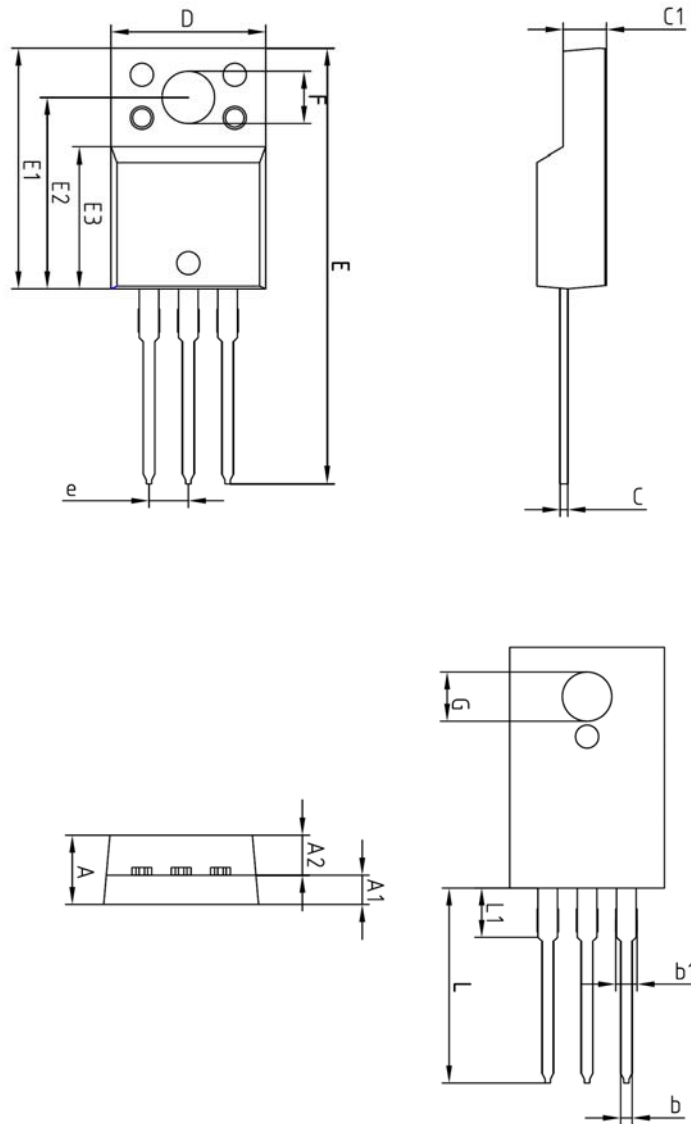


Fig. 14 Diode Reverse Recovery Time Test Circuit & Waveform



## Outline Dimension



SYMBOL	MILLIMETERS			NOTE
	MINIMUM	NOMINAL	MAXIMUM	
A	-	-	4.60	
A1	2.45	2.50	2.55	
A2	1.95	2.00	2.05	
b	0.65	0.75	0.85	
b1	1.07	1.27	1.47	
C	0.40	0.50	0.60	
C1	2.70	2.80	2.90	
D	9.90	10.00	10.10	
E	28.00	-	28.60	
E1	15.50	15.60	15.70	
E2	12.30	12.40	12.50	
E3	9.15	9.20	9.25	
F	3.30	3.40	3.50	
G	3.10	3.20	3.30	
e	2.54 BSC			
L	12.40	-	13.00	
L1	3.46 BSC			

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**Please make sure that you consult with us before you use these AUK Corp. products in equipments which require high quality and / or reliability, and in equipments which could have major impact to the welfare of human life(atomic energy control, airplane, spaceship, transportation, combustion control, all types of safety device, etc.). AUK Corp. cannot accept liability to any damage which may occur in case these AUK Corp. products were used in the mentioned equipments without prior consultation with AUK Corp..**

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## SWITCHING REGULATOR APPLICATIONS

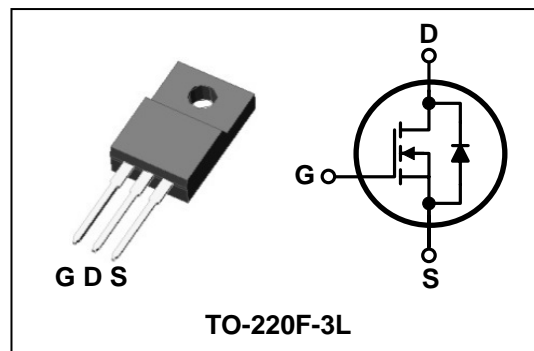
### Features

- High Voltage:  $BV_{DSS}=600V(\text{Min.})$
- Low  $C_{rSS}$  :  $C_{rSS}=9.7pF(\text{Typ.})$
- Low gate charge :  $Q_g=23nC(\text{Typ.})$
- Low  $R_{DS(on)}$  :  $R_{DS(on)}=1.2\Omega(\text{Max.})$

### Ordering Information

Type No.	Marking	Package Code
SMK0760F	SMK0760	TO-220F-3L

### PIN Connection



### Absolute maximum ratings

( $T_c=25^\circ\text{C}$ )

Characteristic	Symbol	Rating	Unit	
Drain-source voltage	$V_{DSS}$	600	V	
Gate-source voltage	$V_{GSS}$	$\pm 30$	V	
Drain current (DC) *	$I_D$	( $T_c=25^\circ\text{C}$ )	7	A
		( $T_c=100^\circ\text{C}$ )	3.16	A
Drain current (Pulsed) *	$I_{DM}$	28	A	
Drain power dissipation	$P_D$	30	W	
Avalanche current (Single) ②	$I_{AS}$	7	A	
Single pulsed avalanche energy ②	$E_{AS}$	325	mJ	
Avalanche current (Repetitive) ①	$I_{AR}$	7	A	
Repetitive avalanche energy ①	$E_{AR}$	21.7	mJ	
Junction temperature	$T_J$	150	$^\circ\text{C}$	
Storage temperature range	$T_{stg}$	-55~150		

\* Limited by maximum junction temperature

Characteristic	Symbol	Typ.	Max	Unit	
Thermal resistance	Junction-case	$R_{th(J-C)}$	-	4.16	$^\circ\text{C}/\text{W}$
	Junction-ambient	$R_{th(J-a)}$	-	62.5	

## Electrical Characteristics

(Tc=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit	
Drain-source breakdown voltage	$BV_{DSS}$	$I_D=250\mu A, V_{GS}=0$	600	-	-	V	
Gate threshold voltage	$V_{GS(th)}$	$I_D=250\mu A, V_{DS}=V_{GS}$	2.0	-	4.0	V	
Drain-source cut-off current	$I_{DSS}$	$V_{DS}=600V, V_{GS}=0V$	-	-	1	$\mu A$	
Gate leakage current	$I_{GSS}$	$V_{DS}=0V, V_{GS}=\pm 30V$	-	-	$\pm 100$	nA	
Drain-source on-resistance ④	$R_{DS(on)}$	$V_{GS}=10V, I_D=3.5A$	-	1.0	1.2	$\Omega$	
Forward transfer conductance ④	$g_{fs}$	$V_{DS}=10V, I_D=3.5A$	-	7.3	-	S	
Input capacitance	$C_{iss}$	$V_{GS}=0V, V_{DS}=25V$ $f=1MHz$	-	968	1290	pF	
Output capacitance	$C_{oss}$		-	105	140		
Reverse transfer capacitance	$C_{rss}$		-	9.7	12.9		
Turn-on delay time	$t_{d(on)}$	$V_{DD}=325V, I_D=7A$ $R_G=25\Omega$	-	18	-	ns	
Rise time	$t_r$		-	19	-		
Turn-off delay time	$t_{d(off)}$		③④	-	72		-
Fall time	$t_f$		-	28	-		
Total gate charge	$Q_g$	$V_{DS}=325V, V_{GS}=10V$ $I_D=7A$	-	23	42	nC	
Gate-source charge	$Q_{gs}$		③④	-	5.5		-
Gate-drain charge	$Q_{gd}$		-	9	-		

## Source-Drain Diode Ratings and Characteristics

(Tc=25°C)

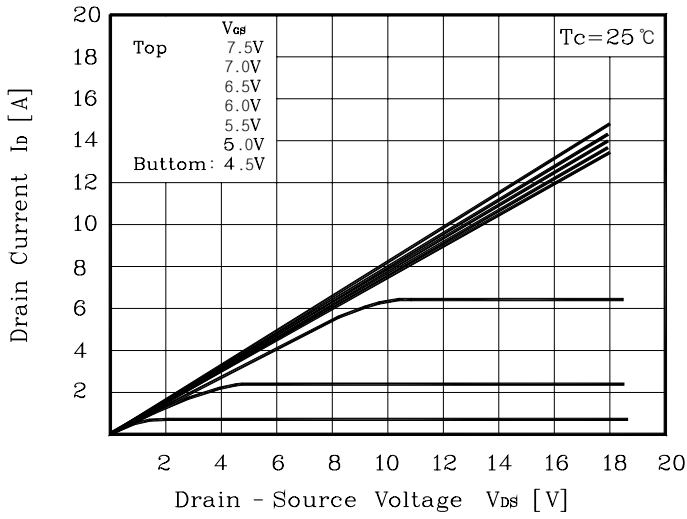
Characteristic	Symbol	Test Condition	Min	Typ	Max	Unit
Source current (DC)	$I_S$	Integral reverse diode in the MOSFET	-	-	7	A
Source current (Pulsed) ①	$I_{SM}$		-	-	28	
Forward voltage ④	$V_{SD}$	$V_{GS}=0V, I_S=7A$	-	-	1.4	V
Reverse recovery time	$t_{rr}$	$I_S=7A, V_{GS}=0,$ $di_S/dt=100A/\mu s$	-	365	-	ns
Reverse recovery charge	$Q_{rr}$		-	3.4	-	$\mu C$

Note ;

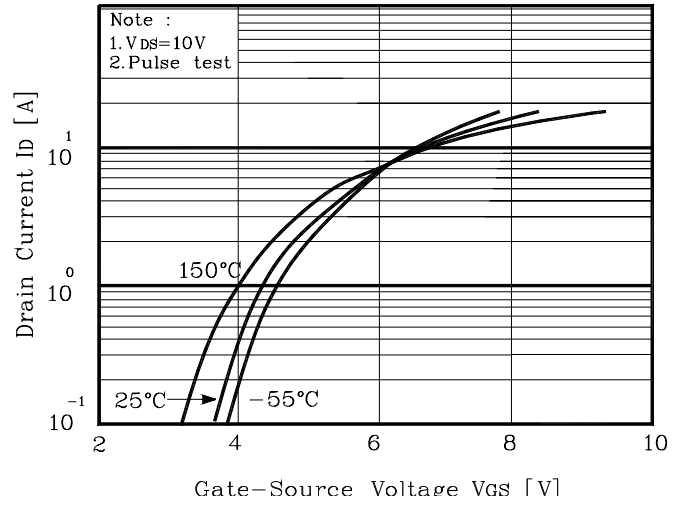
- ① Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature
- ②  $L=12.1mH, I_{AS}=7A, V_{DD}=50V, R_G=27\Omega$
- ③ Pulse Test : Pulse Width < 300us, Duty cycle  $\leq 2\%$
- ④ Essentially independent of operating temperature

## Electrical Characteristic Curves

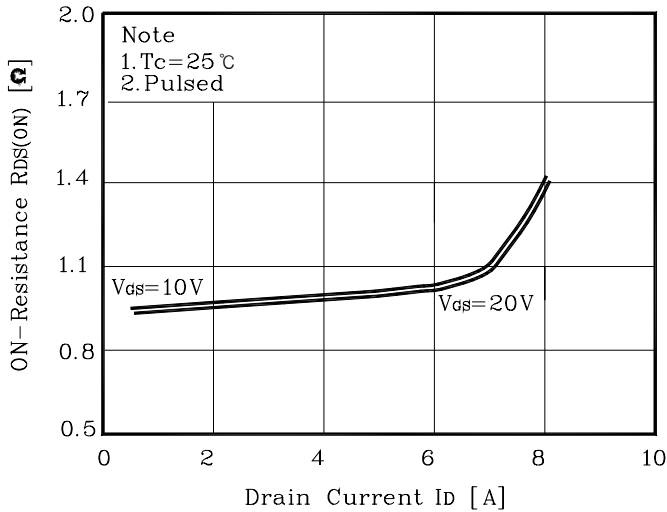
**Fig. 1  $I_D - V_{DS}$**



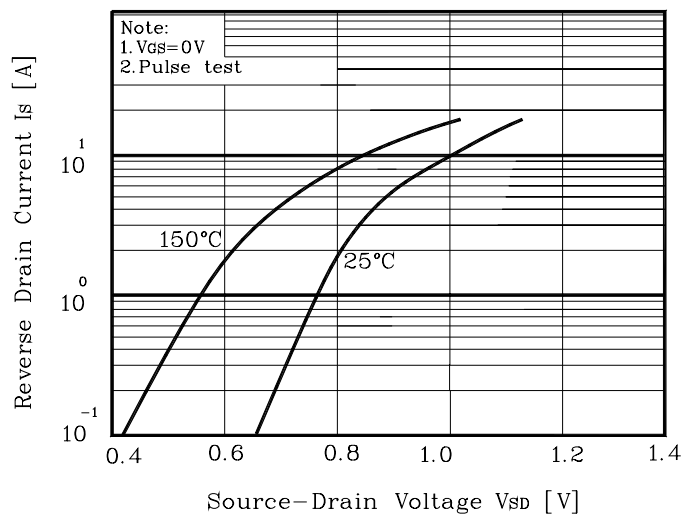
**Fig. 2  $I_D - V_{GS}$**



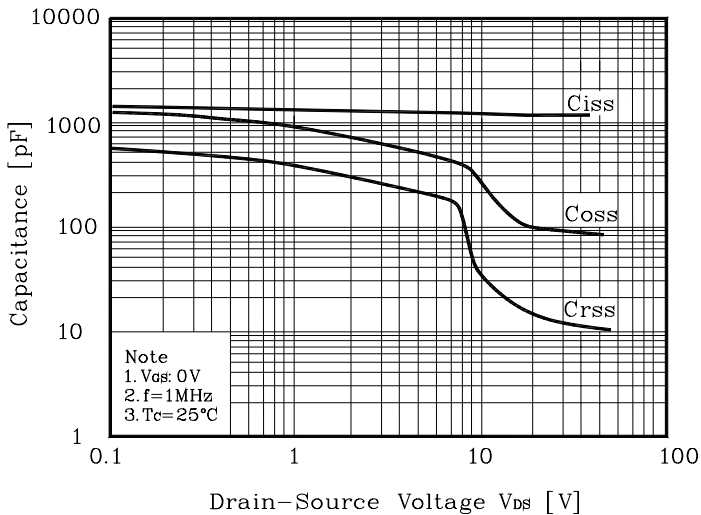
**Fig. 3  $R_{DS(on)} - I_D$**



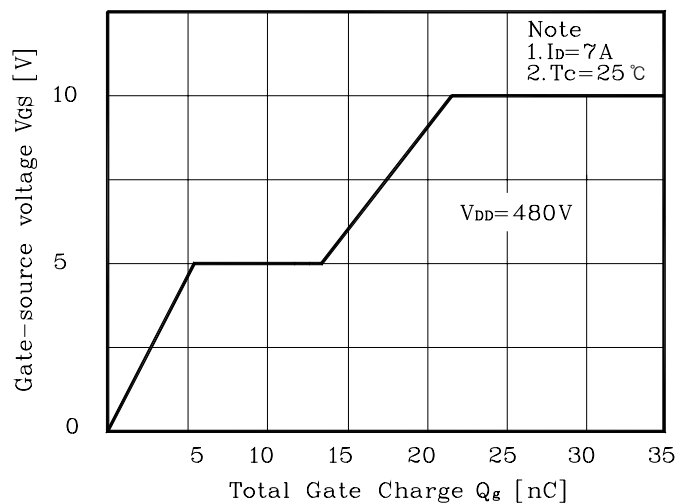
**Fig. 4  $I_S - V_{SD}$**



**Fig. 5 Capacitance -  $V_{DS}$**

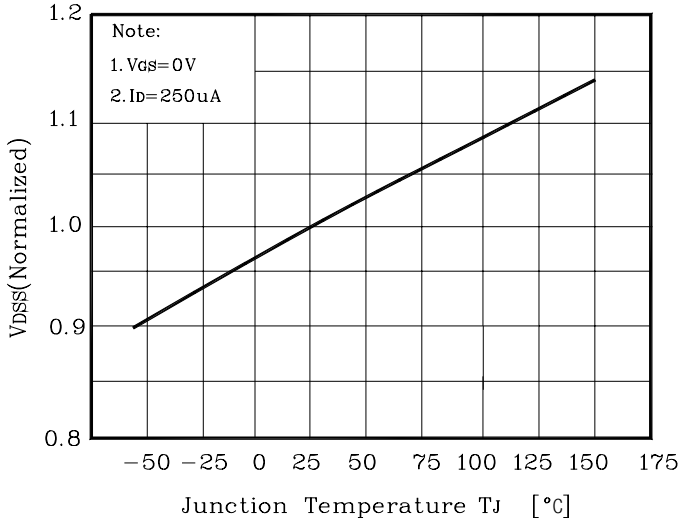


**Fig. 6  $V_{GS} - Q_G$**

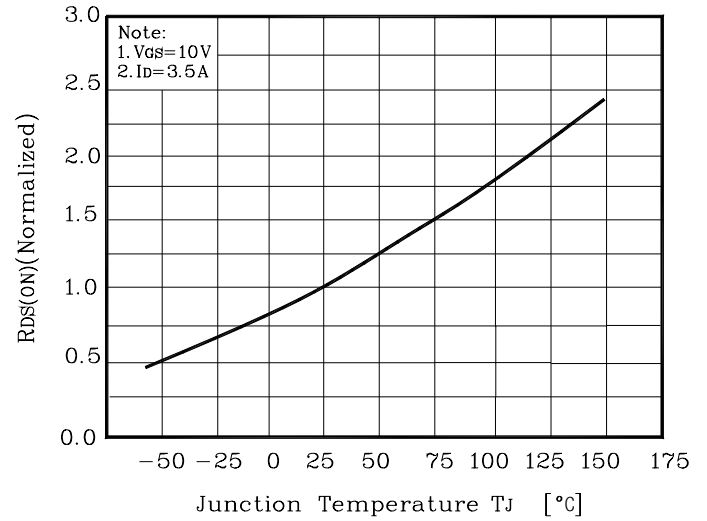


## Electrical Characteristic Curves

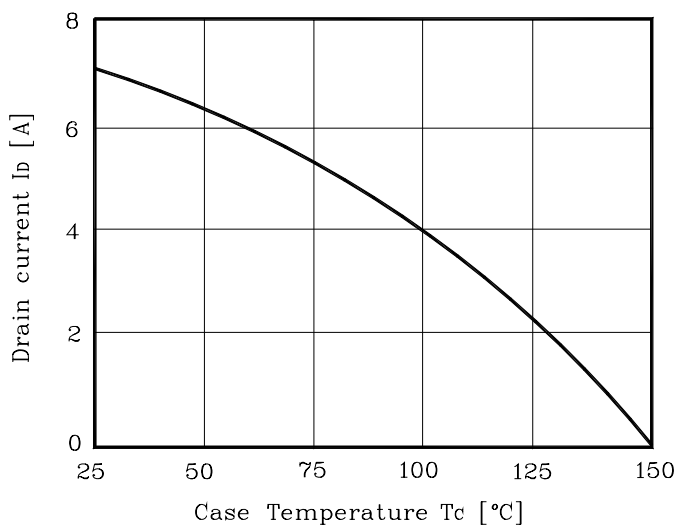
**Fig. 7  $V_{DSS} - T_J$**



**Fig.8  $R_{DS(on)} - T_J$**



**Fig. 9  $I_D - T_C$**



**Fig. 10 Safe Operating Area**

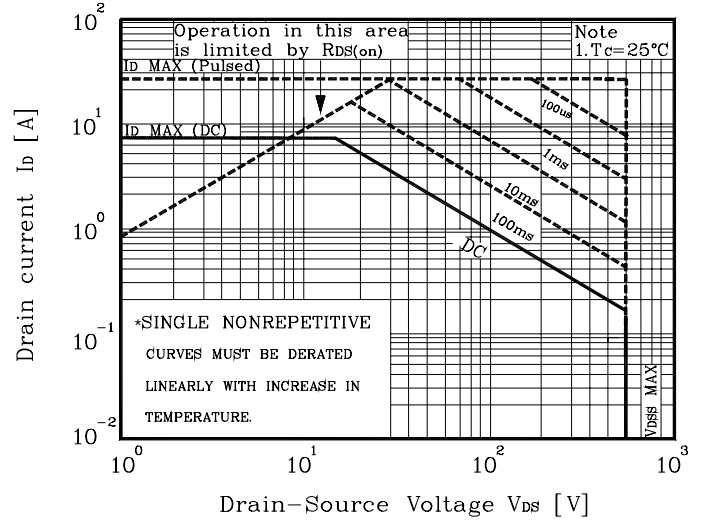


Fig. 10 Gate Charge Test Circuit & Waveform

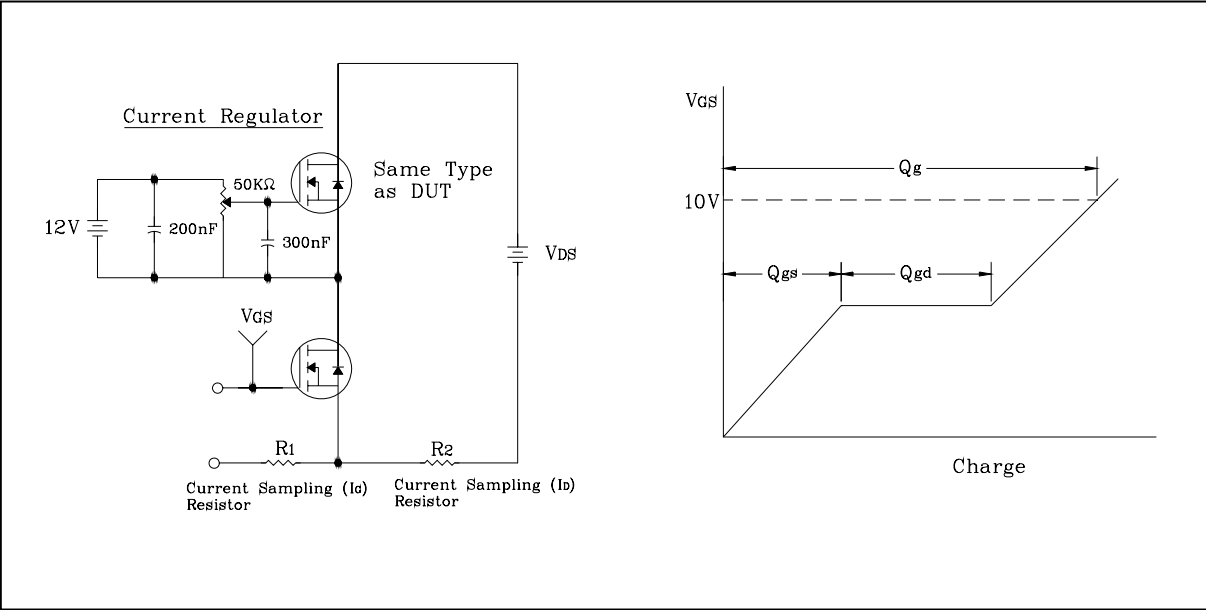


Fig. 11 Resistive Switching Test Circuit & Waveform

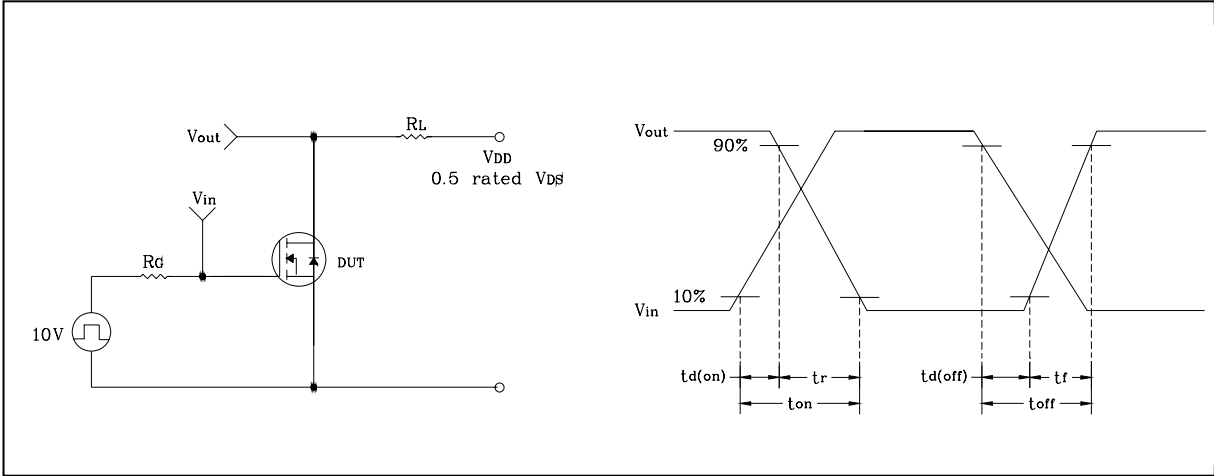


Fig. 12 E<sub>AS</sub> Test Circuit & Waveform

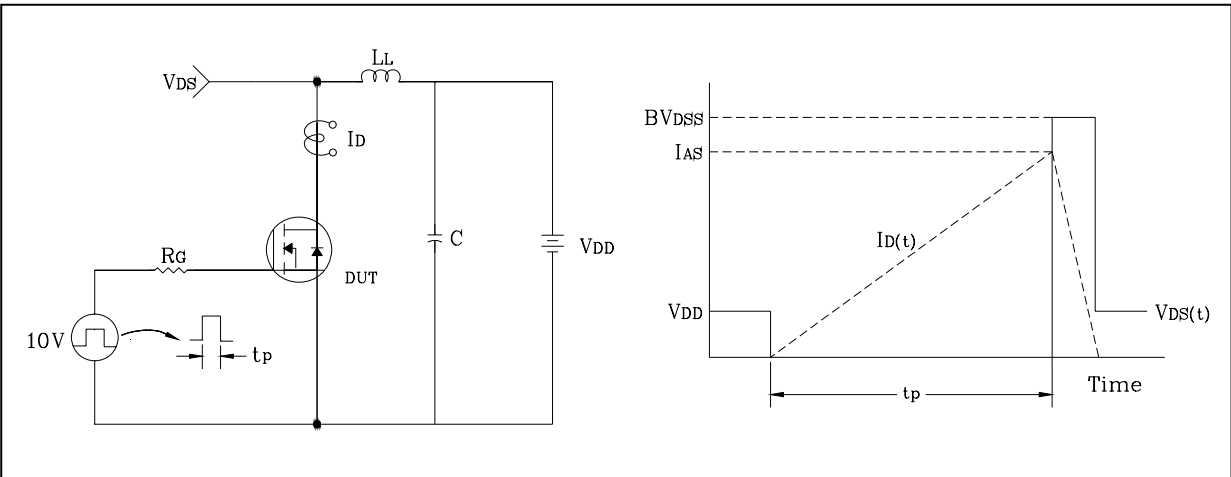
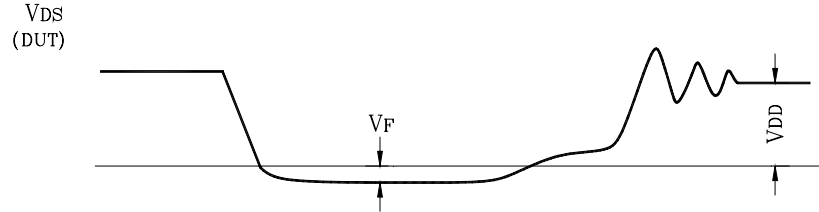
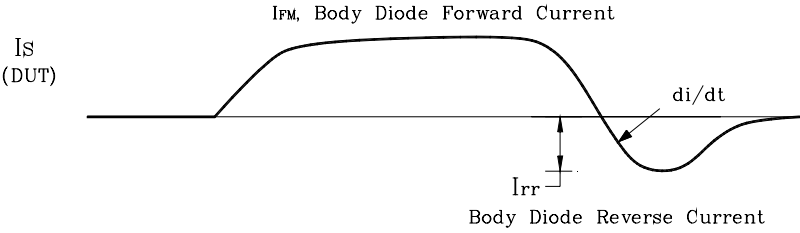
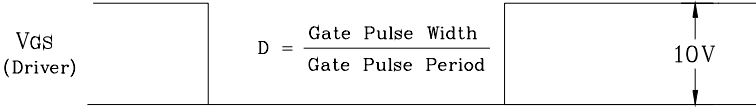
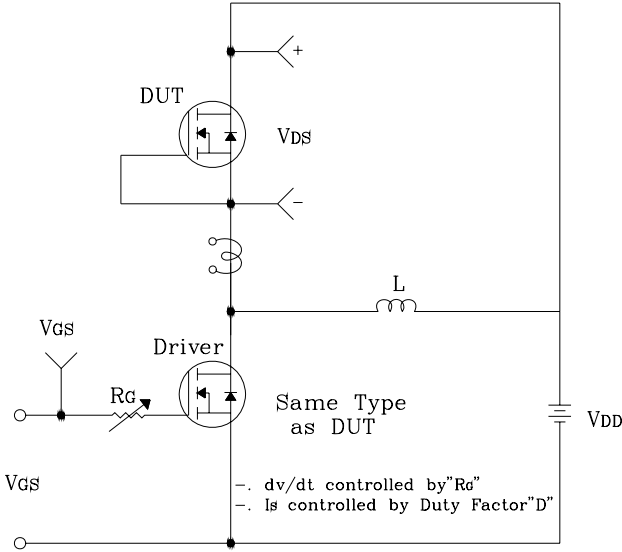
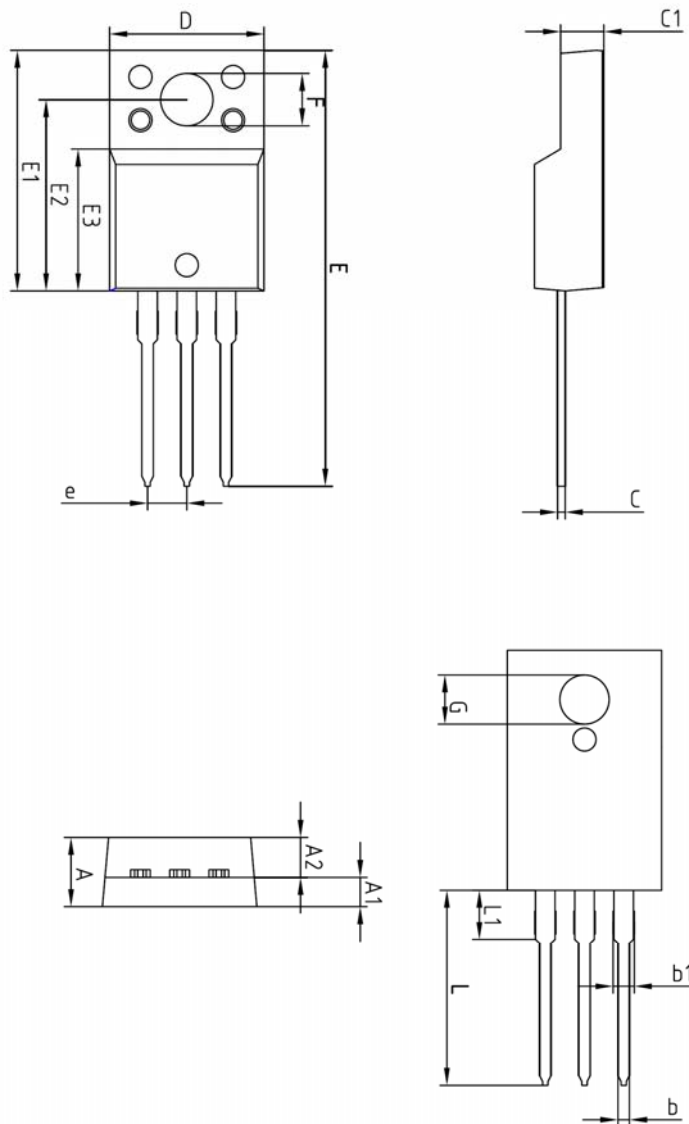


Fig. 13 Diode Reverse Recovery Time Test Circuit & Waveform



## Outline Dimensions



SYMBOL	MILLIMETERS			NOTE
	MINIMUM	NOMINAL	MAXIMUM	
A	-	-	4.60	
A1	2.45	2.50	2.55	
A2	1.95	2.00	2.05	
b	0.65	0.75	0.85	
b1	1.07	1.27	1.47	
C	0.40	0.50	0.60	
C1	2.70	2.80	2.90	
D	9.90	10.00	10.10	
E	28.00	-	28.60	
E1	15.50	15.60	15.70	
E2	12.30	12.40	12.50	
E3	9.15	9.20	9.25	
F	3.30	3.40	3.50	
G	3.10	3.20	3.30	
e	2.54 BSC			
L	12.40	-	13.00	
L1	3.46 BSC			

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