24 🛮 V_{CC}

23 Y1

22 Y2

21 Y3

18 Y6

16**∏** Y8

15 Y9

14 ¶ Y10

13 OE2

20 Y4 19 Y5

17 | Y7

DW OR NT PACKAGE

(TOP VIEW)

OE1

A1 🛮 2

А3

A4 🛮 5

A5 🛮 6

A6

A7 | 8

A8 🛮 9

A9 [] 10 A10 [] 11

GND 12

A2 | 3

[] 4

- Functionally Equivalent to AMD's AM29827 and AM29828
- 3-State Outputs Drive Bus Lines or Buffer Memory Address Registers
- pnp Inputs Reduce dc Loading
- Data Flow-Through Pinout (All Inputs on Opposite Side From Outputs)
- Power-Up High-Impedance State
- Package Options Include Plastic Small-Outline (DW) Packages and Standard Plastic (NT) 300-mil DIPs

description

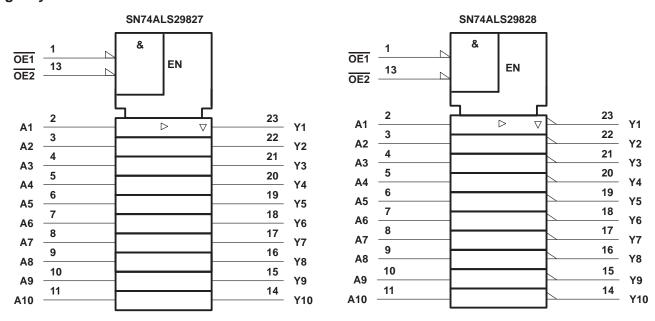
These 10-bit buffers and bus drivers provide high-performance bus interface for wide data paths or buses carrying parity.

The 3-state control gate is a 2-input NOR such that if either output-enable (OE1 or OE2) input is high, all ten outputs are in the high-impedance state.

The SN74ALS29827 provides true data and the SN74ALS29828 provides inverted data at their respective outputs.

The SN74ALS29827 and SN74ALS29828 are characterized for operation from 0°C to 70°C.

logic symbols†

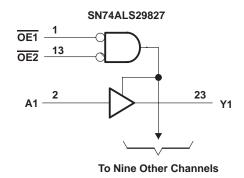


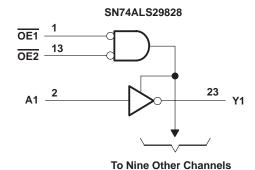
[†] These symbols are in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.



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logic diagrams (positive logic)





absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V _{CC}	7 V
Input voltage, V _I	5.5 V
Voltage applied to a disabled 3-state output	5.5 V
Operating free-air temperature range, T _A	0°C to 70°C
Storage temperature range	. −65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

recommended operating conditions

		SN74ALS29827 SN74ALS29828			UNIT
		MIN	NOM	MAX	
Vcc	Supply voltage	4.75	5	5.25	V
VIH	High-level input voltage	2			V
V _{IL}	Low-level input voltage			0.8	V
ІОН	High-level output current			-24	mA
l _{OL}	Low-level output current			48	mA
TA	Operating free-air temperature	0		70	°C



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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

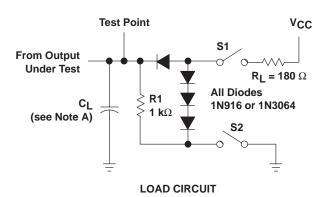
PARAMETER	TEST CON		SN74ALS29827 SN74ALS29828			
			MIN	TYP [†]	MAX	
VIK	$V_{CC} = 4.75 V,$	$I_{I} = -18 \text{ mA}$			-1.2	V
Veri	Voc - 4.75 V	$I_{OH} = -15 \text{ mA}$	2.4			V
VOH	V _{CC} = 4.75 V	$I_{OH} = -24 \text{ mA}$	2			V
VOL	$V_{CC} = 4.75 V,$	$I_{OL} = 48 \text{ mA}$		0.35	0.5	V
lozh	$V_{CC} = 5.25 V,$	V _O = 2.4 V			20	μΑ
lozL	$V_{CC} = 5.25 V,$	V _O = 0.4 V			-20	μΑ
ΙĮ	$V_{CC} = 5.25 V,$	V _I = 5.5 V			0.1	mA
lін	$V_{CC} = 5.25 V,$	V _I = 2.7 V			20	μΑ
I _{IL}	$V_{CC} = 5.25 V,$	V _I = 0.4 V			-0.1	mA
los [‡]	$V_{CC} = 5.25 V,$	V _O = 0	-75		-250	mA
Icc	V _{CC} = 5.25 V			25	40	mA

switching characteristics (see Figure 1)

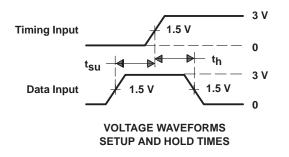
				V _{CC} = 4.75			
PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	SN74ALS29827	SN74ALS29828	UNIT	
	(1111 01)	(INFOT) (OUTFOT)		MIN MAX	MIN MAX		
t _{PLH}	А	V	0 000 = 5	15	14	no	
t _{PHL}	A	Y	C _L = 300 pF	15	14	ns	
t _{PLH}	А	V	0 50 5	8	7	no	
t _{PHL}	A	Υ	$C_L = 50 pF$	8	7.5	ns	
^t PZH		Y	C: 200 = E	20	20	ns	
t _{PZL}	ŌĒ	Y	C _L = 300 pF	23	23	115	
^t PZH	ŌĒ	Y	C: F0 % F	15	15	ns	
t _{PZL}	OE	Y	Y $C_L = 50 \text{ pF}$		15	IIS	
^t PHZ	ŌĒ	V	0. 50 *5	17	17	ns	
tPLZ	OE	Υ	C _L = 50 pF	12	12	115	
^t PHZ	ŌĒ	Y	C _L = 5 pF	9	9	ns	
^t PLZ	OE .	ſ	OL = 5 PF	9	9	115	

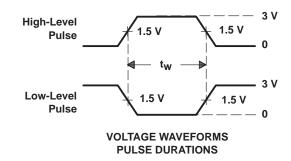
[†] All typical values are at V_{CC} = 5 V, T_A = 25°C. ‡ Not more than one output should be shorted at a time, and the duration of the short circuit should not exceed one second.

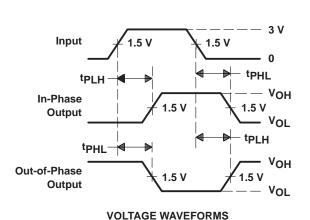
PARAMETER MEASUREMENT INFORMATION



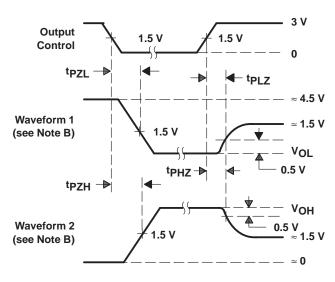
SWITCH POSITION TABLE							
TEST	S1	S2					
t _{PLH}	Closed	Closed					
tPHL	Closed	Closed					
tPZH	Open	Closed					
tPZL	Closed	Open					
tPHZ	Closed	Closed					
tPLZ	Closed	Closed					







PROPAGATION DELAY TIMES



VOLTAGE WAVEFORMS ENABLE AND DISABLE TIMES, 3-STATE OUTPUTS

- NOTES: A. C_L includes probe and jig capacitance.
 - B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
 - C. All input pulses are supplied by generators having the following characteristics: PRR \leq 10 MHz, $Z_O = 50 \Omega$, $t_f \leq 2.5$ ns. $t_f \leq 2.5$ ns.

Figure 1. Load Circuit and Voltage Waveforms





PACKAGE OPTION ADDENDUM

6-Feb-2020

PACKAGING INFORMATION

Orderable Device	Status	Package Type	_	Pins	_	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Op Temp (°C)	Device Marking	Samples
	(1)		Drawing		Qty	(2)	(6)	(3)		(4/5)	
SN74ALS29827DW	ACTIVE	SOIC	DW	24	25	Green (RoHS & no Sb/Br)	NIPDAU	Level-1-260C-UNLIM	0 to 70	ALS29827	Samples
SN74ALS29827DWG4	ACTIVE	SOIC	DW	24	25	Green (RoHS & no Sb/Br)	NIPDAU	Level-1-260C-UNLIM	0 to 70	ALS29827	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (CI) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead/Ball Finish Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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6-Feb-2020

DW (R-PDSO-G24)

PLASTIC SMALL OUTLINE



NOTES: A. All linear dimensions are in inches (millimeters). Dimensioning and tolerancing per ASME Y14.5M-1994.

- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).
- D. Falls within JEDEC MS-013 variation AD.



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