SCBS252A - SEPTEMBER 1988 - REVISED APRIL 1994

- State-of-the-Art BiCMOS Design Significantly Reduces I_{CCZ}
- ESD Protection Exceeds 2000 V Per MIL-STD-883C, Method 3015
- 3-State Outputs Drive Bus Lines or Buffer Memory Address Registers
- Package Options Include Plastic Small-Outline (D) Packages, Ceramic Chip Carriers (FK) and Flatpacks (W), and Standard Plastic and Ceramic 300-mil DIPs (J, N)

description

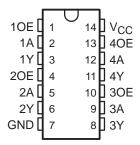
The 'BCT126A bus buffer features independent line drivers with 3-state outputs. Each output is disabled when the associated output-enable (OE) input is low.

The SN54BCT126A is characterized for operation over the full military temperature range of -55° C to 125°C. The SN74BCT126A is characterized for operation from 0°C to 70°C.

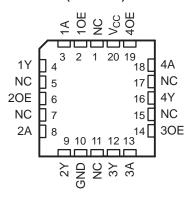
FUNCTION TABLE (each buffer)

INP	JTS	ОИТРИТ				
OE	Α	Υ				
Н	Н	Н				
Н	L	L				
L	Χ	Z				

SN54BCT126A . . . J OR W PACKAGE SN74BCT126A . . . D OR N PACKAGE (TOP VIEW)



SN54BCT126A . . . FK PACKAGE (TOP VIEW)



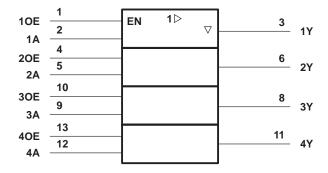
NC - No internal connection



SN54BCT126A, SN74BCT126A QUADRUPLE BUS BUFFER GATES WITH 3-STATE OUTPUTS

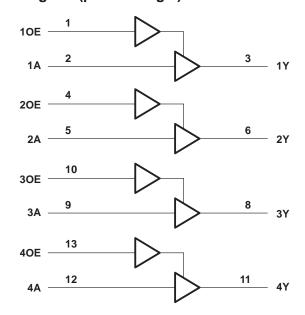
SCBS252A - SEPTEMBER 1988 - REVISED APRIL 1994

logic symbol†



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

logic diagram (positive logic)



Pin numbers shown are for the J, N, and W packages.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)‡

Supply voltage range, V _{CC}		– 0.5 V to 7 V
Input voltage range, V _I (see Note 1)		– 0.5 V to 7 V
Voltage range applied to any output in	the disabled or power-off state, VO	– 0.5 V to 5.5 V
Voltage range applied to any output in	the high state, VO	– 0.5 V to V _{CC}
Current into any output in the low state	: SN54BCT126A	96 mÅ
	SN74BCT126A	128 mA
Operating free-air temperature range:	SN54BCT126A	– 55°C to 125°C
	SN74BCT126A	0°C to 70°C
Storage temperature range		– 65°C to 150°C

[‡] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

recommended operating conditions

		SN5	4BCT12	6A	SN7	UNIT		
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.5	5	5.5	V
VIH	High-level input voltage	2			2			V
V _{IL}	Low-level input voltage			0.8			0.8	V
liK	Input clamp current			-18			-18	mA
loh	High-level output current			-12			-15	mA
loL	Low-level output current			48			64	mA
TA	Operating free-air temperature	-55		125	0		70	°C



SCBS252A - SEPTEMBER 1988 - REVISED APRIL 1994

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETED	7.5	SET CONDITIONS	SN:	54BCT12	6A	SN74BCT126A			UNIT	
PARAMETER	"	ST CONDITIONS	MIN	TYP†	MAX	MIN	TYP	MAX	CIVIT	
VIK	V _{CC} = 4.5 V,	I _I = -18 mA			-1.2			-1.2	V	
		$I_{OH} = -3 \text{ mA}$	2.4	3.3		2.4	3.3			
Voн	V _{CC} = 4.5 V	$I_{OH} = -12 \text{ mA}$	2	3.2					V	
		$I_{OH} = -15 \text{ mA}$				2	3.1			
\/o;	V _{CC} = 4.5 V	$I_{OL} = 48 \text{ mA}$		0.38	0.55				V	
VOL	VCC = 4.5 V	$I_{OL} = 64 \text{ mA}$					0.42	0.55	V	
lį	$V_{CC} = 0$,	V _I = 7 V			0.1			0.1	mA	
lН	V _{CC} = 5.5 V,	V _I = 2.7 V			35			25	μΑ	
Ι _{ΙL}	$V_{CC} = 5.5 \text{ V},$	V _I = 0.5 V			-20			-20	μΑ	
lozh	$V_{CC} = 5.5 V$,	$V_0 = 2.7 \text{ V}$			50			50	μΑ	
lozL	V _{CC} = 5.5 V,	V _O = 0.5 V			-50			-50	μΑ	
los [‡]	V _{CC} = 5.5 V,	V _O = 0	-100		-225	-100		-225	mA	
Іссн	V _{CC} = 5.5 V,	Outputs open		21	33		21	33	mA	
ICCL	V _{CC} = 5.5 V,	Outputs open		35	51		35	51	mA	
I _{CCZ}	$V_{CC} = 5.5 \text{ V},$	Outputs open		5	10		5	10	mA	
C _i	V _{CC} = 5 V,	V _I = 2.5 V or 0.5 V		4			4		pF	
Co	V _{CC} = 5 V,	V _O = 2.5 V or 0.5 V		9			9		pF	

switching characteristics (see Note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	V_{CC} = 5 V, C_{L} = 50 pF, R1 = 500 Ω, R2 = 500 Ω, T_{A} = 25°C			V_{CC} = 4.5 V to 5.5 V, C_L = 50 pF, R1 = 500 Ω , R2 = 500 Ω , T_A = MIN to MAX§				UNIT					
			'BCT126A			SN54BCT126A		SN74BCT126A							
			MIN	TYP	MAX	MIN	MAX	MIN	MAX						
t _{PLH}	А	V	1.5	3.6	4.9	1.5	5.6	1.5	6.3	ns					
t _{PHL}		Α	ı	'	'	2.7	5.3	6.9	2.7	7.7	2.7	7.4	115		
^t PZH	OE	OF	OF	05	OF	OF	V	2.6	4.8	6.4	2.6	7.2	2.6	7.9	ns
t _{PZL}		I	ſ	ı	3.7	6.4	8.3	3.7	10.5	3.7	10	115			
^t PHZ	OE	V	3.2	6.6	8.2	3.2	9.6	3.2	10	ns					
t _{PLZ}			3.4	6.5	8	3.4	12.3	3.4	10.7	115					

[§] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



[†] All typical values are at V_{CC} = 5 V. ‡ Not more than one output should be tested at a time, and the duration of the test should not exceed one second.





9-Mar-2021

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead finish/ Ball material	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
5962-9088901M2A	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	5962- 9088901M2A SNJ54BCT 126AFK	Samples
5962-9088901MCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	5962-9088901MC A SNJ54BCT126AJ	Samples
5962-9088901MDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	5962-9088901MD A SNJ54BCT126AW	Samples
SN54BCT126AJ	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SN54BCT126AJ	Samples
SNJ54BCT126AFK	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	5962- 9088901M2A SNJ54BCT 126AFK	Samples
SNJ54BCT126AJ	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	5962-9088901MC A SNJ54BCT126AJ	Samples
SNJ54BCT126AW	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	5962-9088901MD A SNJ54BCT126AW	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

⁽²⁾ RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".



PACKAGE OPTION ADDENDUM

9-Mar-2021

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead finish/Ball material Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.

Important Information and Disclaimer: The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

FK (S-CQCC-N**)

LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a metal lid.
- D. Falls within JEDEC MS-004



W (R-GDFP-F14)

CERAMIC DUAL FLATPACK



NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only.
- E. Falls within MIL STD 1835 GDFP1-F14



CERAMIC DUAL IN LINE PACKAGE



Images above are just a representation of the package family, actual package may vary. Refer to the product data sheet for package details.

4040083-5/G





CERAMIC DUAL IN LINE PACKAGE



NOTES:

- 1. All controlling linear dimensions are in inches. Dimensions in brackets are in millimeters. Any dimension in brackets or parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
- 2. This drawing is subject to change without notice.
- 3. This package is hermitically sealed with a ceramic lid using glass frit.
- His package is remitted by sealed with a ceramic its using glass mit.
 Index point is provided on cap for terminal identification only and on press ceramic glass frit seal only.
 Falls within MIL-STD-1835 and GDIP1-T14.



CERAMIC DUAL IN LINE PACKAGE



IMPORTANT NOTICE AND DISCLAIMER

TI PROVIDES TECHNICAL AND RELIABILITY DATA (INCLUDING DATASHEETS), DESIGN RESOURCES (INCLUDING REFERENCE DESIGNS), APPLICATION OR OTHER DESIGN ADVICE, WEB TOOLS, SAFETY INFORMATION, AND OTHER RESOURCES "AS IS" AND WITH ALL FAULTS, AND DISCLAIMS ALL WARRANTIES, EXPRESS AND IMPLIED, INCLUDING WITHOUT LIMITATION ANY IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS.

These resources are intended for skilled developers designing with TI products. You are solely responsible for (1) selecting the appropriate TI products for your application, (2) designing, validating and testing your application, and (3) ensuring your application meets applicable standards, and any other safety, security, or other requirements. These resources are subject to change without notice. TI grants you permission to use these resources only for development of an application that uses the TI products described in the resource. Other reproduction and display of these resources is prohibited. No license is granted to any other TI intellectual property right or to any third party intellectual property right. TI disclaims responsibility for, and you will fully indemnify TI and its representatives against, any claims, damages, costs, losses, and liabilities arising out of your use of these resources.

TI's products are provided subject to TI's Terms of Sale (https://www.ti.com/legal/termsofsale.html) or other applicable terms available either on ti.com or provided in conjunction with such TI products. TI's provision of these resources does not expand or otherwise alter TI's applicable warranties or warranty disclaimers for TI products.

Mailing Address: Texas Instruments, Post Office Box 655303, Dallas, Texas 75265 Copyright © 2021, Texas Instruments Incorporated