



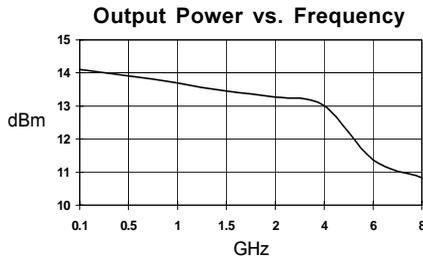
Product Description

Sirenza Microdevices' SNA-186 is a GaAs monolithic broadband amplifier (MMIC) housed in a low-cost surface-mountable plastic package. At 1950 MHz, this amplifier provides 12dB of gain and +13dBm of P1dB power when biased at 50mA.

The use of an external resistor allows for bias flexibility and stability. These unconditionally stable amplifiers are designed for use as general purpose 50 ohm gain blocks.

Also available in chip form (SNA-100), its small size (0.33mm x 0.33mm) and gold metallization makes it an ideal choice for use in hybrid circuits.

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SNA-186

DC-8 GHz, Cascadable GaAs HBT MMIC Amplifier



Product Features

- Patented, Reliable GaAs HBT Technology
- Cascadable 50 Ohm Gain Block
- 12dB Gain, +13dBm P1dB
- 1.5:1 Input and Output VSWR
- Operates From a Single DC Supply
- Low Cost Surface Mount Plastic Package

Applications

- PA Driver Amplifier
- Cellular, PCS, GSM, UMTS
- IF Amplifier
- Wireless Data, Satellite

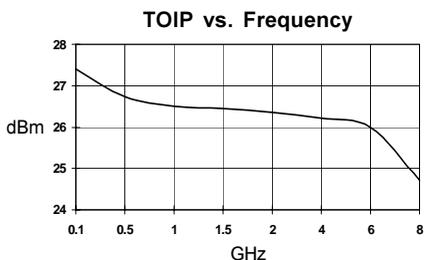
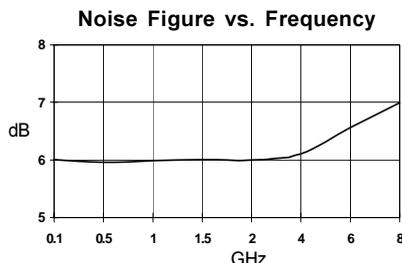
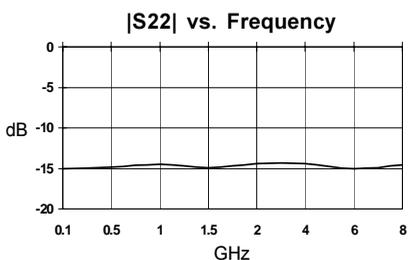
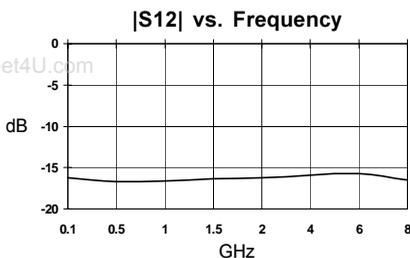
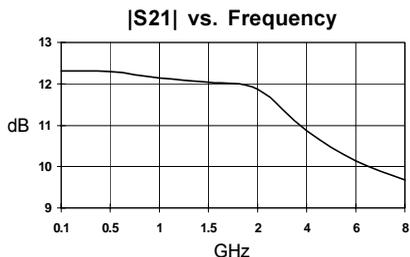
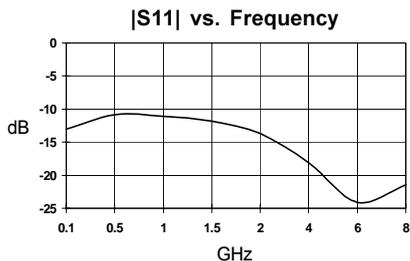
Symbol	Parameter	Units	Frequency	Min.	Typ.	Max.
G _p	Small Signal Power Gain	dB	850 MHz	11.5	12.5	13.8
		dB	1950 MHz		12.0	
		dB	2400 MHz		11.8	
G _F	Gain Flatness	dB	0.1-8 GHz		+/- 0.5	
BW _{3dB}	3dB Bandwidth	GHz			10.0	
P _{1dB}	Output Power at 1dB Compression	dBm	1950 MHz		13.0	
OIP ₃	Output Third Order Intercept Point	dBm	1950 MHz		26.0	
NF	Noise Figure	dB	1950 MHz		6.0	
VSWR	Input / Output	-	0.1-10 GHz		1.5:1	
ISOL	Reverse Isolation	dB	0.1-10 GHz		16	
V _D	Device Operating Voltage	V		3.3	3.8	4.3
I _D	Device Operating Current	mA		45	50	55
dG/dT	Device Gain Temperature Coefficient	dB/°C			-0.0015	
R _{TH, j-l}	Thermal Resistance (junction to lead)	°C/W			350	

Test Conditions: V_S = 8 V, I_D = 50 mA Typ.
R_{BIAS} = 82 Ohms, T_L = 25°C

OIP₃ Tone Spacing = 1 MHz, Pout per tone = 0 dBm
Z_S = Z_L = 50 Ohms

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Typical Performance at 25°C (V_{ds} = 3.8V, I_{ds} = 50mA)



Absolute Maximum Ratings

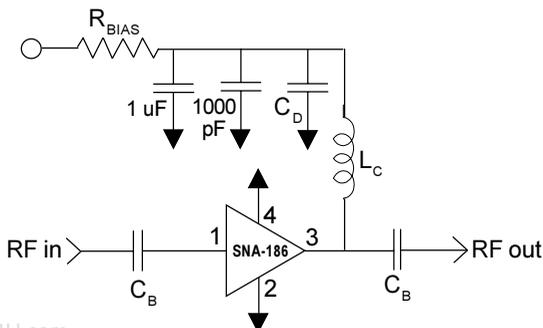
Parameter	Absolute Limit
Max. Device Current (I _D)	70 mA
Max. Device Voltage (V _D)	6 V
Max. RF Input Power	+10 dBm
Max. Junction Temp. (T _J)	+150°C
Operating Temp. Range (T _L)	-40°C to +85°C
Max. Storage Temp.	+150°C

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one.

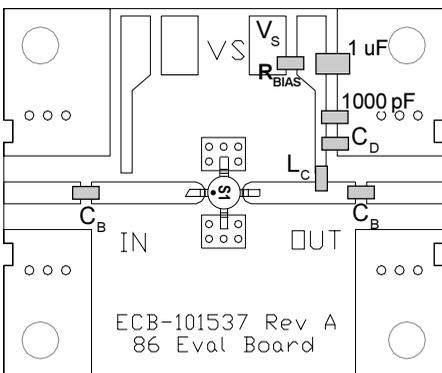
Bias Conditions should also satisfy the following expression:

$$I_D V_D < (T_J - T_L) / R_{TH} j-I$$

Typical Application Circuit

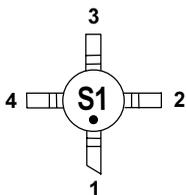


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Part Identification Marking

The part will be marked with an "S1" designator on the top surface of the package.



Caution: ESD sensitive

Appropriate precautions in handling, packaging and testing devices must be observed.

Application Circuit Element Values

Reference Designator	Frequency (Mhz)				
	500	850	1950	2400	3500
C_B	220 pF	100 pF	68 pF	56 pF	39 pF
C_D	100 pF	68 pF	22 pF	22 pF	15 pF
L_C	68 nH	33 nH	22 nH	18 nH	15 nH

Recommended Bias Resistor Values for $I_D=50mA$

Supply Voltage (V_S)	6 V	8 V	10 V	12 V
R_{BIAS}	43 Ω	82 Ω	120 Ω	160 Ω

Note: R_{BIAS} provides DC bias stability over temperature.

Mounting Instructions

1. Use a large ground pad area under device pins 2 and 4 with many plated through-holes as shown.
2. We recommend 1 or 2 ounce copper. Measurements for this data sheet were made on a 31 mil thick FR-4 board with 1 ounce copper on both sides.

Pin #	Function	Description
1	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
2, 4	GND	Connection to ground. For optimum RF performance, use via holes as close to ground leads as possible to reduce lead inductance.
3	RF OUT/BIAS	RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.

Part Number Ordering Information

Part Number	Reel Size	Devices/Reel
SNA-186-TR1	7"	1000
SNA-186-TR2	13"	3000
SNA-186-TR3	13"	5000

