HITACHI

KAOHSIUNG HITACHI
ELECTRONICS CO.,LTD
P.O. BOX 26-27
2,13TH EAST ST. K.E.P.Z.
KAOHSIUNG TAIWAN R.O.C.
TEL:(07) 8215811 (7 LINE)
FAX:(07) 8215815

FOR MESSRS:	DATE	Mar.25,2010

CUSTOMER'S ACCEPTANCE SPECIFICATIONS SP14N001-Z1 CONTENTS

No.	ITEM	SHEET No.	PAGE
1	COVER	7B64PS 2701- SP14N001-Z1-7	1-1/1
2	RECORD OF REVISION	7B64PS 2702- SP14N001-Z1-7	2-1/1
3	GENERAL SPECIFICATION	7B64PS 2703- SP14N001-Z1-7	3-1/1
4	ABSOLUTE MAXIMUM RATINGS	7B64PS 2704- SP14N001-Z1-7	4-1/1
5	ELECTRICAL CHARACTERISTICS	7B64PS 2705- SP14N001-Z1-7	5-1/1
6	OPTICAL CHARACTERISTICS	7B64PS 2706- SP14N001-Z1-7	6-1/2~2/2
7	BLOCK DIAGRAM	7B64PS 2707- SP14N001-Z1-7	7-1/1
8	INTERFACE TIMING	7B64PS 2708- SP14N001-Z1-7	8-1/2~2/2
9	OUTLINE DIMENSIONS	7B63PS 2709- SP14N001-Z1-7	9-1/3
		7B64PS 2709- SP14N001-Z1-7	9-2/3~3/3
10	APPEARANCE STANDARD	7B64PS 2710- SP14N001-Z1-7	10-1/3~3/3
11	PRECAUTION IN DESIGN	7B64PS 2711- SP14N001-Z1-7	11-1/2~2/2
12	DESIGNATION OF LOT MARK	7B64PS 2712- SP14N001-Z1-7	12-1/1
13	PRECAUTION FOR USE	7B64PS 2713- SP14N001-Z1-7	13-1/1

- * When product will be discontinued, customer will be informed by HITACHI with twelve months prior announcement.
- * This product is inhibited to apply in any life support instrument.

ACCEPTED BY;		PROPOSED BY;	Kenlh	en_
KAOHSIUNG HITACHI ELECTRONICS CO.,LTD.	Sh. No.	7B64PS 2701-SP14N001-Z1-7	PAGE	1-1/1

RECORD OF REVISION

DATE	SHEET No.			SUMMARY					
Sep.05,'01	7B64PS 2709-	Change	d :						
	SP14N001-Z1-2		N Functions						
	PAGE 9-3/3		Connector : MOLEX/52103-2617→MOLEX/52207-2690						
Nov.27,'01	7B63PS 2709-	Change		NO 4 00 00 4					
	SP14N001-Z1-3	CN1 P	CN1 PIN Direction NO.1 \rightarrow 26; 26 \rightarrow 1						
A 4 10 4	PAGE 9-1/3	01							
Apr.14,'04	7B63PS 2709-	Change		langth (EO) (EC)					
	SP14N001-Z1-4 PAGE 9-1/3	Revise	u . CFL Cable	length (50) \rightarrow (56)					
May.28,'07	7B64PS 2709-	93 Inte	rnal Pin Con	nection					
Way.20, 07	SP14N001-Z1-5	Change		ncotion					
	Page 9-3/3			163M83 – 04 → JAE IL-	G-4S-	S3C2-SA			
	7B64PS 2712-		SIGNATION (OF LOT MARK					
	SP14N001-Z1-5 Page 12-1/1	Added	REV No.	ITEM	L	OT No.			
	1. 490 12 1/1			CFL I/F Connector :					
			-	Mitsumi M63M83 - 04		-			
				CFL I/F Connector :					
			Α	JAE IL-G-4S-S3C2-SA		7102T			
		'	-		Į.		⊒		
Sep.11,'09	7B64PS 2712-		SIGNATION (OF LOT MARK					
	SP14N001-Z1-5	Added	REV No.	ITEM	1,	_OT No.			
	Page 12-1/1					_O1 NO.			
			В	M count IC change		-			
Mar.25,'10	7B64PS 2703-		RAL SPECIF	FICATIONS					
	SP14N001-Z1-7	Change		2222 / TOOLUDA					
	Page 3-1/1	(11) LCL	Controller 10	6963C / TOSHIBA					
			Т	√ 6963C equivalent					
	7B64PS 2712-	12. DES		OF LOT MARK					
	SP14N001-Z1-7	Added							
	Page 12-1/1		REV No.	ITEM	NO	TE			
			С	Controller IC Change	PCNC	768			
	<u> </u>								

Sh.

No.

7B64PS 2702- SP14N001-Z1-7

DATE | Mar.25,'10

PAGE | 2-1/1

KAOHSIUNG HITACHI

ELECTRONICS CO.,LTD.

3. GENERAL SPECIFICATIONS

(1) Part Name SP14N001-Z1

(2) Outer Dimensions 159.4(W)mm x 101.0(H)mm x 11.0(D) mm (max.)

(3) Effective Display Area 123 mm min. x 68 mm min.

(4) Dot Size 0.48(W)min. x 0.48(H)min.

(5) Dot Pitch 0.50(W)mm x 0.50(H)mm

(6) Dot Number (Resolution) 240 (W) x 128 (H)

(7) Duty Ratio 1/128

(8) LCD Type Transmissive type F-STN

With anti-glare type upper polarizer

(9) Viewing Direction 6 O'clock

(10) Back Light Type Cold cathode fluorescent lamp.

(11) LCD Controller T6963C equivalent

4. ABSOLUTE MAXIMUM RATINGS

4.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS.

VSS=0V:STANDARD

ITEM	SYMBOL	MIN.	MAX.	UNIT	COMMENT
Power Supply For Logic	VDD-VSS	0	7.0	V	
Input Voltage	Vi	-0.3	VDD+0.3	V	(Note 1)
Input Current	li	0	1	Α	
Static Electricity	VESD0	ı	±100	V	(Note 1,2,3)
	VESD1	-	±10	K۷	(Note 1,2,4)

Note 1: Make certain you are grounded when handling LCM.

Note 2 : Energy storage capacitance 200pF , discharge resistance 250 Ω Ta=25 $^{\circ}$ C , 60%RH.

Note 3: Contact discharge to I/F connector pins.

Note 4: Contact discharge to front metal bezel.

4.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.

ITEM	OPER.	ATING	STO	RAGE	COMMENT
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-10°C	60 ℃	-20 ℃	70 ℃	(Note 2,3)
Humidity	(Not	te 1)	(No	te 1)	without condensation
		2.45m/s ²		11.76m/s ²	
Vibration	-	(0.25G)	-	(1.2G)	(Note 4)
				(Note 5)	1h max.
		29.4m/s ²		490.0m/s ²	
Shock	-	(3 G)	-	(50 G)	XYZ directions
				(Note 5)	
Corrosive Gas	Not Accep	table	Not Accep	table	

Note 1 : $Ta \le 40^{\circ}C$: 85%RH max.

Ta>40°C: Absolute humidity must be lower.

Than the humidity of 85%RH at 40°C

Note 2 : Ta at -20° C -----< 48h, at 60° C < 168h.

Note 3: Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Note 4:5Hz~100Hz (except resonance frequency)

Note 5: This module should be operated normally after finishing the test.

Need to make sure of value of the characteristics of inverter.

Also the response time at 0°C will be slower.

Note 7 : There are possibility that color non-uniformity happened while operating at over 40°C .

KAOHSIUNG HITACHI	DATE	Mar 25 '10	Sh.	7B64PS 2704- SP14N001-Z1-7	DAGE	<i>1</i> _1/1
ELECTRONICS CO.,LTD.	DATE	Mar.25,'10	No.	7864P3 2704- 3P14N001-21-7	FAGE	4-1/1

5. ELECTRICAL CHARACTERISTICS

5.1 ELECTRICAL CHARACTERISTICS

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage For Logic	VDD-VSS	-	(4.75)	5.0	(5.25)	V
LC driver Circuit Power Supply Voltage	VEE-VSS	-	-15.5	-15.0	-14.5	V
Input Voltage	VI	H LEVEL	0.8VDD	-	VDD	V
		L LEVEL	0	-	0.2VDD	V
Power Supply Current For Logic (Note 1)	IDD	VDD-VSS=5.0V	-	(11.7)	(14.0)	mA
Power Supply Current For LCD (Note 1)	IEE	VDD-VSS=5.0V	-	(2.5)	(4.0)	mA
Recommended		Ta= 0° C , $\phi = 0^{\circ}$	-	(16.9)	-	V
LC Driving Voltage (Note 2)	VDD-V0	Ta=25 $^{\circ}$ C , ϕ =0 $^{\circ}$	-	(15.8)	-	V
		Ta=50 $^{\circ}$ C , ϕ =0 $^{\circ}$	-	(15.2)	-	V

Note 1 : VDD-V0=(15.8)V , Ta=25°C

Note 2 : Recommended LC driving voltage may fluctuate about ±1.0V by each module test pattern is all "Q".

5.2 ELECTRICAL CHARACTERISTICS OF BACKLIGHT

		-: : • : : : : :				
ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT	NOTE
Lamp Voltage	VL	ı	(300)	1	Vrms	Ta=25°ℂ
Frequency	fL	ı	(70)	(85)	kHz	Ta=25°ℂ
Lamp Current	IL	(4)	(5)	(6)	mArms	Ta=25°ℂ
Starting Discharge Voltage	VS (Note 2)	(1000)	-	-	Vrms	Ta=25°ℂ

Please certainly inform HITACHI before designing lamp drive circuit according to the above specifications.

- Note 1 : Please make sure that your inverter is designed to meet the above specifications.
- Note 2: Starting discharge voltage is increased when LCM is operating at lower temperature.

 Please check the characteristics of your inverter before applying to your set.
- Note 3 : Average life time of CFL will be decreased when LCM is operating at lower temperature.
- Note 4: Under lower driving frequency of an inverter, a certain backlight system (CFL & CFL reflection sheet) may generate a sound noise.
- Note 5: When IL Is used over 5.5mA, it may cause uneven contrast near CFL location, due to heat dispersion from CFL.

KAOHSIUNG HITACHI	DATE	Mar.25,'10	Sh.	7B64PS 2705- SP14N001-Z1-7	DAGE	5-1/1
ELECTRONICS CO.,LTD.	DAIL	IVIAI.23, 10	No.	15041 3 2103-31 14N001-21-1	I YOL	J- 1/ 1

6. OPTICAL CHARACTERISTICS

6.1 OPTICAL CHARACTERISTICS

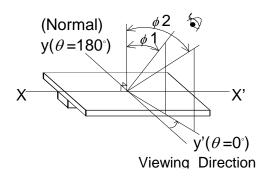
Ta=25°C (Backlight on)

ITEM	SYMBOL	CONDITIONAL	MIN.	TYP.	MAX.	UNIT	NOTE
Viewing Area	φ 2- <i>φ</i> 1	K≧2.0	ı	40	-	deg	1,2
Contrast Ratio	K	ϕ =0°, θ =0°	ı	(20)	-	ı	3
Response Time (Rise)	tr	ϕ =0°, θ =0°	-	(120)	-	ms	4
Response Time (Fall)	tf	ϕ =0°, θ =0°	-	(150)	-	ms	4

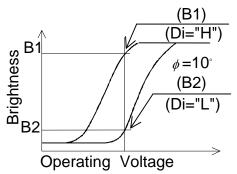
(Measure condition by HITACHI)

Note 3: Definition of contrast "K"

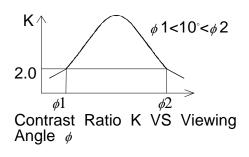
Note 1 : Definition of θ and ϕ

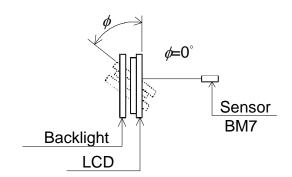


K= Brightness on selected dot (B1)
Brightness on non-selected dot (B2)

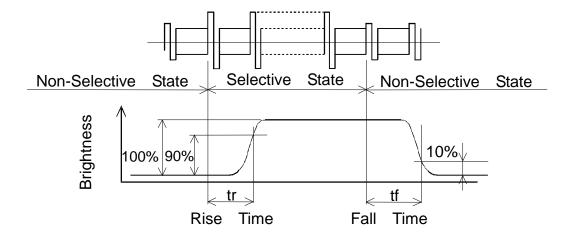


Note 2 : Definition of viewing angle ϕ 1 and ϕ 2.





Note 4: Definition of optical response



KAOHSIUNG HITACHI	DATE	Mar.25,'10	Sh.	7B64PS 2706- SP14N001-Z1-7	DAGE	6-1/2
ELECTRONICS CO.,LTD.	DATE		No.	7604PS 2700- SP14N001-21-7	FAGE	0-1/2

6.2 OPTICAL CHARACTERISTICS OF BACKLIGHT

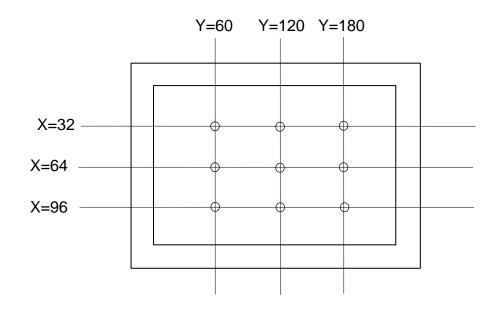
ITEM	MIN.	TYP.	MAX.	UNIT	NOTE	
Brightness	(120)	150		cd/m²	IL=(5mA)	
	(120)	150	-	(Note 1,2)		
Rise Time		E		Minute	IL=(5mA)	
	-	5	-	wiiilute	Brightness 80%	
Brightness Uniformity		+30 %		%	Undermentioned	
	-	-	- ±30		(Note 1,3)	

CFL : Initial, Ta=25°C, VDD-V0=(15.8)V Display data should be all "ON".

Note 1: Measurement after 10 minutes of CFL operating.

Note 2: Brightness control: 100%

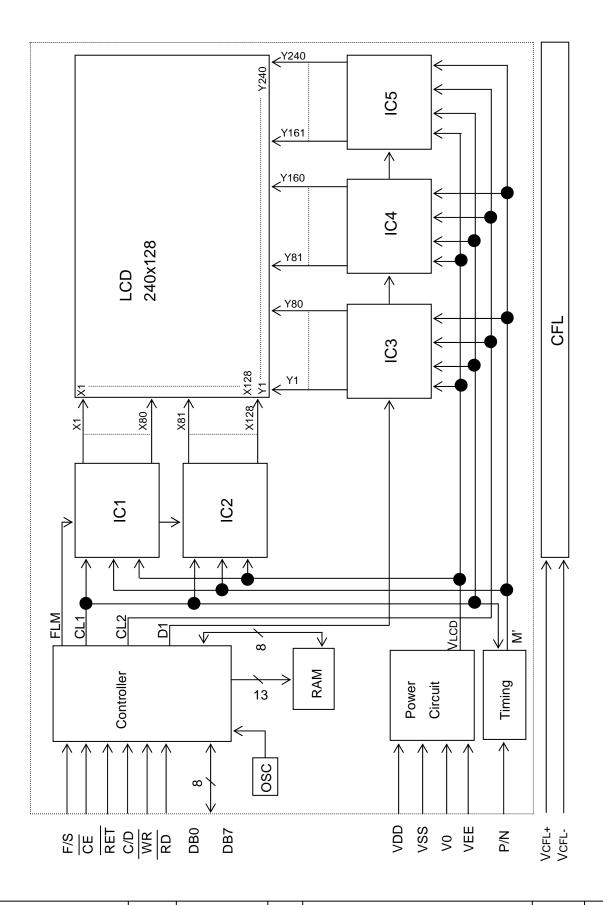
Note 3: Measure of the following 9 places on the display.



Definition of the brightness tolerance.

KAOHSIUNG HITACHI		Mar.25,'10	Sh.	7B64PS 2706- SP14N001-Z1-7	DAGE	6-2/2
ELECTRONICS CO.,LTD.	DATE	Wai.25, 10	No.	7B04PS 2708- SP14N001-21-7	FAGE	0-2/2

7. BLOCK DIAGRAM

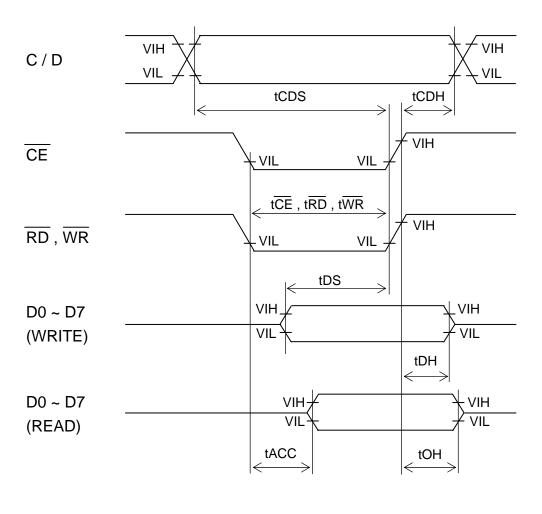


KAOHSIUNG HITACHI ELECTRONICS CO.,LTD. DATE Mar.25,'10 Sh. No. 7B64PS 2707- SP14N001-Z1-7 PAGE 7-1/1

8. INTERFACE TIMING

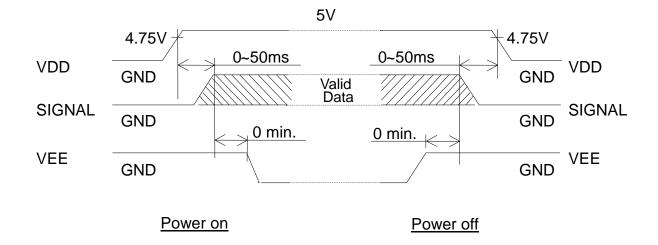
8.1 INTERFACE TIMING

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
C / D Setup Time	tCDS	100	-	-	ns
C / D Hold Time	tCHD	10	-	-	ns
CE, RD, WR Pulse Width	tCE, tRD, tWR	80	-	-	ns
Data Setup Time	tDS	80	-	-	ns
Data Hold Time	tDH	40	-	-	ns
Access Time	tACC	1	-	150	ns
Output Hold Time	tOH	10	-	50	ns



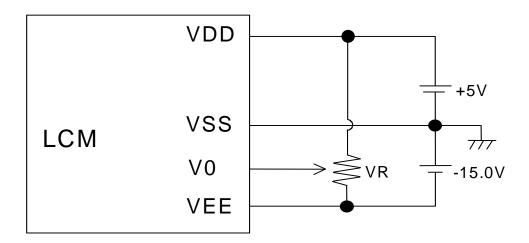
KAOHSIUNG HITACHI	DATE	Mar.25,'10	Sh.	7B64PS 2708- SP14N001-Z1-7	PAGE	8-1/2	
ELECTRONICS CO.,LTD.	DAIE	IVIAI.23, 10	No.	1504F3 2100-3F14N001-21-1	AGL	U- 1/Z	

8.2 TIMING OF POWER SUPPLY AND INTERFACE SIGNAL



The missing pixels may occur when the LCM is driven beyond above power interface timing sequence.

8.3 POWER SUPPLY FOR LCM (EXAMPLE)



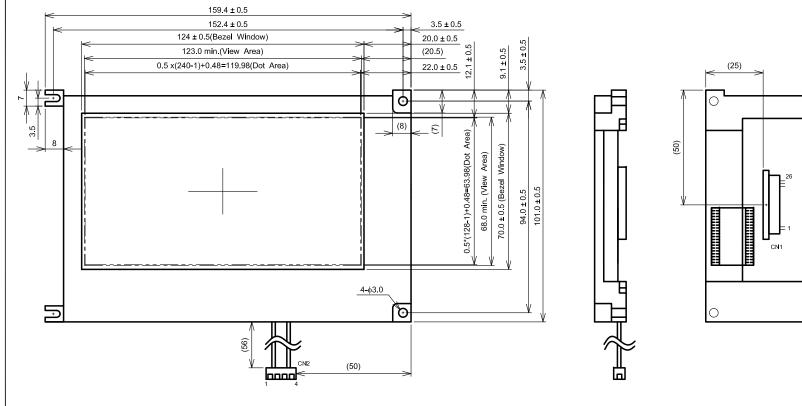
Recommend:

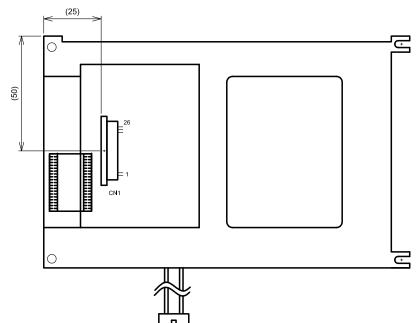
VR:10~20kΩ

VDD-V0: LCD driving voltage

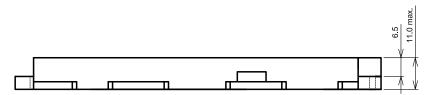
KAOHSIUNG HITACHI		Mar 25 '40	Sh.	7B64PS 2708- SP14N001-Z1-7	DACE	0 2/2
ELECTRONICS CO.,LTD.	DATE	Mar.25,'10	No.	7B04P3 2700- 3P14N001-21-7	PAGE	0-2/2

9. OUTLINE DIMENSIONS 9.1 OUTLINE DIMENSIONS





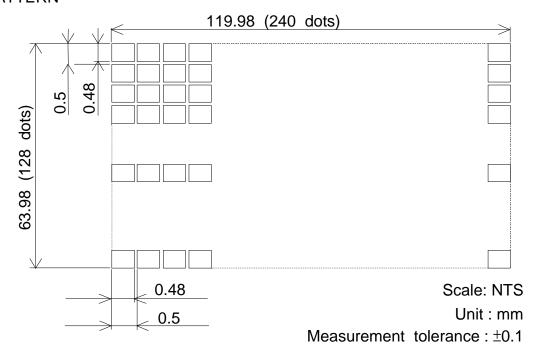
Front View Rear View



Scale : NTS Unit : mm

KAOHSIUNG HITACHI	DATE	Mar 25 '10	Sh.	7B64PS 2709-SP14N001-Z1-7	PAGE	0.4/2	l
ELECTRONICS CO.,LTD.	DAIL	Wai.25, 10	No.	1 B04F3	FAGE	9-1/3	l

9.2 DISPLAY PATTERN



9.3 INTERNAL PIN CONNECTION

CN1 : Pitch 1.0mm 26pins connector Suitable connector : Molex : 52207-2690

PIN No.	SYMBOL	FUNCTION
1	VSS(0V)	Ground
2	VDD(+5V)	Power supply for logic
3	V0(Input)	Power supply for LCD drive
4	C/D	WR="L": C/D="H" Command write C/D="L" Data write RD="L": C/D="H" Status read C/D="L" Data read
5	WR	Data write (Data write at "L")
6	RD	Data read (Read data at "L")
7	DB0	
8	DB1	
9	DB2	
10	DB3	-Data bus
11	DB4	Data bus
12	DB5	
13	DB6	
14	DB7	
15	CE	Chip enable (CE must be "L")
16	RET	Reset
17	VEE	Power supply for LCD drive
18	D.OFF	VDD/Display , GND/Display off
19	F/S	Character font select: F/S="H" 6*8Font F/S="L" 8*8Font
20	P/N	Display mode reverse.
21	NC	No connection
22	NC	No connection
23	NC	No connection
24	NC	No connection
25	NC	No connection
26	NC	No connection

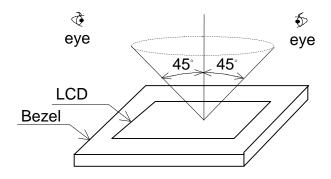
CN2: JAE IL-G-4S-S3C2-SA

PIN No.	SYMBOL	FUNCTION
1	VCFL-	CFL ground
2	NC	No connection
3	NC	No connection
4	VCFL+	Power supply for CFL

KAOHSIUNG HITACHI		Mar 25 '10	Sh.	ZDC4DC 0700 CD44N004 74 7	DACE	9-3/3
ELECTRONICS CO.,LTD.	DATE	Mar.25,'10	No.	7B64PS 2709- SP14N001-Z1-7	PAGE	9-3/3

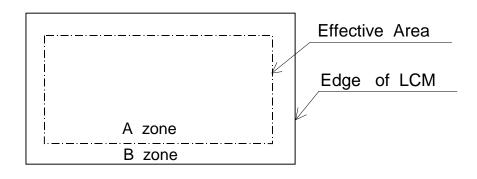
10. APPEARANCE STANDARD

- 10.1 APPEARANCE INSPECTION CONDITIONS (IN THE EFFECTIVE VIEWING AREA) VISUAL INSPECTION SHOULD BE UNDER THE FOLLOWING CONDITION.
 - (1) In the dark room.
 - (2) With CFL panel lighted with prescribed inverter circuit.
 - (3) With eye to LCD distance is 25cm.
 - (4) Viewing angle within 45 degrees from the perpendicular to the center LCD.



10.2 DEFINITION OF EACH ZONE

A zone: Within the viewing area specified at page 9-1/3 of this document. B zone: Area between the outline of LCM and the effective area specified at page 9-1/3 of this document.



KAOHSIUNG HITACHI	D 4 TE	NA 05 140	Sh.		DAGE	40.4/0
ELECTRONICS CO.,LTD.	DATE	Mar.25,'10	No.	7B64PS 2710- SP14N001-Z1-7	PAGE	10-1/3

10.3 APPEARENCE SPECIFICATION

*) If a problem occurs in respect to any of these items, responsible of both parties (customer and HITACHI) will discuss in more detail.

No.	ITEM		CRIT	ERIA			Α	В
	Scratches	Distinguished or					*	-
		(To be judged	by HITACHI	limit sar	mple)			
	Dent	Same as above)				*	-
	Wrinkles in Polarizer	Same as above	9				*	-
	Bubbles	Average D	iameter	Ma	ximum	Number		
		D(mr	n)		Accep	otable		
			≦0.2			ore		
		0.2 <d< td=""><td></td><td></td><td></td><td>2</td><td>О</td><td>-</td></d<>				2	О	-
		0.3 <d< td=""><td>≦0.5</td><td></td><td></td><td>3</td><td></td><td></td></d<>	≦0.5			3		
		0.5 <d< td=""><td></td><td></td><td>No</td><td>ne</td><td></td><td></td></d<>			No	ne		
	Stains,			entous				
	Foreign	Length	Width			mum Number	О	-
	Materials,	L(mm)	W(mn	,	ŀ	Acceptable		
	Dark Spot	L≦2.0		0.03		Ignore		
		L≦3.0	0.03 <w≦< td=""><td>0.05</td><td></td><td>6</td><td></td><td></td></w≦<>	0.05		6		
L		-	0.05 < W			ed by		
					"rour	ıd" shape		
				und				
		Average Diameter				Minimum		
С		D(mm)	Accepta			Space		
		D<0.2	Ignor	е		<u>-</u>	О	-
		$0.2 \leq D < 0.33$	8			10mm		
D		0.33≦D	None			-		
ט		Total	Filamentous					_
		Those wiped ou	•	•			0	О
	Color Tone	To be judged b	•	limit sam	nple		0	-
	Color Uniformity	Same as above		l			О	-
	Pinhole	Average D		Ma	_	Number		
		D(mr			•	otable		
		<u>D≦0</u>				ore		
		0.15 <d≦0< td=""><td></td><td></td><td></td><td>0</td><td></td><td></td></d≦0<>				0		
		C≦0				ore		
	Contrast	Average	Contrast	Maxim		Minimum	О	-
	Irregularity	Diameter		Numk		Space		
	(Spot)	D(mm)	T. b.	Accept			-	
		D≦0.25	To be	Igno		-	-	
		$0.25 < D \le 0.35$	judged by	10		20mm	4	
		$0.35 < D \le 0.5$	HITACHI	4	_	20mm	4	
		0.5 < D		Non	е	-		

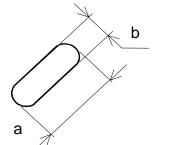
KAOHSIUNG HITACHI	DATE	Mar.25,'10	Sh.	7B64PS 2710- SP14N001-Z1-7	DAGE	10-2/3
ELECTRONICS CO.,LTD.	DATE	IVIAI.25, 10	No.	7604F3 2710-3F14N001-Z1-7	I AGL	10-2/3

No.	ITEM		CRITERIA						
	Contrast Irregularity (Line)	Width W(mm)	Length L(mm)	Maximum Number Acceptable Minimum Space					
L	(Filamentous)	W≦0.25	L≦1.2	2	20mm				
С		W≦0.2	L≦1.5	3	20mm	О	-		
D		W≦0.15	L≦2.0	3	20mm				
		W≦0.1	L≦3.0	4	20mm				
		To							
	Rubbing Scratch	To be judged	by HITACHI sta	andard		О	-		

No.	ITEM		IA	
С	Dark Spots, White Spots	Average Dian	neter D(mm)	Maximum Number Acceptable
F	Foreign Materials (Spot)	D≦0	0.4	Ignore
L		D>(0.4	None
		\\/idth \\/(mm)	Longth L(mm)	Maximum Number
В	Foreign Materials (Line)	Width W(mm)	Length L(mm)	acceptable
/		W≦0.2	L<2.5	≦1
L		W≦0.2	L>2.5	None
		W>0.2	-	None
		Width W(mm)	Length L(mm)	Maximum Number
		vvidiri vv(mm)	Length L(min)	Acceptable
	Caratabaa	W≦0.1	-	Ignore
	Scratches	0.1 <w≦0.2< td=""><td>L≦11.0</td><td>≦1</td></w≦0.2<>	L≦11.0	≦1
		0.1 <w≦0.2< td=""><td>L≧11.0</td><td>None</td></w≦0.2<>	L≧11.0	None
		W>0.2	-	None

Note

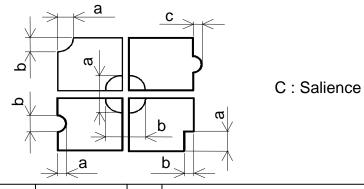
(1) Definition of average diameter D



(2) Definition of length L and width W



(3) Definition of pinhole



KAOHSIUNG HITACHI	DATE	Mar.25,'10	Sh.	7B64PS 2710- SP14N001-Z1-7	PAGE	10-3/3
ELECTRONICS CO.,LTD.	D/ (1 L	Wiai.20, 10	No.		. ,	. 0 0, 0

11. PRECAUTION IN DESIGN

11.1 LC DRIVING VOLTAGE (VEE) AND VIEWING ANGLE RANGE.

Setting VEE out of the recommended condition will be a cause for a change of viewing angle range.

11.2 CAUTION AGAINST STATIC CHARGE

As this module is provided with C-MOS LSI, the care to take such a precaution as grounding the operator's body is required when handling it.

11.3 POWER ON SEQUENCE

Input signals should not be applied to LCD module before power supply voltage is applied and reaches to specified voltage (5V±0.5%).

If above sequence is not kept, C-MOS LSI of LCD modules may be damaged due to latch up problem.

11.4 PACKAGING

(1) No. leaving product is preferable in the place of high humidity for a long period of time.

For their storage in the place where temperature is 35° C or higher, special care to prevent them from high humidity is required.

A combination of high temperature and high humidity may cause them polarization degradation as well as bubble generation and polarizer peel-off.

Please keep the temperature and humidity within the specified range for use and storage.

- (2) Since upper/bottom polarizers tend to be easily damaged, they should be handled full with care so as not to get them touched, pushed or rubbed.
- (3) As the adhesives used for adhering upper/bottom polerizers are made of organic substances which will be deteriorated by a chemical reaction with such chemicals as acetone, toluene, ethanol and isopropyl alcohol.

The following solvents are recommended for use: normal hexane

please contact us when it is necessary for you to use chemicals.

(4) Lightly wipe to clean the dirty surface with absorbent cotton waste or other soft material like chamois, soaked in the chemicals recommended without scrubbing it hardly.

To prevent the display surface from damage and keep the appearance in good state, it is sufficient, in general, to wipe it with absorbent cotton.

- (5) Immediately wipe off saliva or water drop attached on the display area because its long period adherence may cause deformation or faded color on the spot.
- (6) Fogy dew deposited on the surface and contact terminals due to coldness will be caused for polarizer damage, stain and dirt on product.

When necessary to take out the products form some place at low temperature for test, etc.

It is required for them to be warmed up in a container once at the temperature higher than that of room.

(7) Touching the display area and contact terminals with bare hands and contaminating them are prohibited, because the stain on the display area and poor insulation between terminals are often caused by being touched by bare hands. (There are some cosmetics detrimental to polarizers.)

KAOHSIUNG HITACHI	DATE	Mor 25 '10	Sh.	7B64PS 2711- SP14N001-Z1-7	DAGE	11 1/2
ELECTRONICS CO.,LTD.	DATE	Mar.25,'10	No.	7504PS 2711- SP14N001-21-7	PAGE	11-1/2

(8) In general the quality of glass is fragile so that it tends to be cracked or chipped in handling, specially on its periphery.

Be careful not to give it sharp shock caused by dropping down, etc.

11.5 CAUTION FOR OPAERATION

- (1) It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life.
 - An electrochemical reaction due to direct current causes LCD's undesirable deterioration, so that the use of direct current driver should be avoided.
- (2) Response time will be extremely delayed at lower temperature than the operating temperature range and on the other hand at higher temperature LCD's show dark bull color in them.
 - However those phenomena do not mean malfunction or out of order with LCD's which will come back in the specified operating temperature range.
- (3) IF the display area is pushed hard during operation, some font will be abnormally displayed but it resumes normal condition after turning off once.
- (4) A slight dew depositing on terminals is a cause for electorochemical reaction resulting in terminal open circuit.
 - Usage under the relative condition of 40 degree c 50%RH or less is required.

11.6 STORAGE

- In case of storing for a long period of time (for instance, for years) for the purpose of replacement use, the following ways area recommended.
- (1) Storage in a polyethylene bag with the opening sealed so as not to enter fresh air outside in it, and with no desiccant.
- (2) Placing in a dark place where neither exposure to direct sunlight nor light is , keeping temperature in the range from $0^\circ\mathbb{C}$ to $35^\circ\mathbb{C}$.
- (3) Storage with no touch on polarizer surface by anything else.
 (It is not recommended to store them as they have been contained in the inner container at the time of delivery from us.)

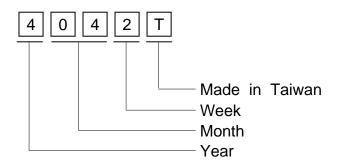
11.7 SAFETY

- (1) It is recommendable to crash damage or unnecessary LCD's into pieces and wash off liquid crystal by either of solvents such as acetone and ethanol, which should be burned up later.
- (2) When any liquid leaked out of a damage glass call comes in contact with your hands, please wash it off well with soap and water.

12. DESIGNATION OF LOT MARK

12.1 Lot mark

Lot mark is consisted of 4 digits number.



YEAR	FIGURE IN
YEAR	LOT MARK
2010	0
2011	1
2012	2
2013	3
2014	4

Note 1: Some products have alphabet at the end or the first.

MONITH	FIGURE IN	MONITU	FIGURE IN
MONTH	LOT MARK	MONTH	LOT MARK
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

WEEK (DAY IN CALENDAR	FIGURE IN LOT MARK
01~07	1
08~14	2
15~21	3
22~28	4
29~31	5

12.2 REVISION

REV No.	ITEM	NOTE
	CFL I/F Connector :	
-	Mitsumi M63M83 - 04	-
А	CFL I/F Connector : JAE IL-G-4S-S3C2-SA	7102T
В	M count IC change	-
С	Controller IC Change	PCN0768

12.3 LOCATION OF LOT MARK on the back side of LCM

4042T

T: Made in Taiwan.

					_	
KAOHSIUNG HITACHI	DATE	Mar.25,'10	Sh.	7B64PS 2712- SP14N001-Z1-7	DAGE	12-1/1
ELECTRONICS CO.,LTD.	DATE	Wai.25, 10	No.	1504F3 2112-3F14N001-21-1	I AGE	12-1/1

13. PRECAUTION FOR USE

- 13.1 A limit sample should be provided by the both parties on an occasion when the both parties agreed its necessity.
 Judgment by a limit sample shall take effect after the limit sample has been established and confirmed by the both parties.
- 13.2 On the following occasions, the handling of the problem should be decided through discussion and agreement between responsible persons of the both parties.
 - (1) When a question is arisen in the specifications.
 - (2) When a new problem is arisen which is not specified in this specifications.
 - (3) When an inspection specifications change or operating condition change in customer is reported to HITACHI, and some problem is arisen in this specification due to the change.
 - (4) When a new problem is arisen at the customer's operating set for sample evaluation in the customer site.

The precaution that should be observed when handling LCM have been explained above. If any points are unclear or if you have any request, please contact HITACHI.

KAOHSIUNG HITACHI			Sh.		D 4 0 E	40.4/4
ELECTRONICS CO.,LTD.	DATE	Mar.25,'10	No.	7B64PS 2713- SP14N001-Z1-7	PAGE	13-1/1