SP6801
Universal High Brightness LED Driver

DESCRIPTION

The SP6801 is a PWM high efficiency LED driver. It operates in wide range input voltage from 10V up to 600V. The device drives an external MOSFET at a fixed frequency. The frequency is programmable up to 300KHz with a single resistor. The dimming control for SP6801 can be either PWM input or linear input. The LED string is driven at a constant current without the need for loop compensation. SP6801 requires only few external components to achieve constant LED current making it ideas for low cost LED driver. The SP6801 is available in SOP-8 package.

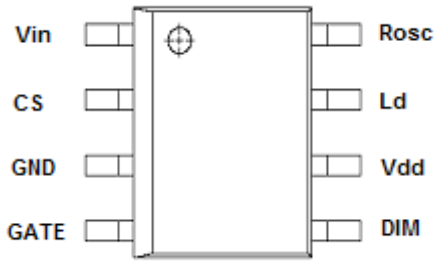
APPLICATIONS

- AC/DC or DC/DC LED driver applications
- Backlighting for flat panel displays
- General purpose constant current source
- Automotive
- Chargers

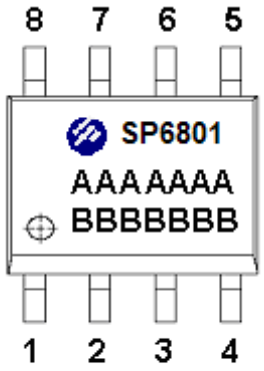
FEATURES

- >90% efficiency
- 10V to 600V DC input range
- Constant current LED driver
- Linear and PWM dimming capability
- Internal thermal overload protection

PIN CONFIGURATION(SOP-8)

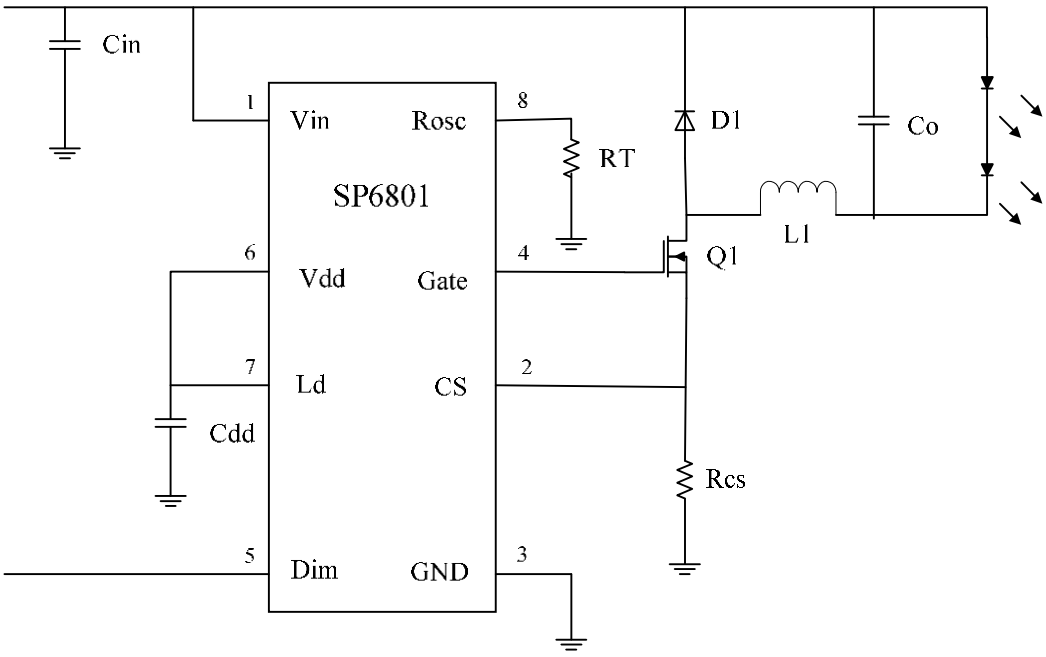


PART MARKING



A : Lot Code
B : Data Code

TYPICAL APPLCATION CIRCUIT



PIN DESCRIPTION

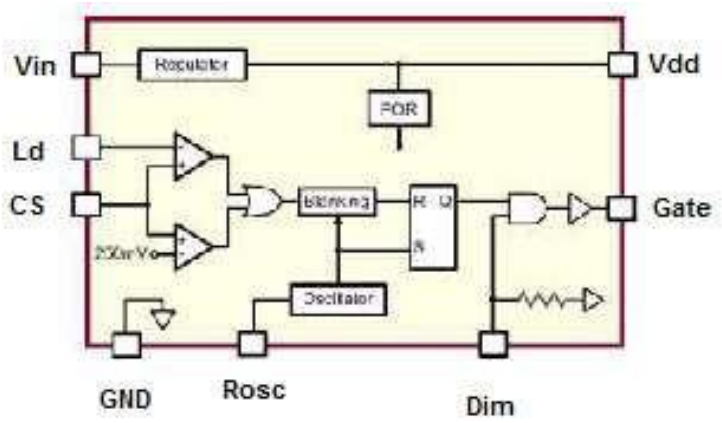
Pin	Symbol	Description
1	VIN	Supply Voltage Input
2	CS	Current sense. This pin senses the voltage across a resistor, to control PWM output. This pin also provides current amplitude information for current-mode control
3	GND	Ground
4	Gate	Gate driver output to drive the external MOSFET
5	Dim	Dimming Control
6	Vdd	Power supply pin for internal circuits
7	Ld	Linear dimming by changing the current limit threshold at current sense comparator
8	Rosc	This is used to charge an internal capacitor, to determine the switching frequency

ORDERING INFORMATION

Part Number	Package	Part Marking
SP6801S8RGB	SOP-8	SP6801

※ SP6801S8RGB : Tape Reel ; Pb – Free ; Halogen -Free

BLOCK DIAGRAM



ABSOULTE MAXIMUM RATINGS

(T_A=25℃ Unless otherwise specified)

Parameter	Symbol	Value	Unit
DC Supply Voltage	V_{IN}	600	V
Vdd to Ground	Vdd	12	V
CS, Gate, Dim, Ld		Vdd+0.3V	V
Operating Temperature	T_{OPR}	-40 ~ 85	℃
Maximum Junction Temperature	$T_{J(Max)}$	-40~125	℃
Storage Temperature	T_S	-65 ~ 150	℃
Thermal Resistance Junction – Case (*)	$R_{\Theta JC}$	150	℃/W
Power Dissipation	P_D	630	mW

The IC has a protection circuit against static electricity. Do not apply high static electricity or high voltage that exceeds the performance of the protection circuit to the IC.

ELECTRICAL CHARACTERISTICS

(T_A=25℃, V_{IN}=12V, Unless otherwise specified)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Supply Voltage (V_{IN} Pin)						
I_{SD}	Shut Down Mode Supply Current	DIM to Ground,		0.5	1	mA
Vin	DC Input Supply Voltage		10		600	V
UVLO (on)	Start Threshold Voltage	V_{DD} Rising	6.3	6.7	7.0	V
ΔUVLO	Under Voltage Lockout Hysteresis	V_{DD} Falling		500		mV
Internal Regulator (V_{DD} Pin)						
V_{DD}	Internal Regulated Voltage	Vin=10V~600V, I_{DD}=0, Gate Open	7.0	7.5	8.0	V
ΔV_{DD}	Load Regulation	I_{DD}=0~1mA, V_{DIM}=V_{DD}, Rosc=226KΩ, Gate=500pF			100	mV
V_{DD}	Maximum V_{DD} Voltage	Apply External Voltage			10	V
$I_{DD(ext)}$	Current Available for External Circuit	Vin=15~100V			0.7	mA
Oscillator (Rosc Pin)						
F_{OSC}	Frequency	Rosc=1 MΩ	20	25	30	KHz
		Rosc=226 KΩ	80	100	120	KHz
Current Sensing (CS Pin)						
$V_{CS(TH)}$	Current Sense Pull-in Threshold Voltage	T_A=-40℃~85℃	240	250	260	mV
T_{BLANK}	Current Sense Blanking Interval	V_{CS}=0.55V_{LD}, V_{LD}=V_{DD}	150	215	280	nS
T_{DELAY}	Delay to Output	V_{LD}=0.15V, Vcs=0~0.22V after T_{BLANK}, Vin=12V			300	nS
Gate Driver Output (GATE Pin)						
V_{OL}	Output Low Level	Io=-10mA	0		0.3	V
V_{OH}	Output High Level	Io=10mA	V_{DD}-0.3		V_{DD}	V
Tr	Rising Time	Load Cap=500pF, V_{DD}=7.5V	30		50	nS
Tf	Falling Time	Load Cap=500pF, V_{DD}=7.5V	30		50	nS
PWM Dimming (DIM Pin)						
$V_{EN(LO)}$	PWM Dimming Input Low Voltage	Vin=10V~600V			0.8	V
$V_{EN(HI)}$	PWM Dimming Input High Voltage	Vin=10V~600V	2			V
R_{EN}	PWM Dimming Pull Down Resistance	V_{EN}=5V	50	100	150	KΩ
Linear Dimming (Ld Pin)						
V_{LD}	Linear Dimming Voltage	Vin=12V, T_A<85℃			250	mV

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