AEC-Q101 Qualified

4V Drive Pch+Pch MOS FET

Structure

Silicon P-channel MOS FET

Features

1) Low On-resistance. (25m Ω at 4.5V)

2) High Power Package. (PD=2.0W)

3) High speed switching.

4) Low voltage drive. (4V)

Applications

Power switching, DC-DC converter

Packaging specifications

	Package	Taping
Туре	Code	TB
	Basic ordering unit (pieces)	2500
SP8J5 FRA	0	

•Absolute maximum ratings (Ta=25°C)

<It is the same ratings for Tr1 and Tr2.>

			Limits	Unit
Drain-source voltage			-30	V
Gate-source voltage			±20	V
Continuous	lо		±7.0	А
Pulsed	DP	*1	±28	А
Continuous	ls		-1.6	А
Pulsed	Isp	*1	-28	А
Total power dissipation			2.0	W
Channel temperature			150	°C
Range of Storage temperature			-55 to +150	°C
	Pulsed Continuous Pulsed	Pulsed IbP Continuous Is Pulsed IsP PD Tch	VGSS Continuous ID Pulsed IDP *1 Continuous IS Pulsed Isp *1 PD *2 Tch	VGSS ±20 Continuous Ib ±7.0 Pulsed IbP *1 ±28 Continuous Is -1.6 Pulsed IsP *1 -28 Pob *2 2.0 Tch 150 150

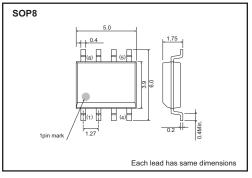
*1 Pw≤10µs, Duty cycle≤1% *2 Mounted on a ceramic board

Thermal resistance

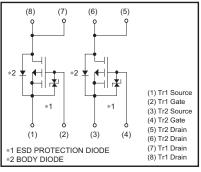
Parameter	Symbol	Limits	Unit
Channel to ambient	Rth(ch-a) *	62.5	°C / W

* Mounted on a ceramic board.

•External dimensions (Unit : mm)



Inner circuit



Transistors

•Electrical characteristics (Ta=25°C)

< It is the same characteristics for Tr1 and Tr2.>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	lgss	-	-	±10	μA	Vgs=±20V, Vds=0V	
Drain-source breakdown voltage	V(BR) DSS	-30	-	-	V	ID=-1mA, VGS=0V	
Zero gate voltage drain current	IDSS	-	-	-1	μA	$V_{DS}=-30V, V_{GS}=0V$	
Gate threshold voltage	V _{GS (th)}	-1.0	-	-2.5	V	$V_{DS} = -10V, I_{D} = -1mA$	
		-	20	28	mΩ	$I_D = -7A$, $V_{GS} = -10V$	
Static drain-source on-state resistance	RDS (on)*	_	25	35	mΩ	$I_D = -3.5A$, $V_{GS} = -4.5V$	
		-	30	42	mΩ	$I_D = -3.5A$, $V_{GS} = -4.0V$	
Forward transfer admittance	Y _{fs} *	6.0	-	-	S	$V_{DS} = -10V, I_D = -3.5A$	
Input capacitance	Ciss	-	2600	-	pF	V _{DS} = -10V	
Output capacitance	Coss	-	450	-	pF	V _{GS} =0V	
Reverse transfer capacitance	Crss	-	350	-	рF	f=1MHz	
Turn-on delay time	td (on) $*$	-	20	-	ns	ID= -3.5A	
Rise time	tr *	-	50	-	ns	VDD≒ -15V	
Turn-off delay time	t _{d (off)} *	-	110	-	ns	VGs= −10V R∟=4.3Ω	
Fall time	t _f *	-	70	-	ns	R _G =10Ω	
Total gate charge	Qg *	-	25	-	nC	V _{DD} ≒−15V	
Gate-source charge	Q _{gs} *	-	5.5	-	nC	V _{GS} =-5V	
Gate-drain charge	Q _{gd} *	_	10	_	nC	I _D =-7A	

*Pulsed

•Body diode characteristics (Source-drain) (Ta=25°C)

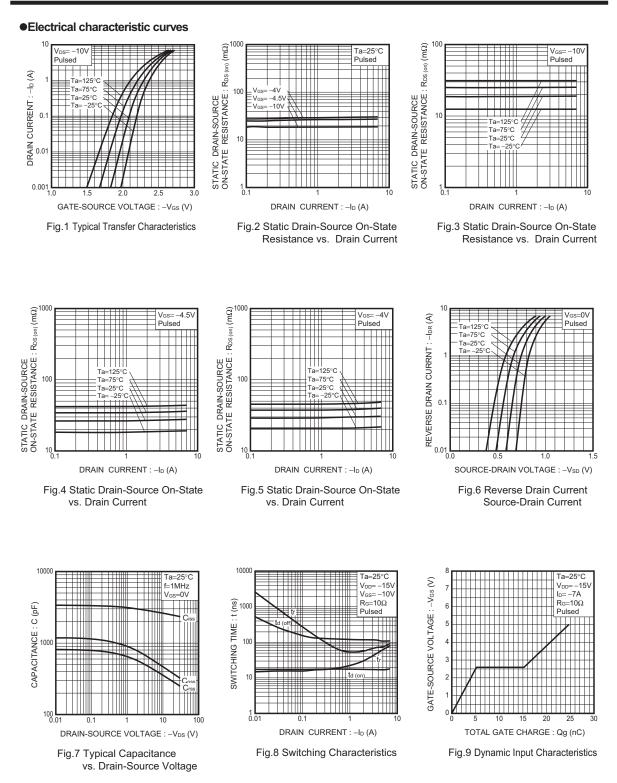
< It is the same characteristics for Tr1 and Tr2.>

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsd	-	-	-1.2	V	Is= -1.6A, V _{GS} =0V



SP8J5FRA

Transistors



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Transistors

Measurement circuits

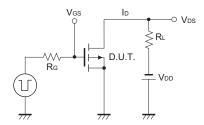


Fig.10 Switching Time Test Circuit

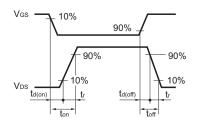


Fig.11 Switching Time Waveforms

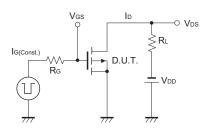


Fig.12 Gate Charge Test Circuit

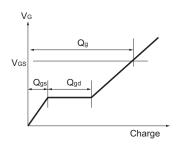


Fig.13 Gate Charge Waveform

Notice

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CLASSI	CLASSⅢ	CLASS II b	CLASSII	
CLASSⅣ	CLASSI	CLASSⅢ	CLASSII	

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 - [a] Use of our Products in any types of liquid, including water, oils, chemicals, and organic solvents
 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation depending on ambient temperature. When used in sealed area, confirm that it is the use in the range that does not exceed the maximum junction temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used on a surface-mount products, the flow soldering method must be used on a through hole mount products. If the flow soldering method is preferred on a surface-mount products, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

Precautions Regarding Application Examples and External Circuits

- 1. If change is made to the constant of an external circuit, please allow a sufficient margin considering variations of the characteristics of the Products and external components, including transient characteristics, as well as static characteristics.
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Precaution for Electrostatic

This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

Precaution for Storage / Transportation

- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
 - [a] the Products are exposed to sea winds or corrosive gases, including Cl2, H2S, NH3, SO2, and NO2
 - [b] the temperature or humidity exceeds those recommended by ROHM
 - [c] the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- 4. Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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A two-dimensional barcode printed on ROHM Products label is for ROHM's internal use only.

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When disposing Products please dispose them properly using an authorized industry waste company.

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SP8J5FRA - Web Page

Part Number	SP8J5FRA
Package	SOP8
Unit Quantity	2500
Minimum Package Quantity	2500
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes