

# isc N-Channel MOSFET Transistor

# SPA04N80C3

### • FEATURES

- With TO-220F packaging
- High speed switching
- Low gate input resistance
- Standard level gate drive
- Easy to use
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

### • APPLICATIONS

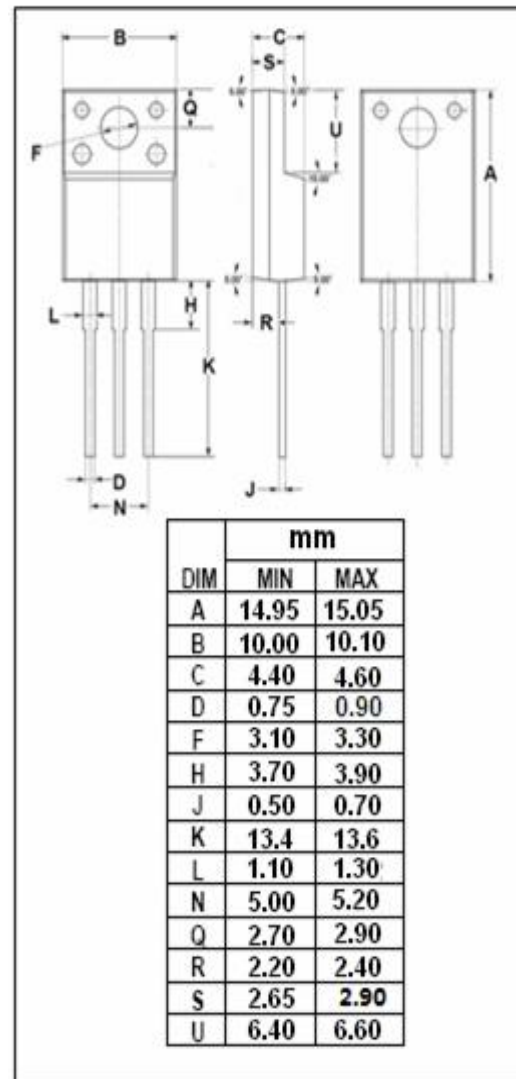
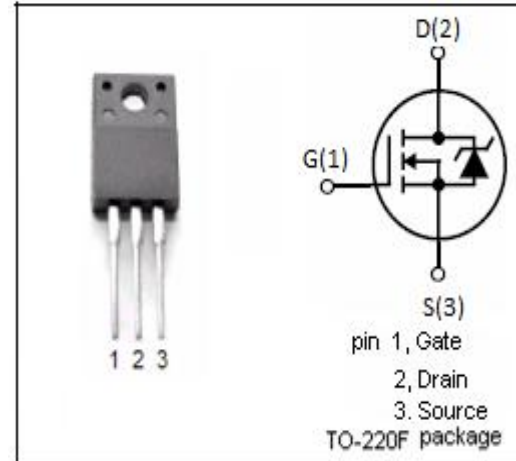
- Power supply
- Switching applications

### • ABSOLUTE MAXIMUM RATINGS( $T_a=25^{\circ}\text{C}$ )

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	800	V
$V_{GSS}$	Gate-Source Voltage	$\pm 30$	V
$I_D$	Drain Current-Continuous @ $T_c=25^{\circ}\text{C}$ $T_c=100^{\circ}\text{C}$	4 2.5	A
$I_{DM}$	Drain Current-Single Pulsed	12	A
$P_D$	Total Dissipation	38	W
$T_j$	Operating Junction Temperature	150	$^{\circ}\text{C}$
$T_{stg}$	Storage Temperature	-55~150	$^{\circ}\text{C}$

### • THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	4.0	$^{\circ}\text{C/W}$
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	80	$^{\circ}\text{C/W}$



**isc N-Channel MOSFET Transistor****SPA04N80C3****ELECTRICAL CHARACTERISTICS** $T_c=25^{\circ}\text{C}$  unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0V; I_D=0.25mA$	800			V
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}; I_D=0.24mA$	2.1		3.9	V
$R_{DS(on)}$	Drain-Source On-Resistance	$V_{GS}=10V; I_D=2.5A$		1.1	1.3	$\Omega$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20V; V_{DS}=0V$			$\pm 0.1$	$\mu A$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=800V; V_{GS}=0V; T_c=25^{\circ}\text{C}$ $V_{DS}=800V; V_{GS}=0V; T_c=125^{\circ}\text{C}$			10 100	$\mu A$
$V_{SDF}$	Diode forward voltage	$I_{SD}=4A, V_{GS}=0V$		1.0	1.2	V

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