*Preliminary data*

**SPP80N04S2L-03**
**SPB80N04S2L-03**

## OptiMOS Power-Transistor

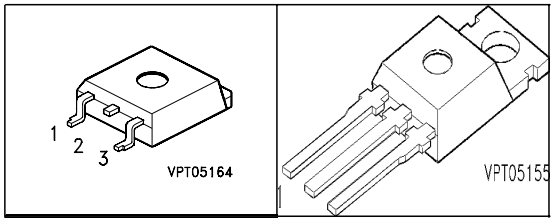
### Feature

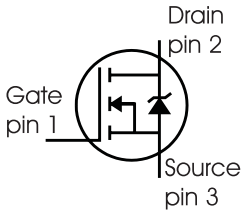
- N-Channel
- Enhancement mode
- Logic Level
- 175°C operating temperature
- Avalanche rated
- d*v*/d*t* rated

### Product Summary

| | | |
|---|---|---|
| $V_{DS}$ | 40 | V |
| $R_{DS(on)}$ max. SMD version | 3.1 | mΩ |
| $I_D$ | 80 | A |

P-TO263-3-2 P-TO220-3-1




| Type | Package | Ordering Code | Marking |
|---|---|---|---|
| SPP80N04S2L-03 | P-TO220-3-1 | Q67040-S4261 | 2N04L03 |
| SPB80N04S2L-03 | P-TO263-3-2 | Q67040-S4262 | 2N04L03 |

**Maximum Ratings**,at $T_j$ = 25 °C, unless otherwise specified

| Parameter | Symbol | Value | Unit |
|---|---|---|---|
| Continuous drain current | $I_D$ | | A |
| $T_C$ = 25 °C, 1) | | 80 | |
| $T_C$=100°C | | 80 | |
| Pulsed drain current | $I_{D\ puls}$ | 320 | |
| $T_C$=25°C | | | |
| Avalanche energy, single pulse | $E_{AS}$ | 810 | mJ |
| $I_D$=80 A , $V_{DD}$=25V, $R_{GS}$=25Ω | | | |
| Avalanche energy, periodic limited by $T_{jmax}$ | $E_{AR}$ | 30 | |
| Reverse diode d*v*/d*t* | d*v*/d*t* | 6 | kV/µs |
| $I_S$=80A, $V_{DS}$=32V, d*i*/d*t*=200A/µs, $T_{jmax}$=175°C | | | |
| Gate source voltage | $V_{GS}$ | ±20 | V |
| Power dissipation | $P_{tot}$ | 300 | W |
| $T_C$=25°C | | | |
| Operating and storage temperature | $T_j$ , $T_{stg}$ | -55... +175 | °C |
| IEC climatic category; DIN IEC 68-1 | | 55/175/56 | |

## Thermal Characteristics

| Parameter | Symbol | Values | | | Unit |
|---|---|---|---|---|---|
| | | min. | typ. | max. | |
| **Characteristics** | | | | | |
| Thermal resistance, junction - case | $R_{thJC}$ | - | - | 0.5 | K/W |
| Thermal resistance, junction - ambient, leaded | $R_{thJA}$ | - | - | 62 | |
| SMD version, device on PCB: | $R_{thJA}$ | | | | |
| @ min. footprint | | - | - | 62 | |
| @ 6 cm$^2$ cooling area [2)] | | - | - | 40 | |

## Electrical Characteristics, at $T_j$ = 25 °C, unless otherwise specified

| Parameter | Symbol | Values | | | Unit |
|---|---|---|---|---|---|
| | | min. | typ. | max. | |
| **Static Characteristics** | | | | | |
| Drain-source breakdown voltage<br>$V_{GS}$=0V, $I_D$=1mA | $V_{(BR)DSS}$ | 40 | - | - | V |
| Gate threshold voltage, $V_{GS}$ = $V_{DS}$<br>$I_D$=250µA | $V_{GS(th)}$ | 1.2 | 1.6 | 2 | |
| Zero gate voltage drain current | $I_{DSS}$ | | | | µA |
| $V_{DS}$=40V, $V_{GS}$=0V, $T_j$=25°C | | - | 0.01 | 1 | |
| $V_{DS}$=40V, $V_{GS}$=0V, $T_j$=125°C | | - | 1 | 100 | |
| Gate-source leakage current<br>$V_{GS}$=20V, $V_{DS}$=0V | $I_{GSS}$ | - | 1 | 100 | nA |
| Drain-source on-state resistance | $R_{DS(on)}$ | | | | mΩ |
| $V_{GS}$=4.5V, $I_D$=80A | | - | 3.5 | 4.5 | |
| $V_{GS}$=4.5V, $I_D$=80A, SMD version | | - | 3.2 | 4.2 | |
| Drain-source on-state resistance[3)] | $R_{DS(on)}$ | | | | |
| $V_{GS}$=10V, $I_D$=80A | | - | 2.7 | 3.4 | |
| $V_{GS}$=10V, $I_D$=80A, SMD version | | - | 2.4 | 3.1 | |

[1]Current limited by bondwire; with a $R_{thJC}$ = 0.5 K/W the chip is able to carry $I_D$ = 217A and calculated with max. source pin temperature of 85°C.

[2]Device on 40mm*40mm*1.5mm epoxy PCB FR4 with 6cm² (one layer, 70 µm thick) copper area for drain connection. PCB is vertical without blown air.

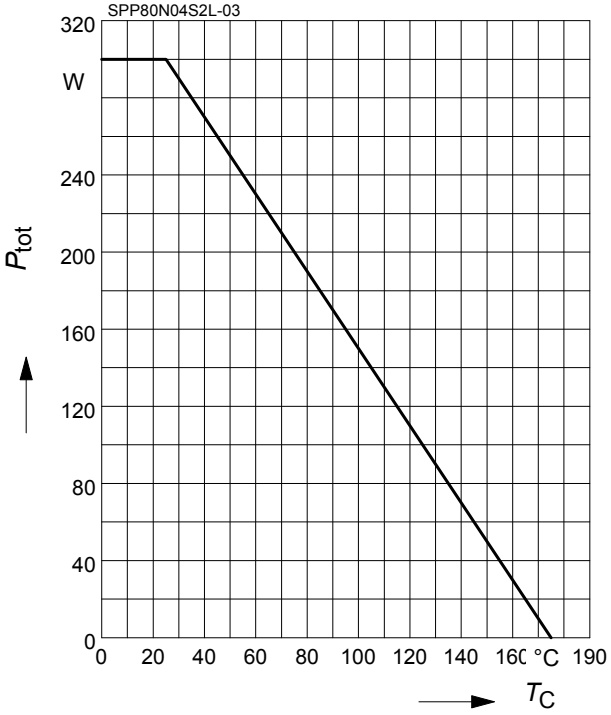
[3]Diagrams are related to straight lead versions

**Electrical Characteristics**, at $T_j$ = 25 °C, unless otherwise specified

| Parameter | Symbol | Conditions | Values | | | Unit |
|---|---|---|---|---|---|---|
| | | | min. | typ. | max. | |
| **Dynamic Characteristics** | | | | | | |
| Transconductance | $g_{fs}$ | $V_{DS}\geq 2*I_D*R_{DS(on)max}$, $I_D$=80A | 79 | 158 | - | S |
| Input capacitance | $C_{iss}$ | $V_{GS}$=0V, $V_{DS}$=25V, $f$=1MHz | - | 5960 | 7450 | pF |
| Output capacitance | $C_{oss}$ | | - | 1890 | 2360 | |
| Reverse transfer capacitance | $C_{rss}$ | | - | 460 | 690 | |
| Turn-on delay time | $t_{d(on)}$ | $V_{DD}$=20V, $V_{GS}$=10V, $I_D$=80A, $R_G$=1.1Ω | - | 24 | 36 | ns |
| Rise time | $t_r$ | | - | 83 | 125 | |
| Turn-off delay time | $t_{d(off)}$ | | - | 60 | 90 | |
| Fall time | $t_f$ | | - | 80 | 120 | |
| **Gate Charge Characteristics** | | | | | | |
| Gate to source charge | $Q_{gs}$ | $V_{DD}$=32V, $I_D$=80A | - | 18 | 23 | nC |
| Gate to drain charge | $Q_{gd}$ | | - | 54 | 80 | |
| Gate charge total | $Q_g$ | $V_{DD}$=32V, $I_D$=80A, $V_{GS}$=0 to 10V | - | 160 | 210 | |
| Gate plateau voltage | $V_{(plateau)}$ | $V_{DD}$ = 32 V , $I_D$=80A | - | 3.2 | - | V |
| **Reverse Diode** | | | | | | |
| Inverse diode continuous forward current | $I_S$ | $T_C$=25°C | - | - | 80 | A |
| Inverse diode direct current, pulsed | $I_{SM}$ | | - | - | 320 | |
| Inverse diode forward voltage | $V_{SD}$ | $V_{GS}$=0V, $I_F$=80A | - | 0.9 | 1.3 | V |
| Reverse recovery time | $t_{rr}$ | $V_R$=20V, $I_F$=$I_S$, $di_F/dt$=100A/µs | - | 62 | 78 | ns |
| Reverse recovery charge | $Q_{rr}$ | | - | 145 | 180 | nC |

## 1 Power dissipation

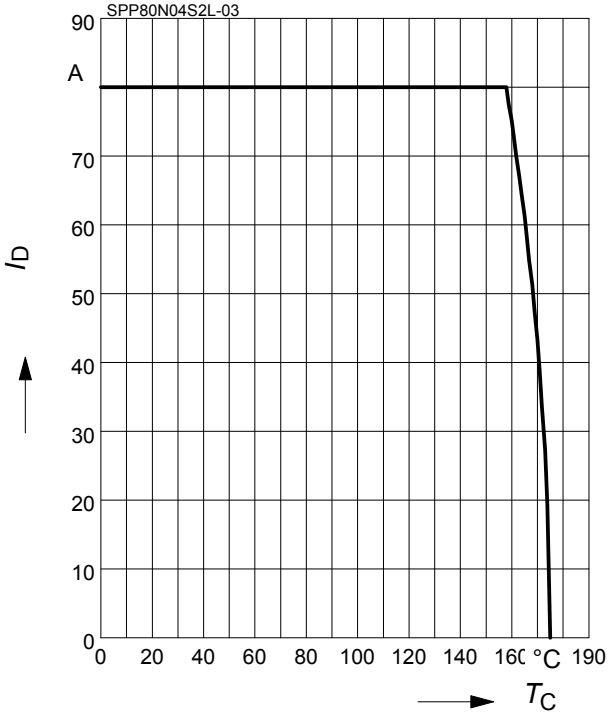
$P_{tot} = f(T_C)$




## 2 Drain current

$I_D = f(T_C)$

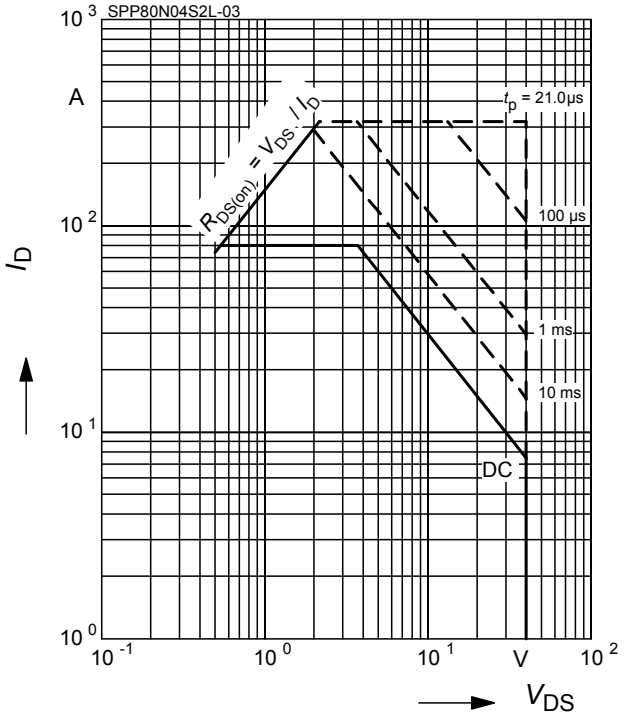
parameter: $V_{GS} \geq 10$ V




## 3 Safe operating area

$I_D = f(V_{DS})$

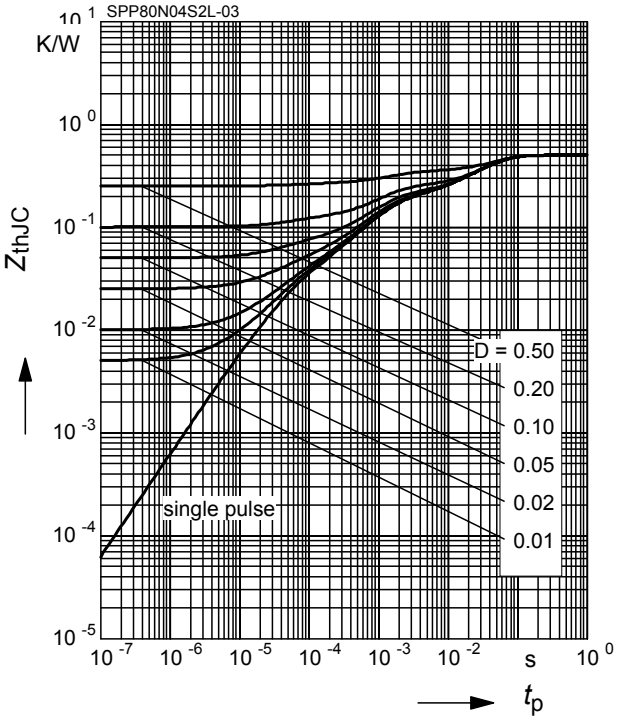
parameter : $D = 0$ , $T_C = 25$ °C




## 4 Transient thermal impedance

$Z_{thJC} = f(t_p)$

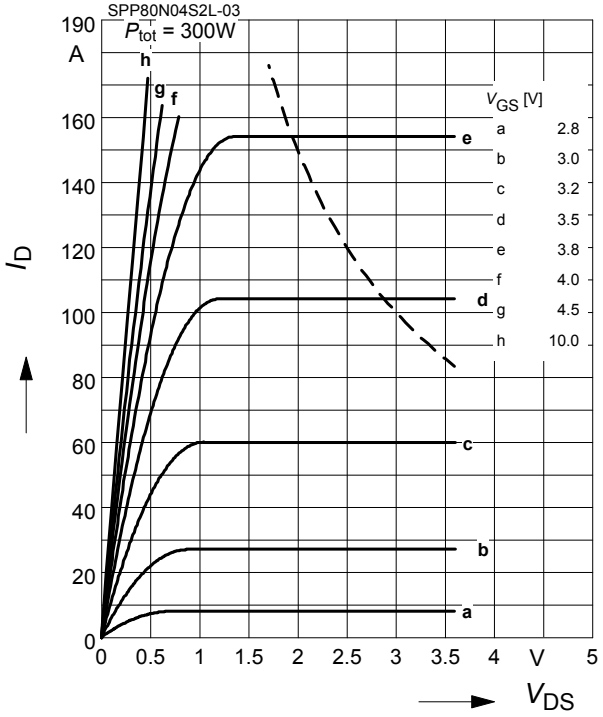
parameter : $D = t_p/T$



## 5 Typ. output characteristic

$I_D = f(V_{DS})$; $T_j$=25°C

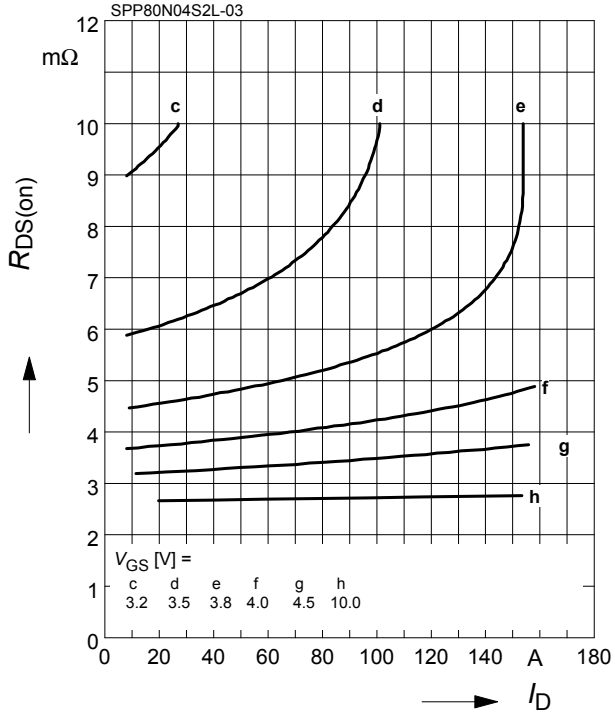
parameter: $t_p$ = 80 µs




## 6 Typ. drain-source on resistance

$R_{DS(on)} = f(I_D)$

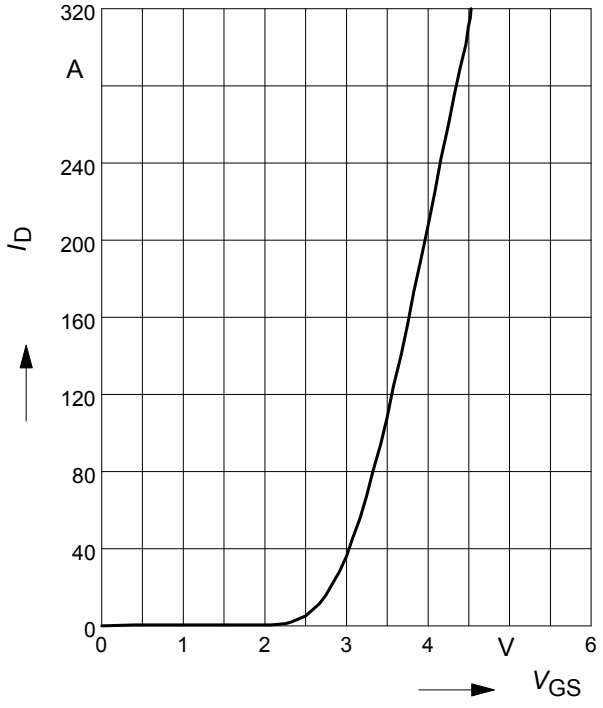
parameter: $V_{GS}$




## 7 Typ. transfer characteristics

$I_D = f(V_{GS})$; $V_{DS} \geq 2 \times I_D \times R_{DS(on)max}$

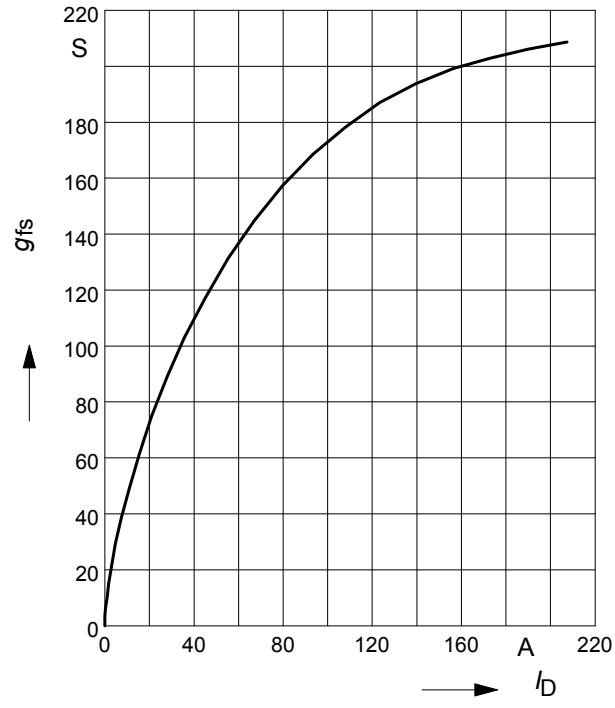
parameter: $t_p$ = 80 µs




## 8 Typ. forward transconductance

$g_{fs}$ = f($I_D$); $T_j$=25°C

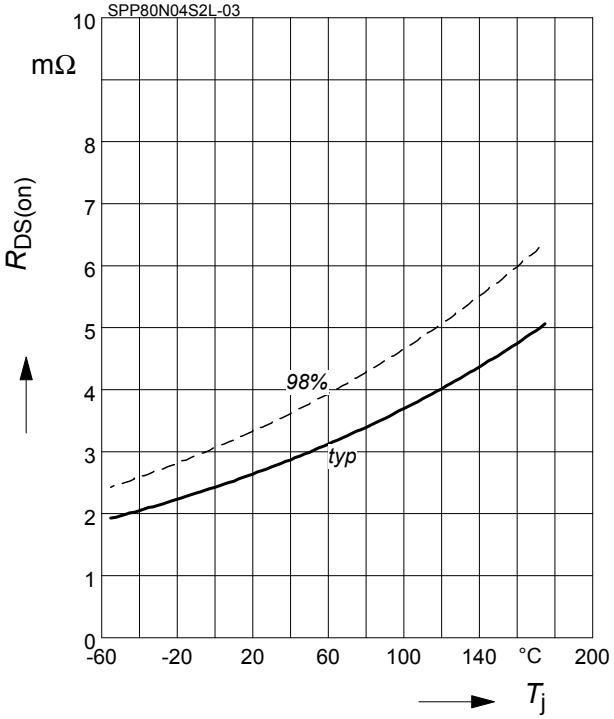
parameter: $g_{fs}$



### 9 Drain-source on-state resistance

$R_{DS(on)} = f(T_j)$

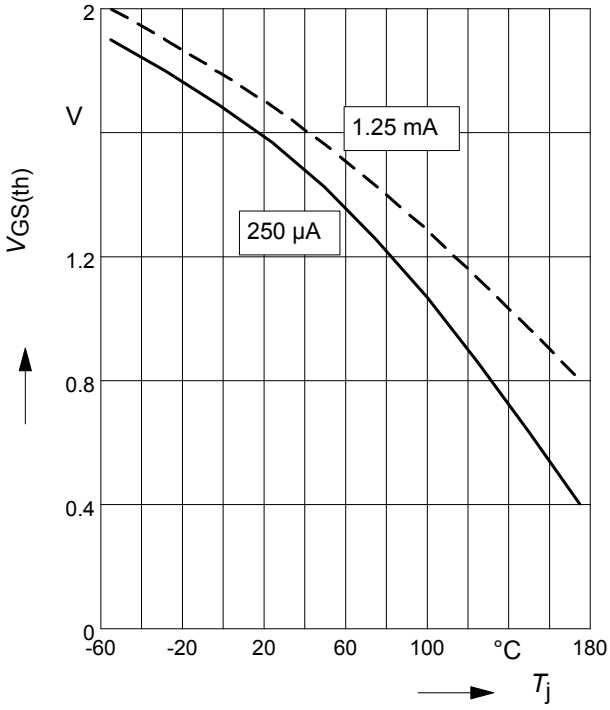
parameter : $I_D$ = 80 A, $V_{GS}$ = 10 V




### 10 Typ. gate threshold voltage

$V_{GS(th)} = f(T_j)$

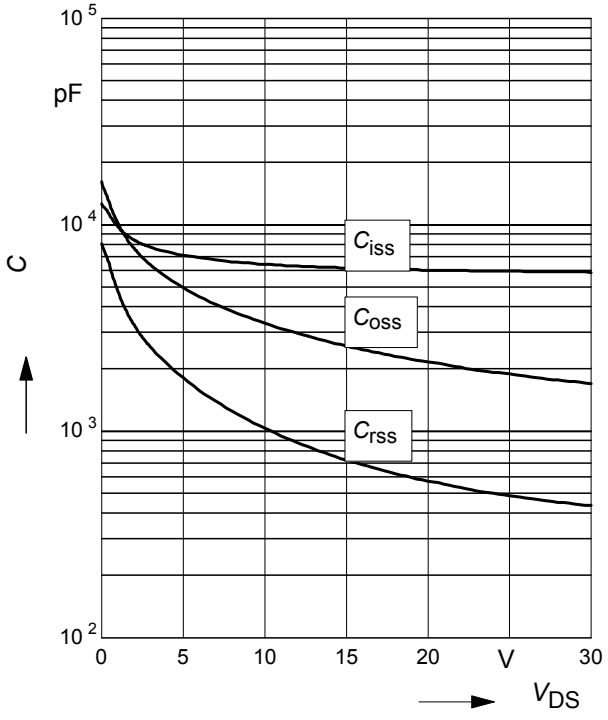
parameter: $V_{GS} = V_{DS}$




### 11 Typ. capacitances

$C = f(V_{DS})$

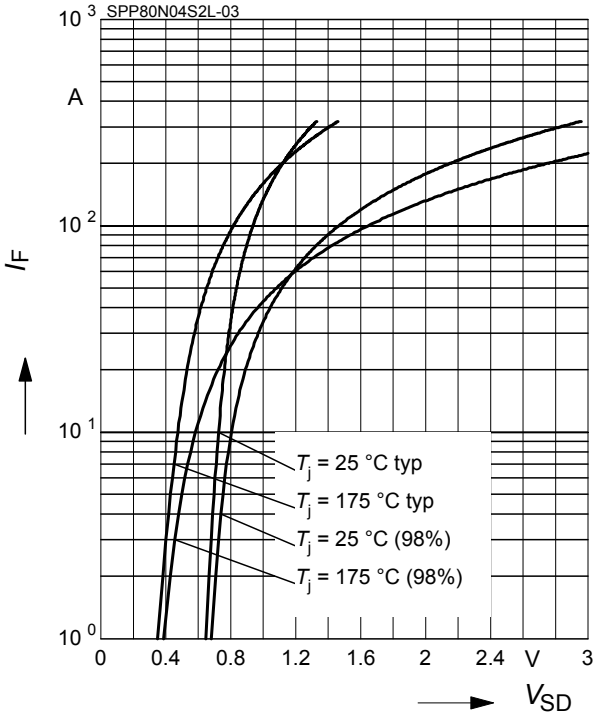
parameter: $V_{GS}$=0V, $f$=1 MHz




### 12 Forward character. of reverse diode

$I_F = f(V_{SD})$

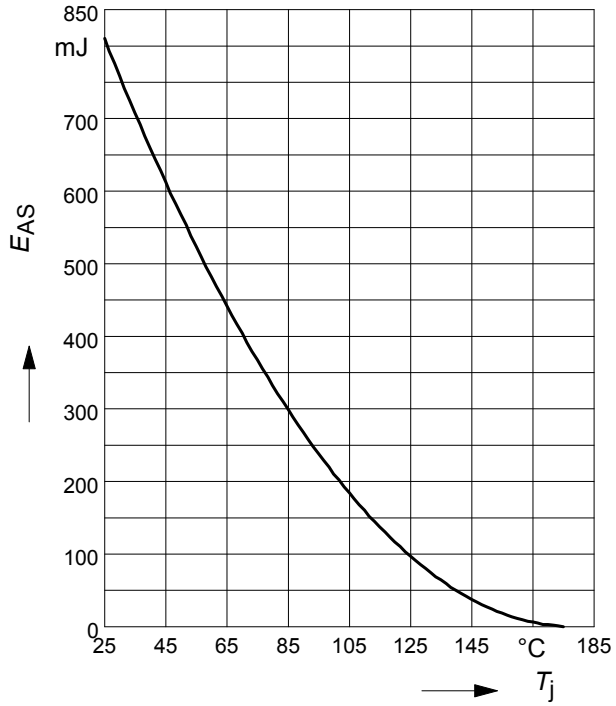
parameter: $T_j$ , tp = 80 µs



## 13 Typ. avalanche energy

$E_{AS} = f(T_j)$

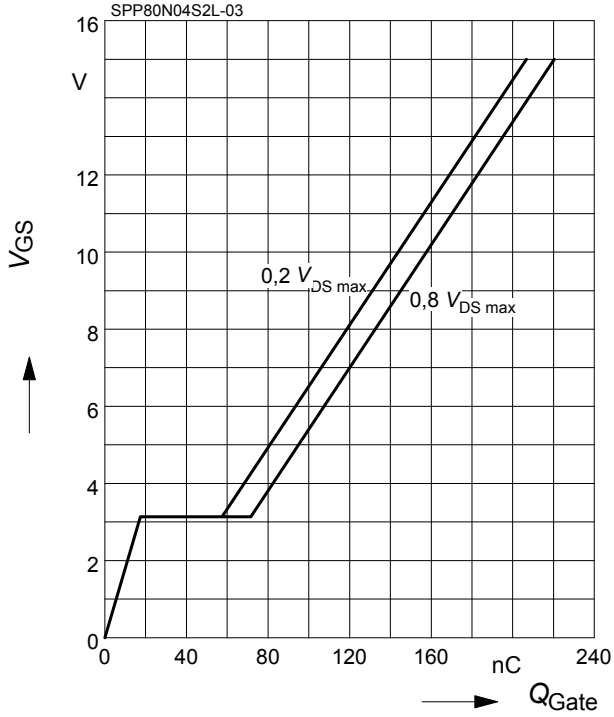
par.: $I_D$ = 80 A , $V_{DD}$ = 25 V, $R_{GS}$ = 25 Ω




## 14 Typ. gate charge

$V_{GS} = f(Q_{Gate})$

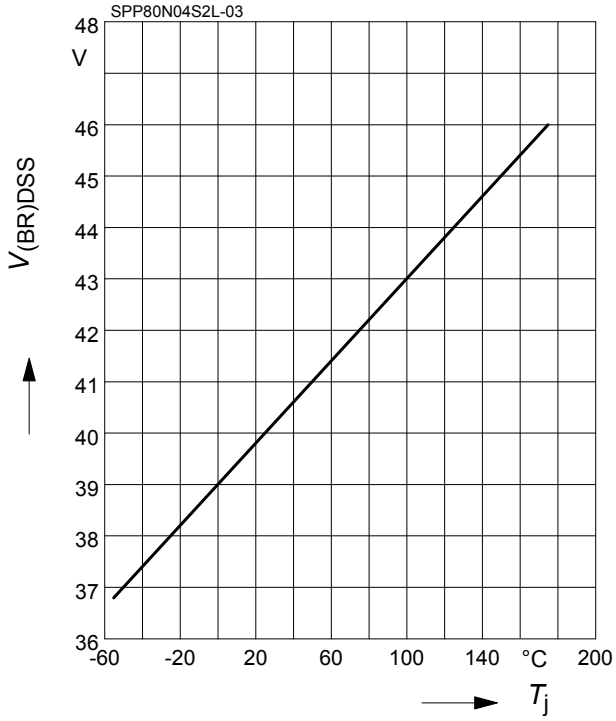
parameter: $I_D$ = 80 A pulsed




## 15 Drain-source breakdown voltage

$V_{(BR)DSS} = f(T_j)$

parameter: $I_D$=10 mA



**Published by**
**Infineon Technologies AG,**
**Bereichs Kommunikation**
**St.-Martin-Strasse 53,**
**D-81541 München**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**Further information**
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