



SPC6601

N & P Pair Enhancement Mode MOSFET

DESCRIPTION

The SPC6601 is the N- and P-Channel enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching , low in-line power loss, and resistance to transients are needed.

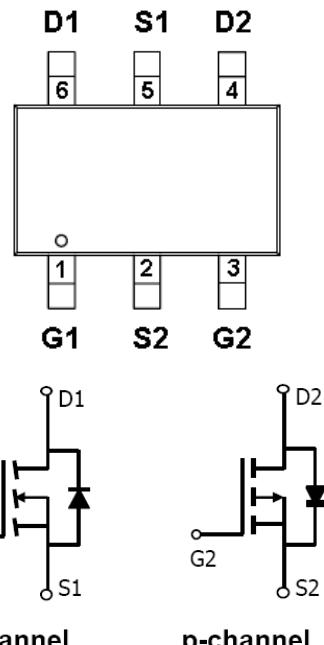
APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

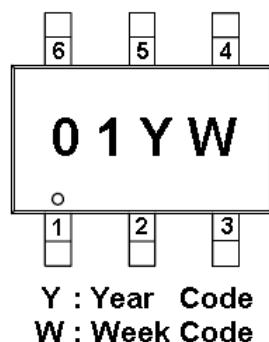
FEATURES

- ◆ N-Channel
 - 30V/2.8A,RDS(ON)=68mΩ@VGS=10V
 - 30V/2.3A,RDS(ON)=78mΩ@VGS=4.5V
 - 30V/1.5A,RDS(ON)=108mΩ@VGS=2.5V
- ◆ P-Channel
 - 30V/-2.8A,RDS(ON)=105mΩ@VGS=-10V
 - 30V/-2.5A,RDS(ON)=120mΩ@VGS=-4.5V
 - 30V/-1.5A,RDS(ON)=148mΩ@VGS=-2.5V
- ◆ Super high density cell design for extremely low RDS(ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ TSOT-23-6L package design

PIN CONFIGURATION(TSOT-23-6L)



PART MARKING





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PIN DESCRIPTION

Pin	Symbol	Description
1	G1	Gate 1
2	S2	Source 2
3	G2	Gate 2
4	D2	Drain 2
5	S1	Source 1
6	D1	Drain1

ORDERING INFORMATION

Part Number	Package	Part Marking
SPC6601TS26RGB	TSOT-23-6L	01

※ Week Code : A ~ Z(1 ~ 26) ; a ~ z(27 ~ 52)

※ SPC6601TS26RGB : Tape Reel ; Pb – Free ; Halogen -Free

ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical		Unit
		N-Channel	P-Channel	
Drain-Source Voltage	V _{DSS}	30	-30	V
Gate –Source Voltage	V _{GSS}	±12	±12	V
Continuous Drain Current(T _J =150°C)	T _A =25°C	ID	2.8	A
	T _A =70°C		2.3	
Pulsed Drain Current	I _{DM}	10	-8	A
Continuous Source Current(Diode Conduction)	I _S	1.25	-1.4	A
Power Dissipation	T _A =25°C	P _D	1.15	W
	T _A =70°C		0.75	
Operating Junction Temperature	T _J	-55/150		°C
Storage Temperature Range	T _{STG}	-55/150		°C
Thermal Resistance-Junction to Ambient	T ≤ 10sec	R _{θJA}	50	°C/W
	Steady State		90	



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ELECTRICAL CHARACTERISTICS

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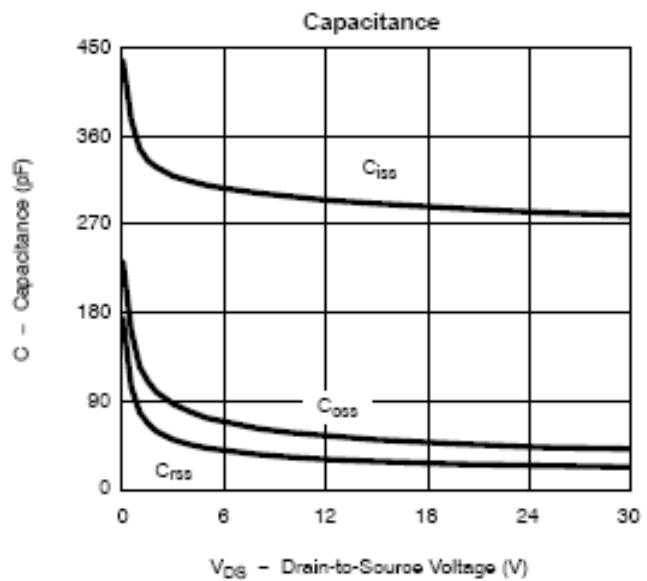
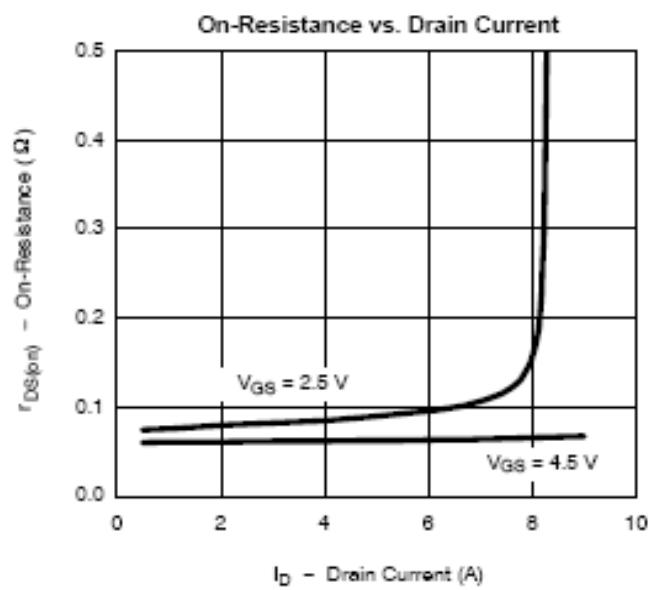
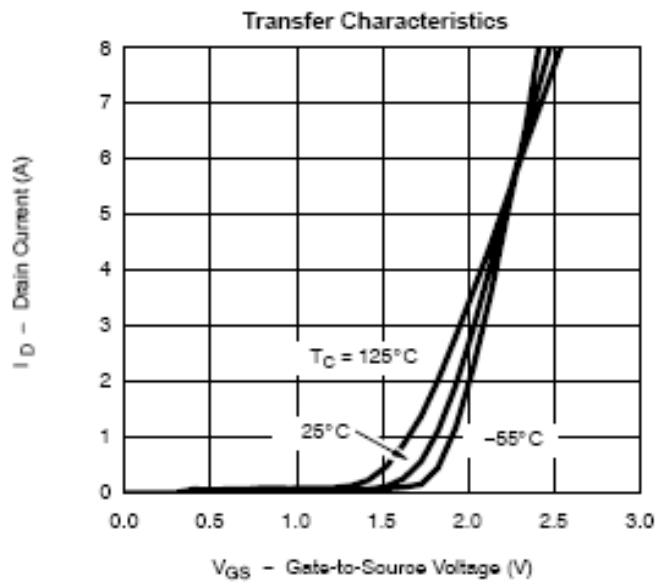
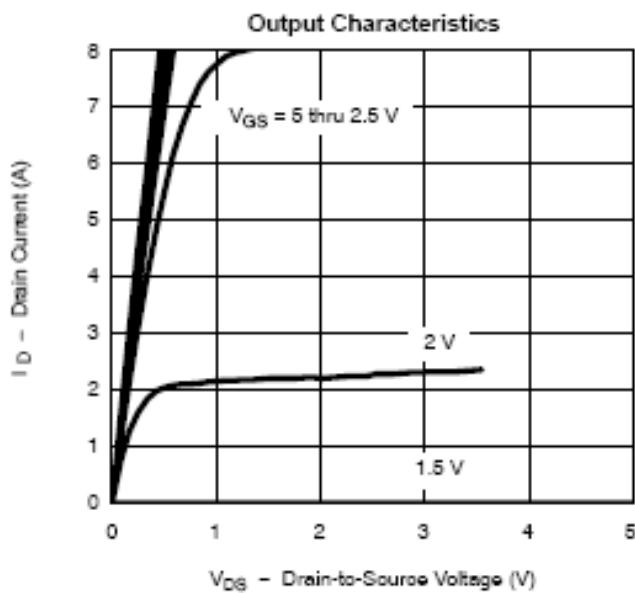
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, ID=250uA	N-Ch	30		V
		V _{GS} =0V, ID=-250uA	P-Ch	-30		
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , ID=250uA	N-Ch	0.5		1.6
		V _{DS} =V _{GS} , ID=-250uA	P-Ch	-0.4		-1.0
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±12V	N-Ch		±100	nA
		V _{DS} =0V, V _{GS} =±12V	P-Ch		±100	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 21V, V _{GS} =0V	N-Ch		1	uA
		V _{DS} = -21V, V _{GS} =0V	P-Ch		-1	
		V _{DS} = 21V, V _{GS} =0V T _J =55°C	N-Ch		10	
		V _{DS} = -21V, V _{GS} =0V T _J =55°C	P-Ch		-10	
On-State Drain Current	I _{D(on)}	V _{DS} ≥ 5V, V _{GS} =10V	N-Ch	6		A
		V _{DS} ≤ -5V, V _{GS} =-10V	P-Ch	-6		
Drain-Source On-Resistance	R _{D(on)}	V _{GS} = 10V, ID=2.8A	N-Ch		0.048	0.068
		V _{GS} = -10V, ID=-2.8A	P-Ch		0.077	0.105
		V _{GS} = 4.5V, ID= 2.3A	N-Ch		0.054	0.078
		V _{GS} = -4.5V, ID=-2.5A	P-Ch		0.092	0.120
		V _{GS} = 2.5V, ID=1.5A	N-Ch		0.079	0.108
		V _{GS} = -2.5V, ID=-1.5A	P-Ch		0.118	0.148
Forward Transconductance	g _f s	V _{DS} =4.5V, ID=2.8A	N-Ch		4.6	S
		V _{DS} =-10V, ID=-2.8A	P-Ch		4	
Diode Forward Voltage	V _{SD}	I _S =1.25A, V _{GS} =0V	N-Ch		0.8	1.2
		I _S =-1.2A, V _{GS} =0V	P-Ch		-0.8	-1.2
Dynamic						
Total Gate Charge	Q _g	N-Channel V _{DS} =15 , V _{GS} =4.5V , ID=2.0A P-Channel V _{DS} =-15V , V _{GS} =-4.5V , ID=-2.0A	N-Ch		4.2	6
Gate-Source Charge	Q _{gs}		P-Ch		5.8	
Gate-Drain Charge	Q _{gd}		N-Ch		0.6	nC
Turn-On Time	t _{d(on)}		P-Ch		0.8	
	t _r		N-Ch		1.5	
Turn-Off Time	t _{d(off)}	N-Channel V _{DD} =15 , R _L =10Ω V _{GEN} =10V , R _G =3Ω P-Channel V _{DD} =-15V , R _L =15Ω V _{GEN} =-10V , R _G =3Ω	P-Ch		1.5	nS
	t _f		N-Ch		2.5	
			P-Ch		6	
			N-Ch		2.5	
			P-Ch		3.9	
			N-Ch		20	



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TYPICAL CHARACTERISTICS (N-Channel)

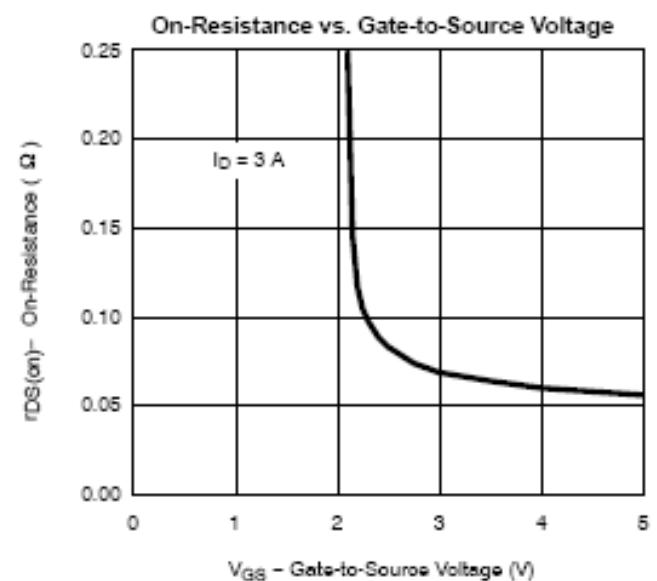
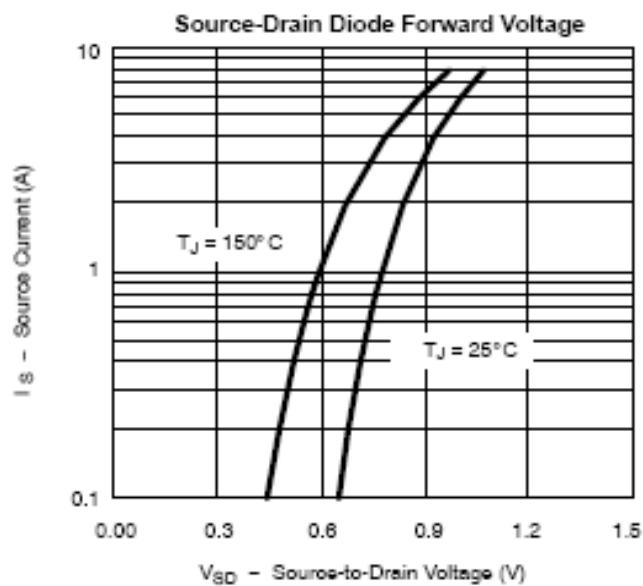
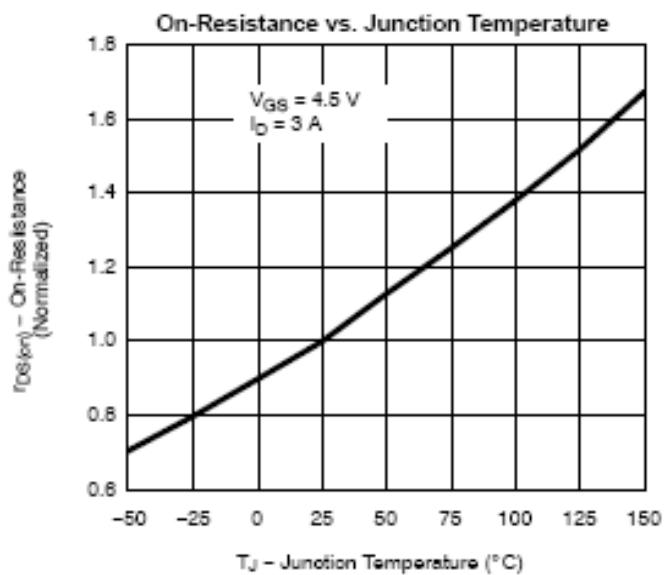
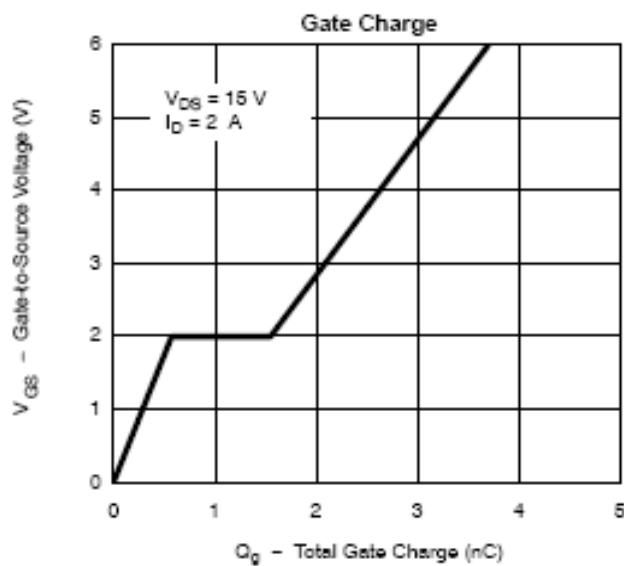




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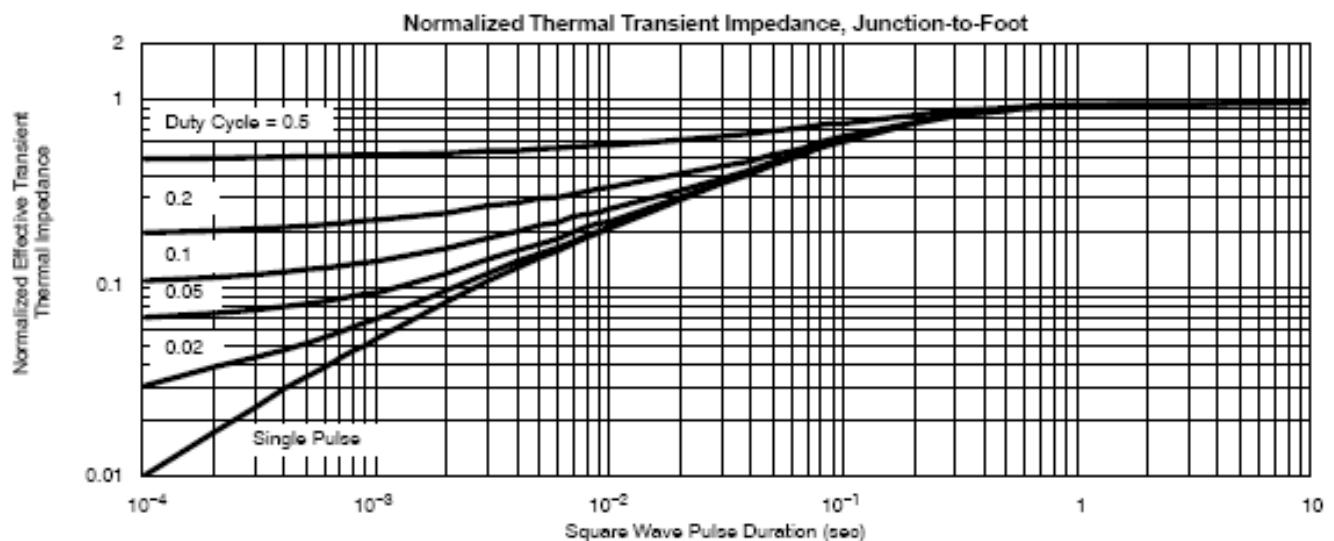
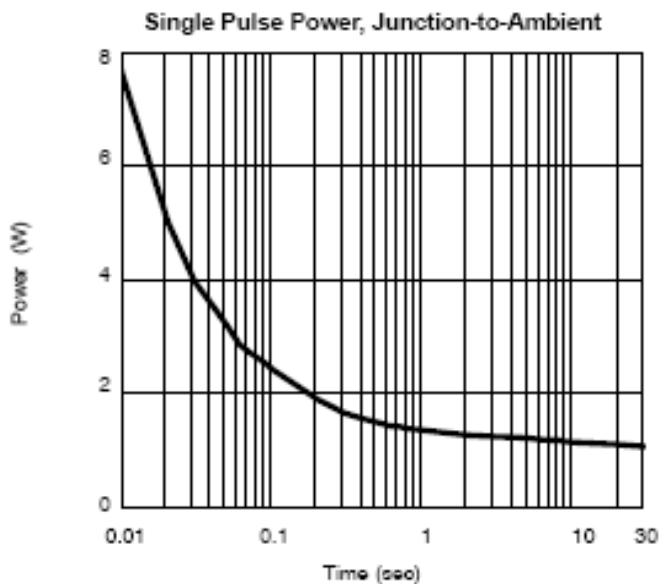
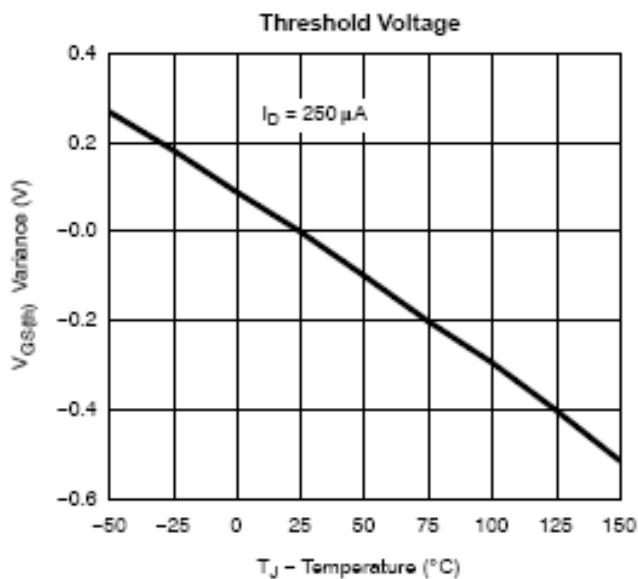




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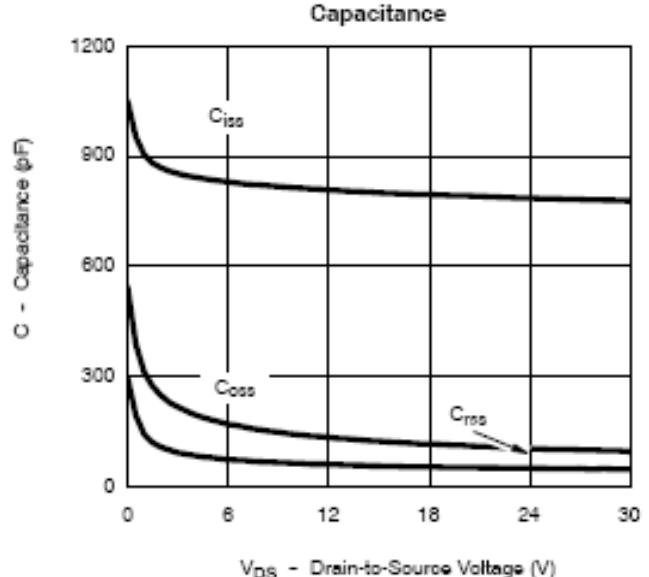
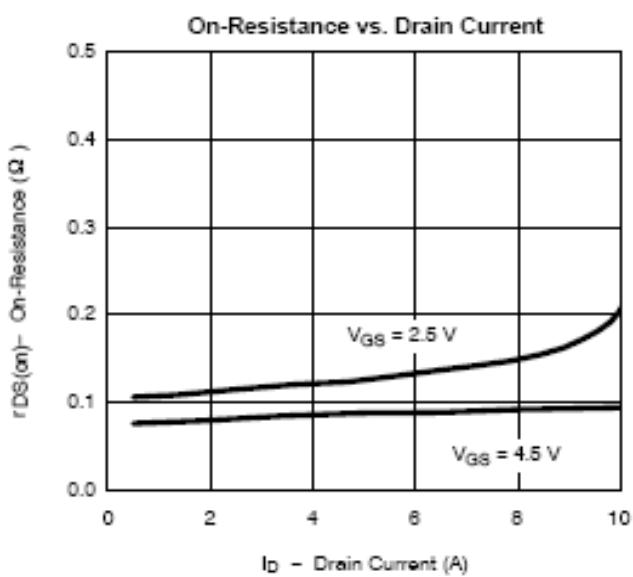
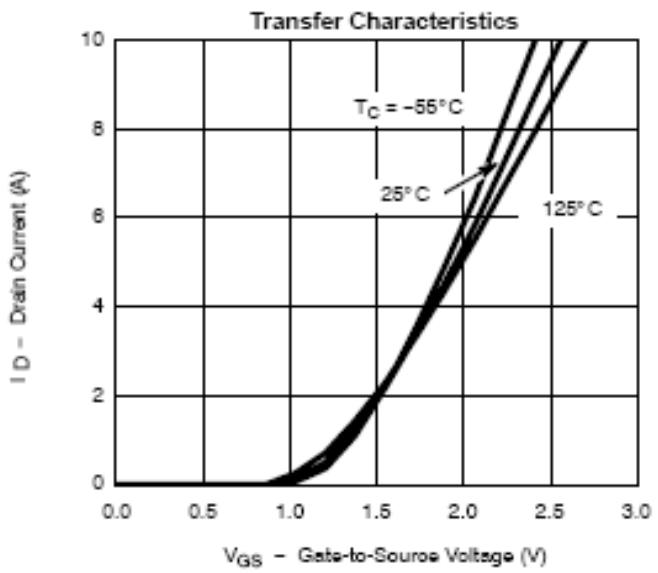
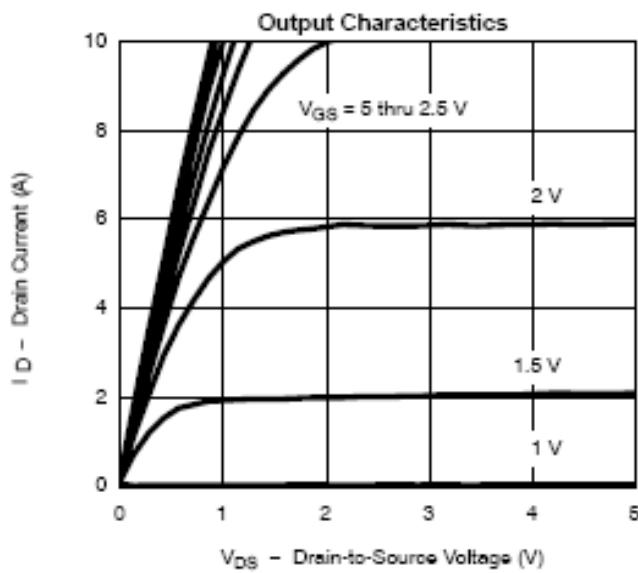




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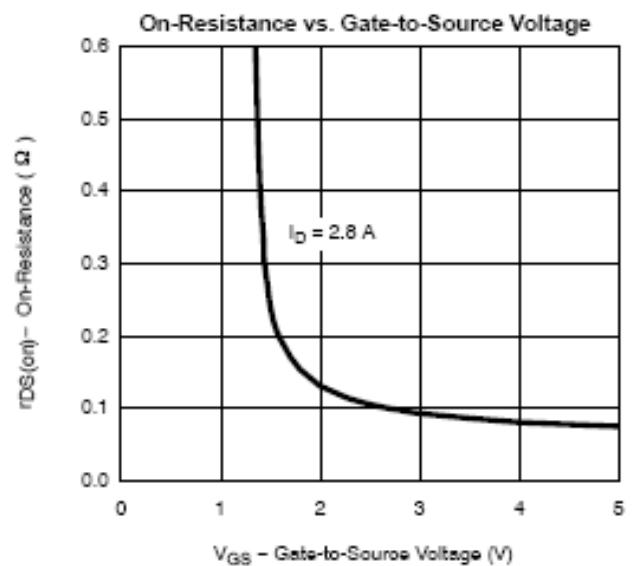
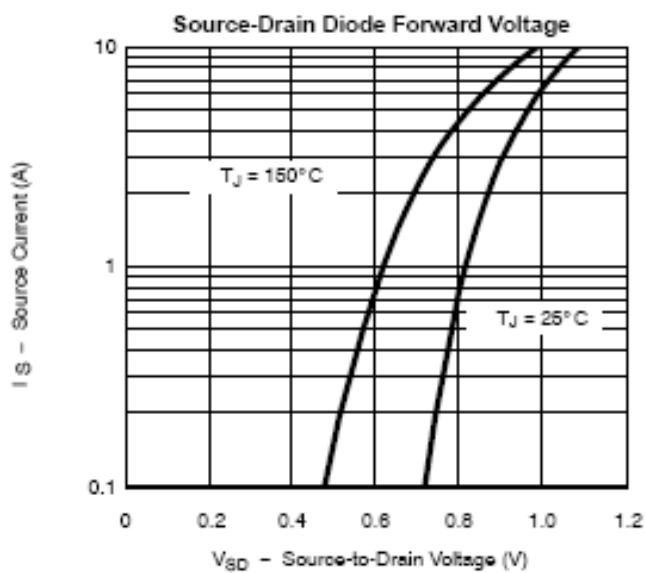
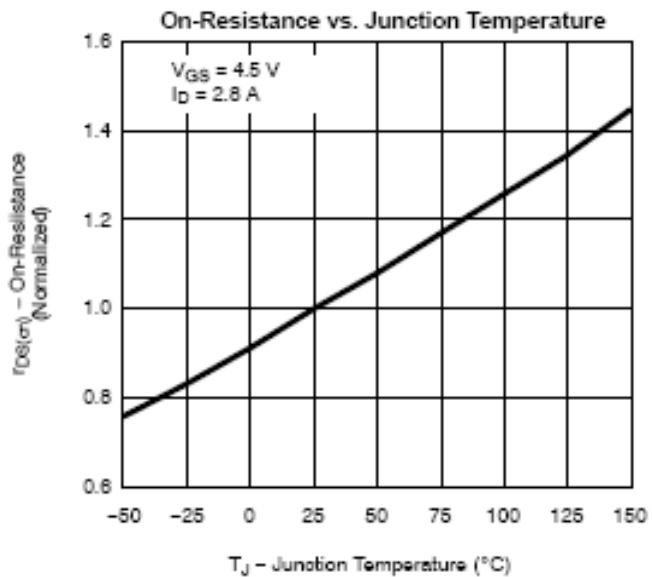
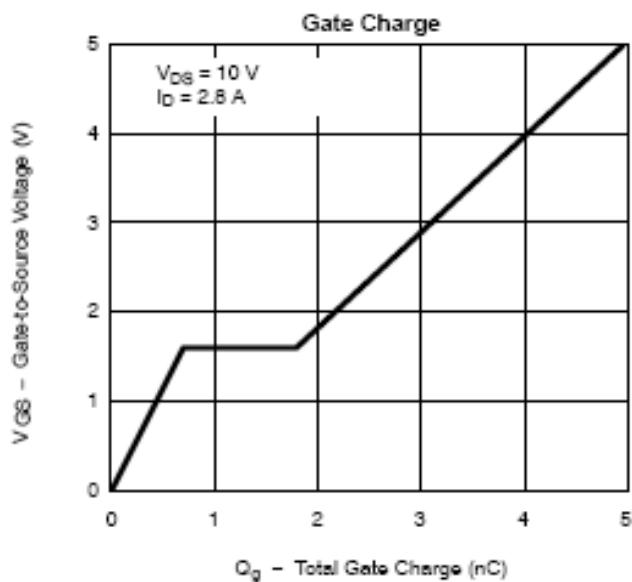




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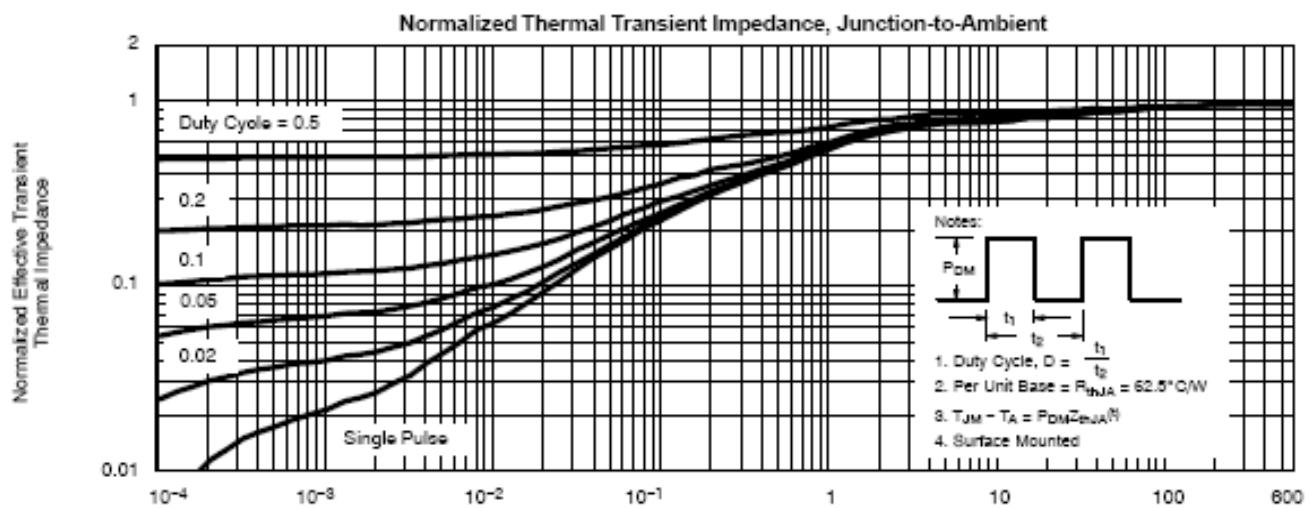
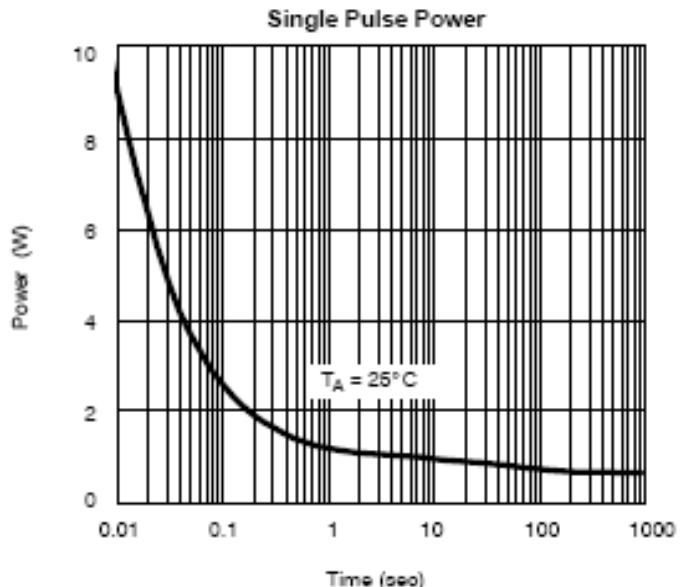
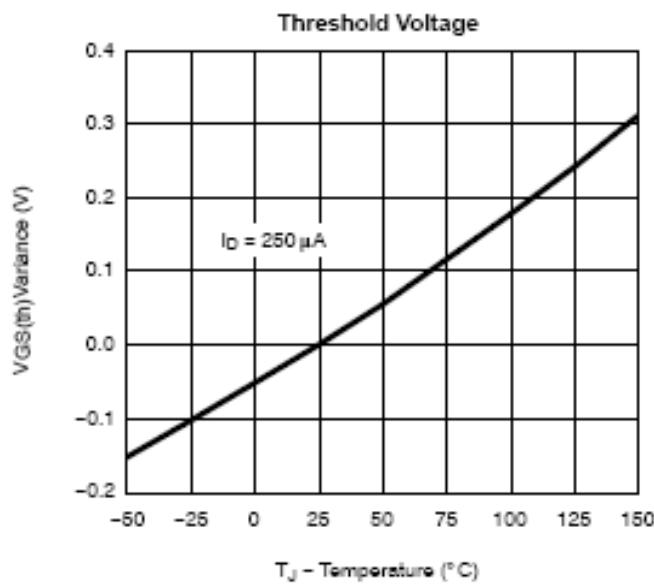




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