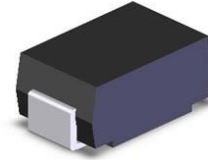


**SPD9251A**
**1-Line, Bi-directional, Thyristor Surge Suppressors**
<http://www.sh-willsemi.com>
**Descriptions**

The SPD9251A is a bi-directional TSS (Thyristor Surge Suppressors). It is specifically designed to protect telecom equipments from damaging overvoltage transients.

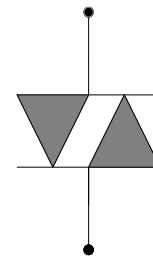
The SPD9251A is used to enable equipments to meet various regulatory requirements including ITU-T K.20, K.21 and IEC 61000-4-5


**SMA**

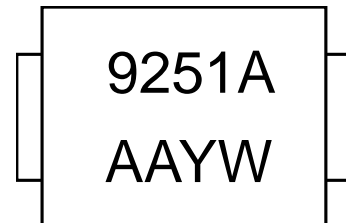
The SPD9251A is available in SMA package. Standard products are Pb-free and Halogen-free.

**Features**

- Peak off-state voltage:  $\pm 6.0V$  Max
- Excellent capability of absorbing transient surge
- Quick response to surge voltage
- Eliminate voltage overshoot caused by fast-rising transients
- Low leakage current:
- Solid-state silicon technology, non degenerative


**Schematic Diagram**
**Applications**

- Audio/Video line
- Network and telecom
- Data lines and security systems
- Serial ports
- BNC interface
- DVR



AA = Device code  
 Y = Year code  
 W = Week code

**Marking (Top View)**
**Order information**

Device	Package	Shipping
SPD9251A-2/TR	SMA	5000/Tape&Reel

**Electrical characteristics ( $T_A=25\text{ }^\circ\text{C}$ , unless otherwise noted)**

Part Number	$V_{\text{DRM}}$	$I_{\text{DRM}}$	$V_S$	$V_{\text{BR}}^1$	$I_S$	$I_H$	$V_T$	$I_T$	$C_o$	
	V	$\mu\text{A}$	V	V	mA	mA	V	A	pF	
		Max.	Max.	Min.		Max.	Max.		0V,1MHz	2V,1MHz
SPD9251A	6.0	1	25	6.2	800	150	4	2.2	13	9

Notes:

1)  $V_{\text{BR}}$  is measured at  $I_{\text{BR}}=1\text{ mA}$ .

Off-state capacitance is measured at  $f = 1\text{ MHz}$ ,  $V_{\text{DC}} = 2\text{ V}$ .

**Surge Ratings**

Part Number	$8/20\mu\text{s}^1$	$5/320\mu\text{s}^1$
	$1.2/50\mu\text{s}^2$	$10/700\mu\text{s}^2$
SPD9251A	140 A	2000 V

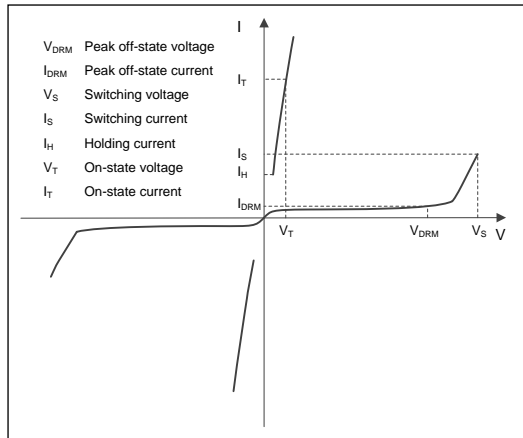
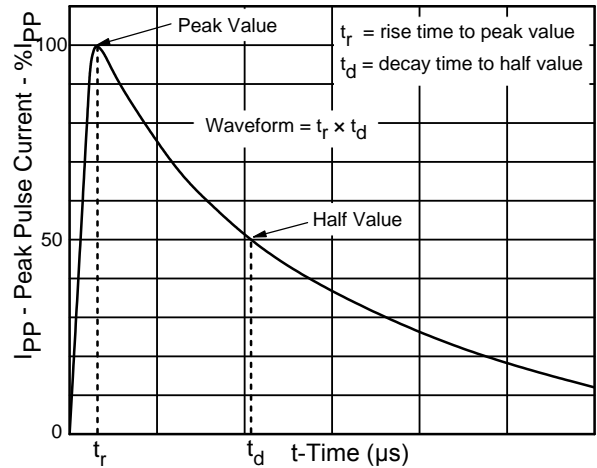
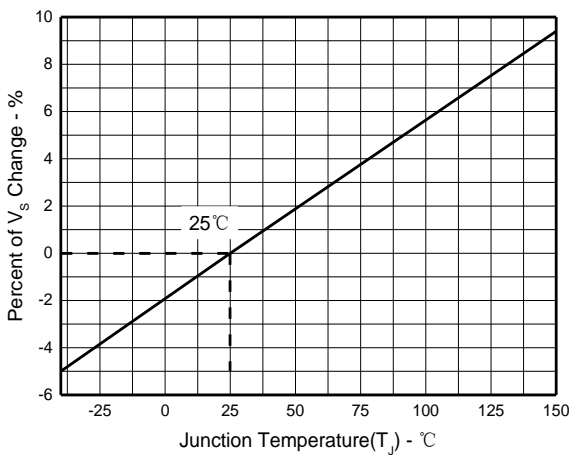
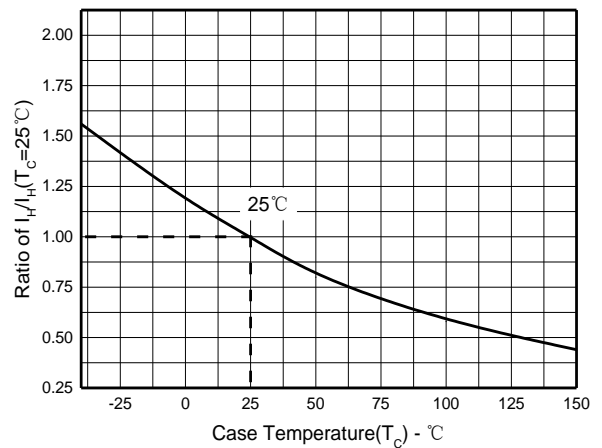
Notes:

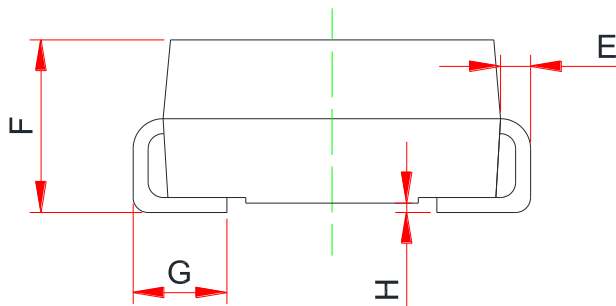
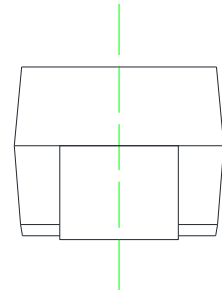
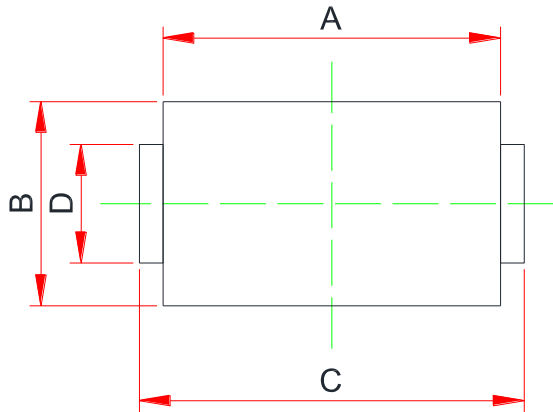
1) Current waveform.

2) Voltage waveform.

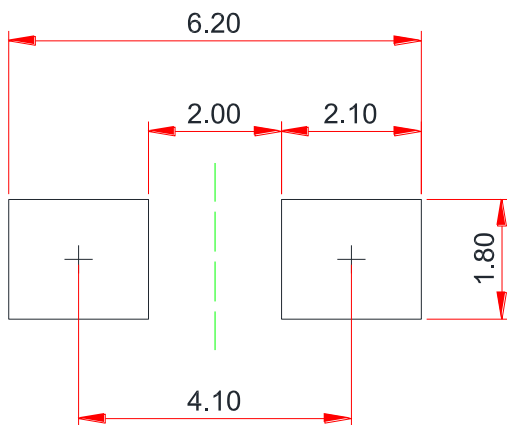
**Thermal considerations Thermal considerations**

Parameter	Symbol	Rating	Unit
Operation junction temperature	$T_J$	-40~150	$^{\circ}\text{C}$
Storage temperature	$T_{\text{STG}}$	-55~150	$^{\circ}\text{C}$
Lead temperature	$T_L$	260	$^{\circ}\text{C}$
Junction to ambient thermal resistance	$R_{\theta\text{JA}}$	90	$^{\circ}\text{C/W}$

**Typical characteristics ( $T_A=25^{\circ}\text{C}$ , unless otherwise noted)**

**Definitions of electrical characteristics**

**Peak pulse current waveform**

**Normalized  $V_S$  Change vs. Junction Temperature**

**Normalized Holding Current vs. Case Temperature**

**Package outline dimensions**
**SMA**


Symbol	Dimensions in millimeter		
	Min.	Typ.	Max.
A	3.990	-	4.500
B	2.540	-	2.790
C	4.930	-	5.280
D	1.250	-	1.650
E	0.152	-	0.305
F	1.980	-	2.290
G	0.780	-	1.520
H	-	-	0.203

**Recommend land pattern (Unit: mm)**

**Notes:**

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.