

SPE04M50T-A

TRANSFER-MOLD TYPE
FULL PACK TYPE

SPE04M50T-A

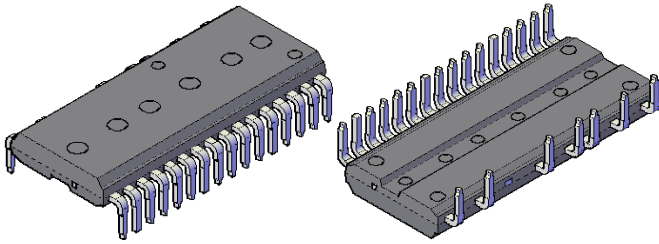


Figure 1

Applications

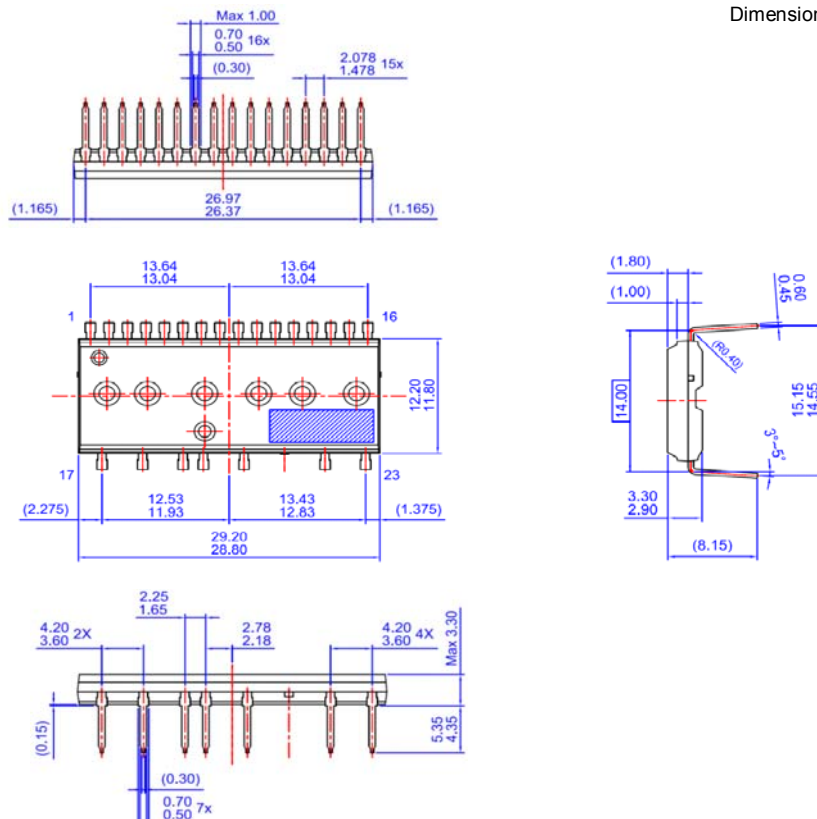
500V/4A low-loss MOSFET inverter driver for
Small Power AC Motor Drives

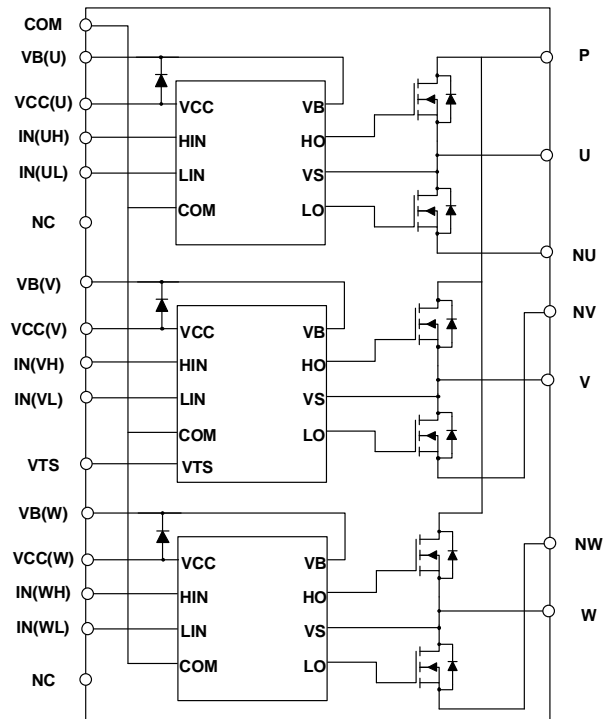
Features

- 500V $R_{ds(on)}=3.5\Omega$ (Max) MOSFET 3-Phase inverter with Gate Drivers and protection
- Separate Open-Source Pins from Low-Side MOSFETs for Three-Phase Current-Sensing
- Gate drive supply range from 10v to 20v
- Active-High interface, works with 3.3v/5v logic input, Schmitt-trigger input
- HVIC Temperature-Sensing Built-In for temperature Monitoring
- HVIC for Gate Driving and Under-voltage Protection
- Built-In Bootstrap Diodes simplify PCB layout
- Isolation Rating:1500Vrms/min

Detailed Package Outline Drawings

Dimensions in mm



Pin Configuration and Internal Block Diagram**Figure 2. Bottom view****Pin Descriptions**

Pin number	Pin name	Pin Description
1	COM	IC Common Supply Ground
2	VB(U)	Bias Voltage for U-Phase High-Side MOSFET Driving
3	VCC(U)	Bias Voltage for U-Phase IC and Low-Side MOSFET Driving
4	IN(UH)	Signal Input for U-Phase High-Side
5	IN(UL)	Signal Input for U-Phase Low-Side
6	NC	No Connection
7	VB(V)	Bias Voltage for V-Phase High Side MOSFET Driving
8	VCC(V)	Bias Voltage for V-Phase IC and Low Side MOSFET Driving
9	IN(VH)	Signal Input for V-Phase High-Side
10	IN(VL)	Signal Input for V-Phase Low-Side
11	VTS	Output for HVIC Temperature Sensing
12	VB(W)	Bias Voltage for W-Phase High-Side MOSFET Driving
13	VCC(W)	Bias Voltage for W-Phase IC and Low-Side MOSFET Driving
14	IN(WH)	Signal Input for W-Phase High-Side
15	IN(WL)	Signal Input for W-Phase Low-Side
16	NC	No Connection
17	P	Positive DC-Link Input
18	U	Output for U-Phase & Bias Voltage Ground for High-Side MOSFET Driving
19	NU	Negative DC-Link Input for U-Phase
20	NV	Negative DC-Link Input for V-Phase
21	V	Output for V-Phase & Bias Voltage Ground for High-Side MOSFET Driving
22	NW	Negative DC-Link Input for W-Phase
23	W	Output for W Phase & Bias Voltage Ground for High-Side MOSFET Driving

SPE04M50T-ATRANSFER-MOLD TYPE
FULL PACK TYPE**Absolute Maximum Ratings** (T_J= 25°C, Unless Otherwise Specified)**Inverter Part**

Symbol	Parameter	Conditions	Ratings	Unit
VDSS	Drain-Source Voltage of Each MOSFET		500	V
ID	Each MOSFET Current, Continuous	TC = 25°C	4	A
IDM	Each MOSFET Pulse Current, Peak	TC = 25°C, Less than 100us	12	A
IDrms	Each MOSFET Current, Rms	TC = 25°C, FPWM <20KHz	4	Arms
PD	Maximun Power Dissipation	TC = 25°C For each MOSFET	16.6	W

Control Part

Symbol	Parameter	Conditions	Ratings	Unit
VCC	Control Supply Voltage	Applied between VCC and COM	20	V
VBS	High-side Bias Voltage	Applied between VB and VS	20	V
VIN	Input Signal Voltage	Applied between VIN and COM	-0.3~VCC+0.3	V

Bootstrap Diode Part

Symbol	Parameter	Conditions	Ratings	Unit
VRRMB	Maximum Repetitive Reverse Voltage		500	V
IFB	Forward Current	TC = 25°C	1	A
IFPB	Forward Peak Current, Peak	TC = 25°C, Under 1ms Pulse Width	2.5	A

Total System

Symbol	Parameter	Conditions	Ratings	Unit
TJ	Operating Junction Temperature		-40~150	°C
TSTG	Storage Temperature	TC = 25°C	-40~125	°C
VISO	Isolation Voltage	60Hz, Sinusoidal, AC 1 minute, between pins and heat-sink plate	1500	V

Note:

- To insure safe operation of the IPM, the average junction temperature should be limited to T_J ≤ 150°C (@T_c ≤ 100°C).

Thermal Resistance

Symbol	Parameter	Conditions	Ratings	Unit
Rth(j-c)	Junction to Case Thermal resistance	For Each MOSFET	7.5	°C/W

Electrical Characteristics (T_J= 25°C, Unless Otherwise Specified)**Inverter Part**

Symbol	Parameter	Condition	Min.	Typ.	Max.	Units
BVDSS	Drain - Source Breakdown Voltage	V _{IN} = 0 V, I _D = 1 mA	500	-	-	V
IDSS	Zero Gate Voltage Drain Current	V _{IN} = 0 V, V _{DS} = 500 V	-	-	10	uA
VSD	Drain - Source Diode Forward Voltage	V _{CC} = V _{BS} = 15V, V _{IN} = 0 V, I _D = -1 A	-	0.95	1.5	V
RDS(on)	Drain-Source Turn-On Resistance	V _{CC} = V _{BS} = 15 V, V _{IN} = 5 V, I _D = 1A	-	1.45	1.8	ohm
t _{ON}	Switching Times	V _{PN} = 300 V, V _{CC} = V _{BS} = 15 V, I _D = 1 A V _{IN} = 0/5 V, Inductive Load L = 3 mH High- and Low-Side MOSFET Switching	-	1050	-	ns
t _{OFF}			-	450	-	ns
trr			-	200	-	ns
EON			-	50	-	uJ
EOFF			-	10	-	uJ
RBSOA	Reverse Bias Safe Operating Area	V _{PN} = 400 V, V _{CC} = V _{BS} = 15 V, I _D = I _{DP} , V _{DS} = BVDSS, T _J = 150°C High- and Low-Side MOSFET Switching	Full Square			

Control Part

Symbol	Parameter	Condition	Min.	Typ.	Max.	Units
I _{QCC}	Quiescent V _{CC} Supply Current	V _{CC} = 15V V _{IN} = 5V Applied between V _{CC} and COM	-	-	500	uA
I _{QB}	Quiescent V _{BS} Supply Current	V _{DB} = 15V V _{IN} = 5V Applied between V _{B(U)} - U, V _{B(V)} - V, V _{B(W)} - W	-	-	200	uA
UV _{CCD}	Low-Side Under-Voltage Protection	V _{CC} Under-Voltage Protection Detection Level	7.4	7.9	9	V
UV _{CCR}		V _{CC} Under-Voltage Protection Reset Level	8.0	8.6	9.8	V
UV _{BSD}	High-Side Under-Voltage Protection	V _{BS} Under-Voltage Protection Detection Level	7.4	7.9	9	V
UV _{BSR}		V _{BS} Under-Voltage Protection Reset Level	8.0	8.6	9.8	V
V _{TS}	HVIC Temperature Sensing Voltage Output	V _{CC} = 15 V, T _{HVIC} = 25°C	600	790	980	mV
V _{IH}	ON Threshold Voltage	Logic HIGH Level, Applied between V _{IN} and COM	2.7	-	-	V
V _{IL}	OFF Threshold Voltage	Logic Low Level, Applied between V _{IN} and COM	-	-	0.8	V
V _F	BSD Forward voltage	I _F = 0.1 A, T _C = 25°C	-	1.35	1.8	V
trr	Reverse Recovery Time	I _F = 0.1 A, T _C = 25°C	-	45	-	ns

SPE04M50T-A

TRANSFER-MOLD TYPE

FULL PACK TYPE

Recommended Operating Conditions

Symbol	Parameter	Condition	Min.	Typ.	Max.	Units
VPN	Supply Voltage	Applied between P and N	-	300	400	V
VCC	Control Supply Voltage	Applied between VCC and COM	13.5	15.0	16.5	V
VBS	High-Side Bias Voltage	Applied between VB and VS	13.5	15.0	16.5	V
VIN(ON)	Input ON Threshold Voltage	Applied between VIN and COM	3.0	-	10	V
VIN(OFF)	Input OFF Threshold Voltage		0	-	0.6	V
tdead	Blanking Time for Preventing Arm-Short	VCC = VBS = 13.5 ~ 16.5 V, TJ <150°C	1.0	-	-	us
fPWM	PWM Switching Frequency	TJ <150°C	-	15	-	KHz