



SPN3006

N-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPN3006 is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology. The SPN3006 has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers. It has been optimized for low gate charge, low RDS(ON) and fast switching speed.

FEATURES

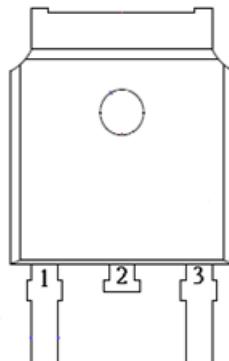
- ◆ 30V/80A,RDS(ON)=4.7mΩ@V_{GS}=10V
- ◆ 30V/80A,RDS(ON)=7.5mΩ@V_{GS}=4.5V
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ TO-252-2L/TO-251S-3L package design

APPLICATIONS

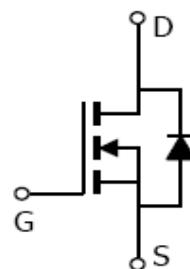
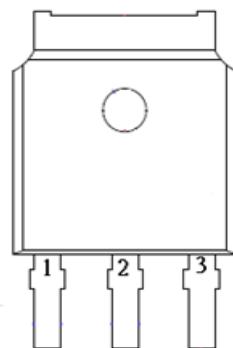
- High Frequency Synchronous Buck Converter
- DC/DC Power System
- Load Switch

PIN CONFIGURATION

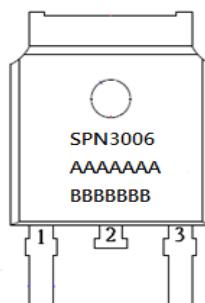
TO-252-2L



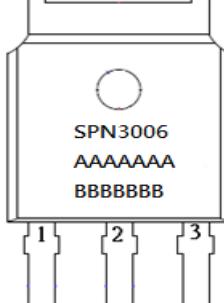
TO-251S-3L



PART MARKING



A : Lot Code
B : Date Code



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B : Date Code



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PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	D	Drain
3	S	Source

ORDERING INFORMATION

Part Number	Package	Part Marking
SPN3006ST251TGB	TO-251S-3L	SPN3006
SPN3006T252RGB	TO-252-2L	SPN3006

※ SPN3006T252RGB : Tape Reel ; Pb – Free ; Halogen - Free
※ SPN3006ST251TGB: Tube ; Pb – Free; Halogen – Free

ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V _{DSS}	30	V
Gate –Source Voltage	V _{GSS}	±20	V
Continuous Drain Current	TA=25°C	80	A
	TA=100°C	57	
Pulsed Drain Current	I _{DM}	160	A
Avalanche Current	I _{AS}	48	A
Single Pulse Avalanche Energy	E _{AS}	259	mJ
Power Dissipation	P _D	2	W
Operating Junction Temperature	T _J	150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Ambient (t≤10s)	R _{θJA}	62	°C/W



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ELECTRICAL CHARACTERISTICS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, ID=250uA	30			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , ID=250uA	1.0		2.5	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =24V, V _{GS} =0V			1	
		V _{DS} =24V, V _{GS} =0V, T _J =55°C			5	uA
On-State Drain Current	I _{D(on)}	V _{DS} ≥5V, V _{GS} =10V			80	A
Drain-Source On-Resistance	R _{D(on)}	V _{GS} =10V, ID=30A		4.7	5.5	
		V _{GS} =4.5V, ID=15A		7.5	9	mΩ
Forward Transconductance	g _{fs}	V _{DS} =5V, ID=30A		22		S
Diode Forward Voltage	V _{SD}	I _S =1A, V _{GS} =0V			1	V
Single Pulse Avalanche Energy	E _{AS}	V _{DD} =25V, L=0.1mH, I _{AS} =24A	60			mJ
Dynamic						
Total Gate Charge	Q _g	V _{DS} =15V, V _{GS} =4.5V ID=20A		11.1	18	nC
Gate-Source Charge	Q _{gs}			1.85	3.8	
Gate-Drain Charge	Q _{gd}			6.8	12	
Input Capacitance	C _{iss}	V _{DS} =24V, V _{GS} =0V f=1MHz		1210	1800	pF
Output Capacitance	C _{oss}			190	280	
Reverse Transfer Capacitance	C _{rss}			100	150	
Turn-On Time	t _{d(on)}	V _{DD} =15V, ID=15A, V _{GEN} =10V R _G =3.3Ω		7.5	14	nS
	t _r			14.5	28	
Turn-Off Time	t _{d(off)}			35.2	67	
	t _f			9.6	18	



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TYPICAL CHARACTERISTICS

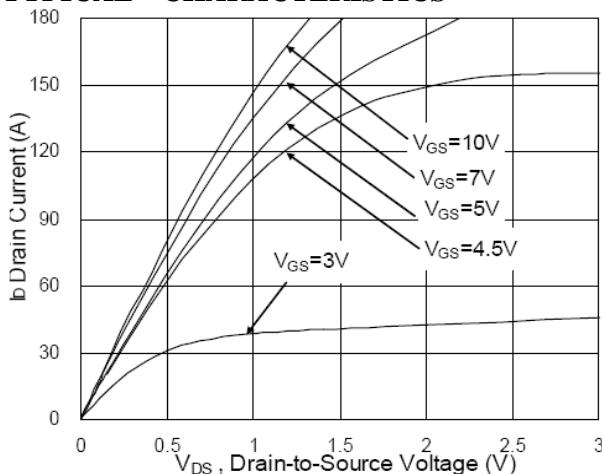


Fig. 1 Typical Output Characteristics

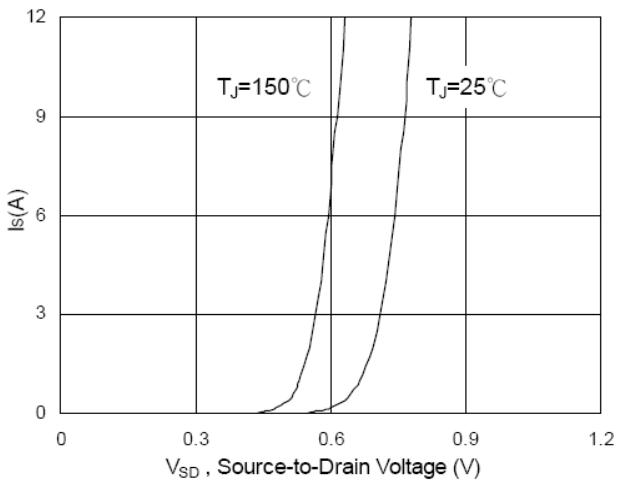


Fig. 2 Transfer Characteristics

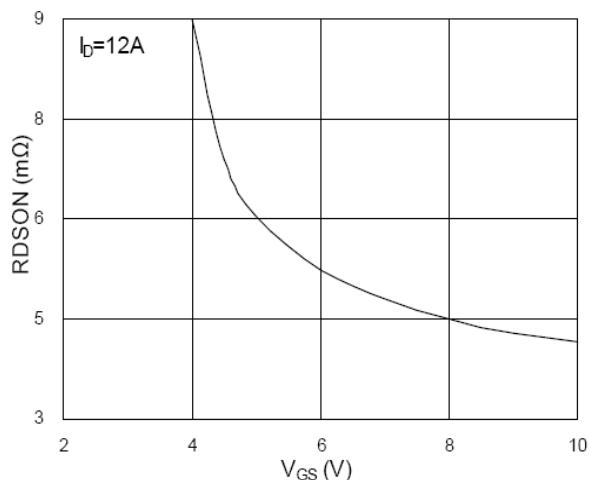


Fig. 3 On-Resistance vs Gate voltage

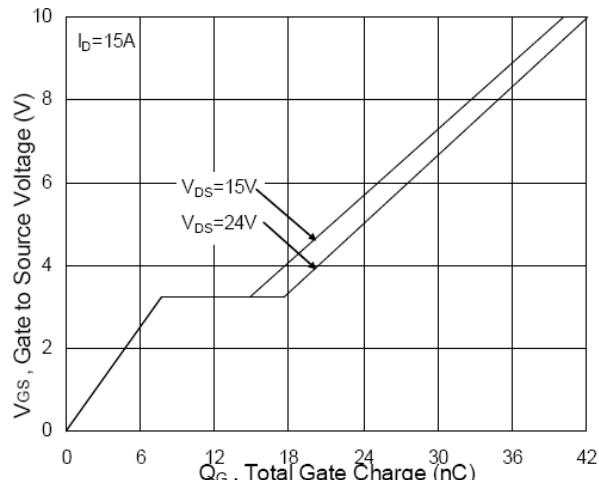


Fig. 4 Gate Charge Characteristics

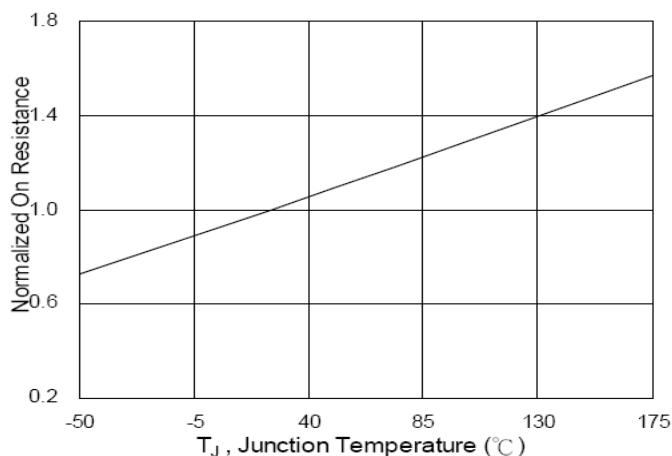


Fig. 5 On-Resistance vs Junction Temp

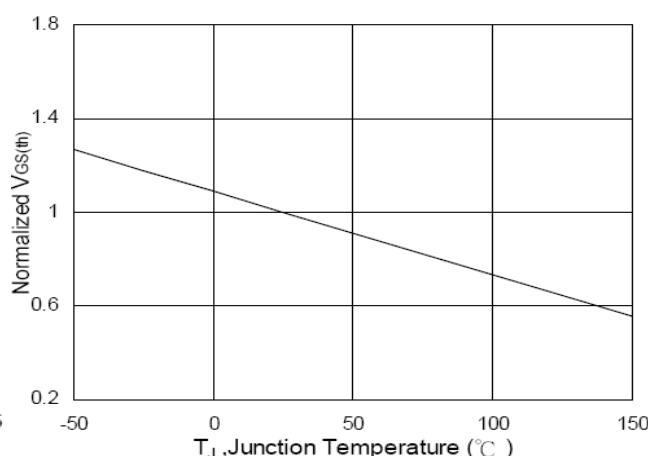


Fig. 6 V_{GS} vs Junction Temperature



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TYPICAL CHARACTERISTICS

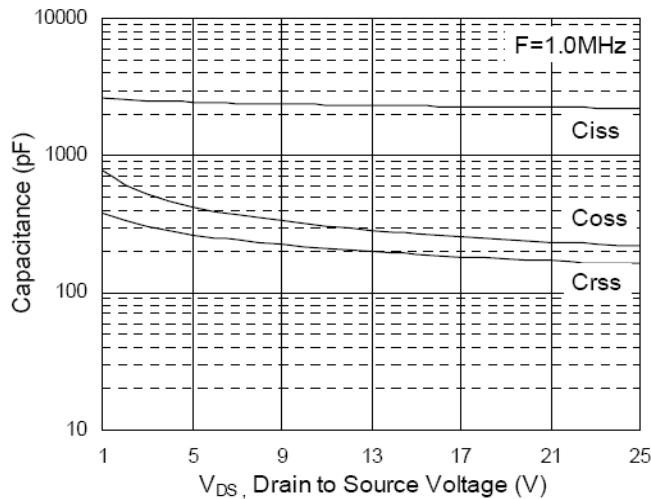


Fig. 7 Typical Capacitance Characteristics

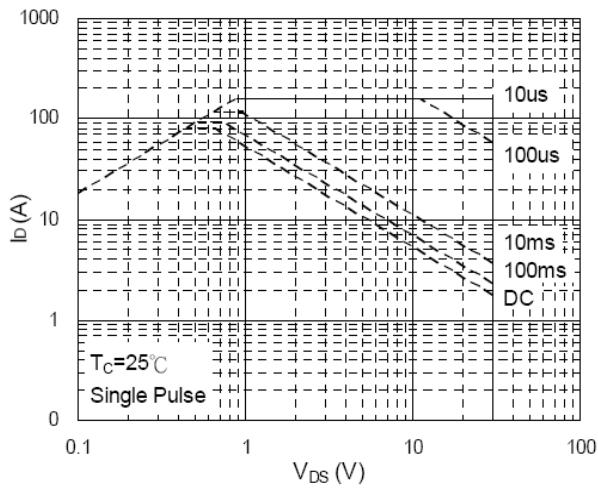


Fig. 8 Maximum Safe Operation Area

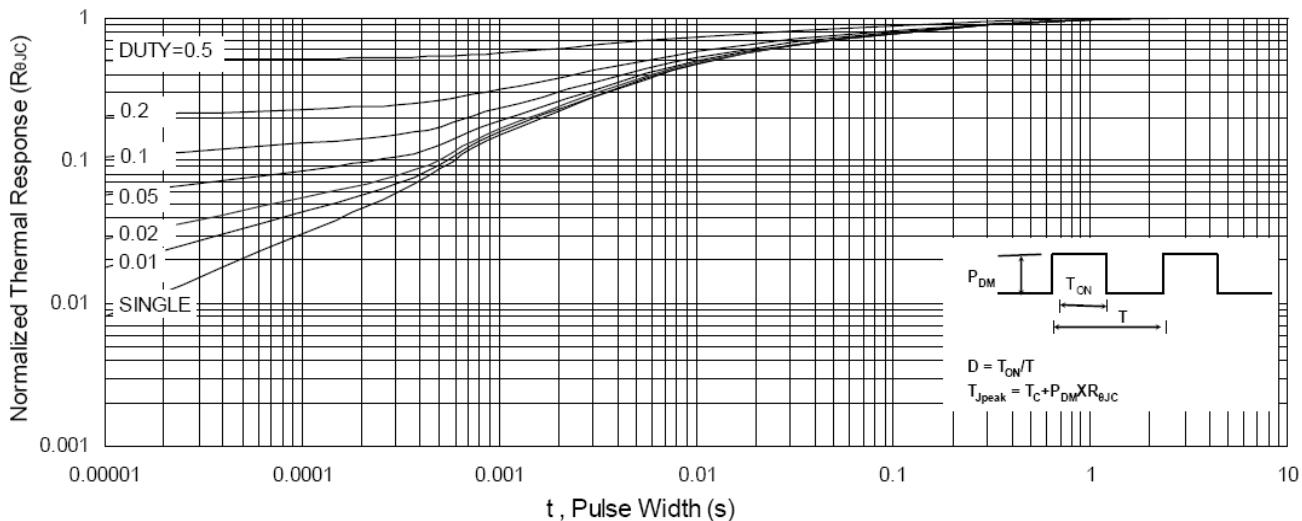


Fig. 9 Effective Transient Thermal Impedance

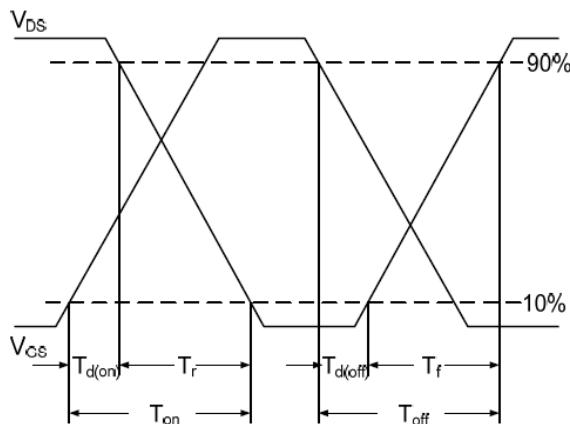


Fig. 10 Switching Time Waveform

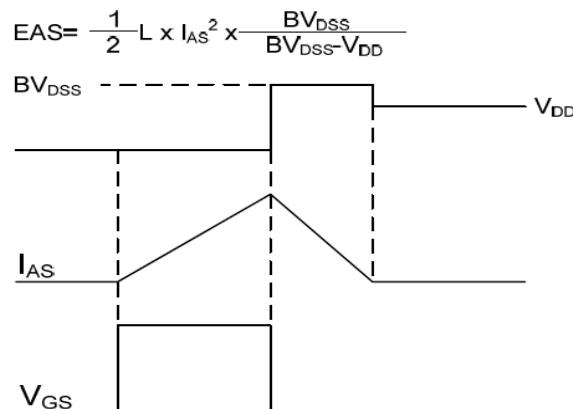


Fig. 11 Unclamped Inductive Waveform



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