SPN4392 N-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPN4392 is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application, notebook computer power management and other battery powered circuits where high-side switching.

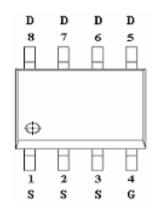
FEATURES

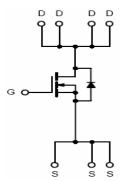
- 30V/22A, RDS(ON)= $8m\Omega$ @VGS=10V
- 30V/18A, RDS(ON)= $12m\Omega$ @VGS=4.5V
- ◆ Super high density cell design for extremely low RDS (ON)
- Exceptional on-resistance and maximum DC current capability
- ♦ SOP 8P package design

APPLICATIONS

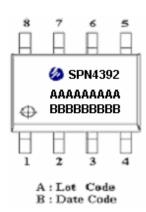
- Power Management in Note book
- Portable Equipment
- Battery Powered System
- High-Side DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

PIN CONFIGURATION(SOP – 8P)





PART MARKING



PIN	DES	CR	TPT	TO	N
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Pin	Symbol	Description
1	S	Source
2	S	Source
3	S	Source
4	G	Gate
5	D	Drain
6	D	Drain
7	D	Drain
8	D	Drain

ORDERING INFORMATION

Part Number	Package	Part Marking
SPN4392S8RG	SOP- 8P	SPN4392
SPN4392S8TG	SOP- 8P	SPN4392

※ SPN4392S8RG : 13" Tape Reel ; Pb − Free

% SPN4392S8TG: Tube; Pb – Free

ABSOULTE MAXIMUM RATINGS

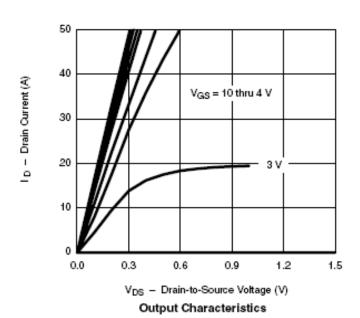
(Ta=25°C Unless otherwise noted)

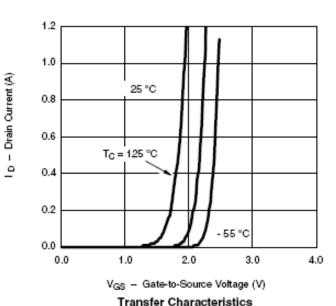
Parameter	Symbol	Typical	Unit		
Drain-Source Voltage	Vdss	30	V		
Gate –Source Voltage	VGSS	±20	V		
Continuous Drain Current(Tr-150°C)	TA=25°C	In	22	А	
Continuous Drain Current(TJ=150°€)	TA=70°C	- Id	18	A	
Pulsed Drain Current	Ірм	50	А		
Continuous Source Current(Diode Conduct	Is	5.6	А		
Downer Dissinction	TA=25°C	Do	2.5	W	
Power Dissipation	TA=70°C	PD	1.6	W	
Operating Junction Temperature	TJ	-55/150	$^{\circ}$		
Storage Temperature Range	Tstg	-55/150	$^{\circ}\!\mathbb{C}$		
Thermal Resistance-Junction to Ambient	RθJA	80	°C/W		

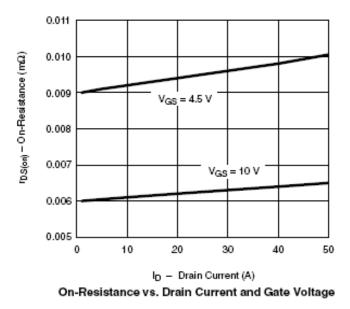
ELECTRICAL CHARACTERISTICS

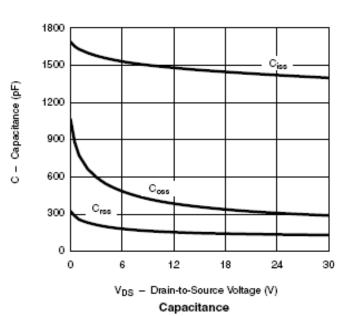
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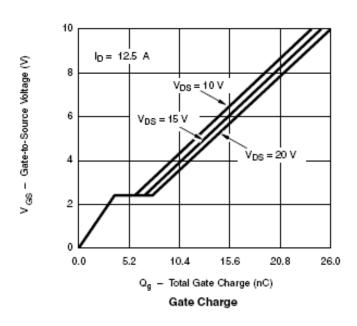
Parameter	Symbol	Conditions	Min.	Тур	Max.	Unit		
Static								
Drain-Source Breakdown Voltage	V(BR)DSS	$V_{GS} = 0V$, $I_D = 250uA$	30			V		
Gate Threshold Voltage	VGS(th)	$V_{DS} = V_{GS}, I_{DS} = 250uA$	0.6		1.6	v		
Gate Leakage Current	Igss	$V_{DS} = 0V, V_{GS} = \pm 20 \text{ V}$			±100	nA		
		$V_{DS} = 30V, V_{GS} = 0V$ $V_{DS} = 30V, V_{GS} = 0V,$ $T_{J} = 125C$			1	uA		
Zero Gate Voltage Drain Current	IDSS				100			
Drain-Source On-Resistance	RDS(on)	V _{GS} = 10V, ID = 13A V _{GS} = 4.5V, ID = 10A		0.006	0.008 0.012	Ω		
Forward Transconductance	gfs	$V_{DS} = 15V, I_{D} = 20 A$	10	0.002	0.012	S		
Diode Forward Voltage	Vsd	$I_F = 13 \text{ A}, V_{GS} = 0 \text{ V}$		1.0	1.5	V		
Dynamic								
Total Gate Charge	Qg			12	20	nC		
Gate-Source Charge	Qgs	$V_{DS} = 15V, V_{GS} = 5V,$ $I_{D} = 13 \text{ A}$		4				
Gate-Drain Charge	Qgd	-1D -13 A		5				
Input Capacitance	Ciss			1500		pF		
Output Capacitance	Coss	$V_{GS} = 0V$, $V_{DS} = 25V$, F=1MHz		320				
Reverse Transfer Capacitance	Crss			200		1		
Turn-On Time	td(on)			8	12	ns		
Turn-On Time	tr	$V_{DD} = 15 \text{ V}, I_{D} = 13 \text{ A},$		10	15			
T. OMT.	td(off)	$V_{GS}=10V,R_G=2.5\Omega)$		18	30			
Turn-Off Time	tf			6	9			

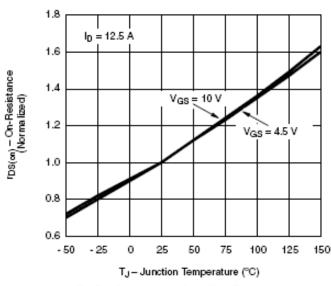




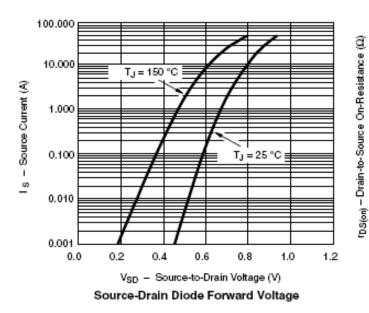


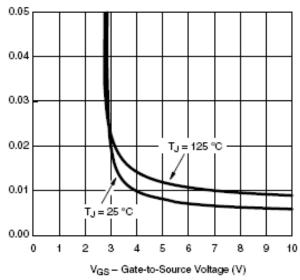




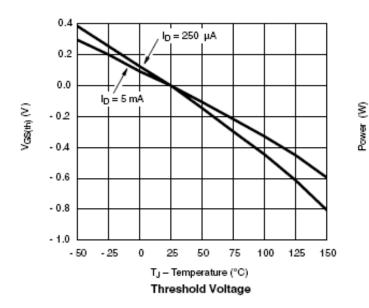


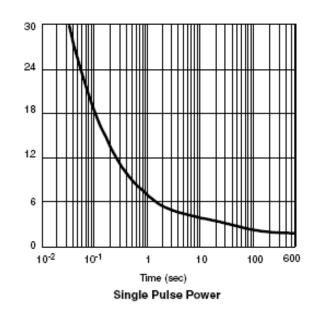
On-Resistance vs. Junction Temperature

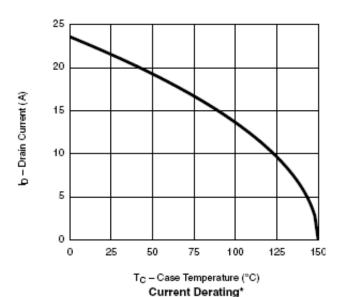


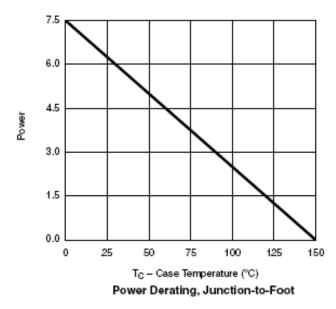


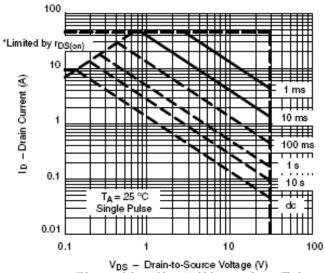
On-Resistance vs. Gate-to-Source Voltage











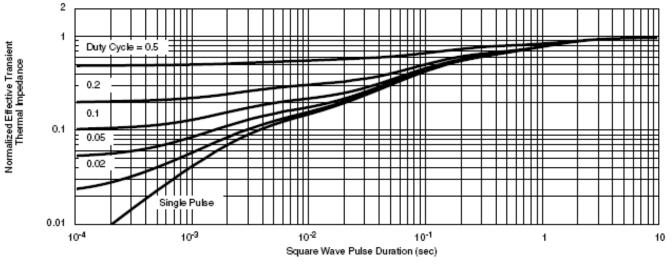
1.70 1.36 1.02 0.68 0.34 0.00 0 25 50 75 100 125 150

V_{DS} - Drain-to-Source Voltage (V)

*V_{GS} > minimum V_{GS} at which r_{DS(cn)} is specified

Safe Operating Area, Junction-to-Ambient

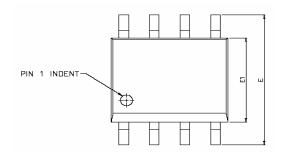


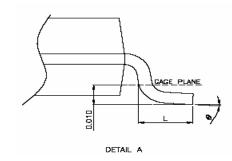


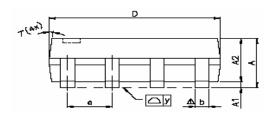
Normalized Thermal Transient Impedance, Junction-to-Foot

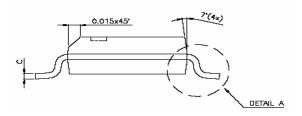


SOP- 8 PACKAGE OUTLINE









0.4.4001.0	DIMENSIONS IN MILLIMETERS		DIMENSIONS IN INCHES			
SYMBOLS	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.47	1.60	1.73	0.058	0.063	0.068
A1	0.10		0.25	0.004		0.010
A2		1.45			0.057	
Ь	0.33	0.41	0.51	0.013	0.016	0.020
С	0.19	0.20	0.25	0.0075	0.008	0.0098
D	4.80	4.85	4.95	0.189	0.191	0.195
Е	5.80	6.00	6.20	0.228	0.236	0.244
E1	3.80	3.90	4.00	0.150	0.154	0.157
е		1.27			0.050	
L	0.38	0.71	1.27	0.015	0.028	0.050
<u>∕</u> 2 y			0.076			0.003
0	0,		8.	0,		8*

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