



# SPN4546

## N-Channel Enhancement Mode MOSFET

### DESCRIPTION

The SPN4546 is the N-Channel logic enhancement mode power field effect transistors are produced using high cell density , DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

These devices are particularly suited for low voltage application , notebook computer power management and other battery powered circuits where high-side switching .

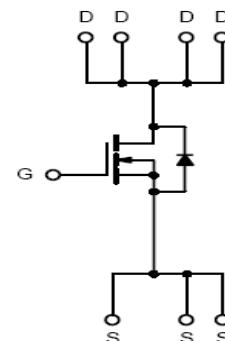
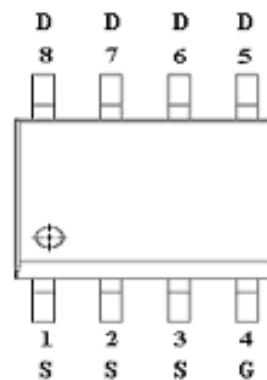
### FEATURES

- ◆ 40V/6.0A,R<sub>DS(ON)</sub>=48mΩ@V<sub>GS</sub>=10V
- ◆ 40V/5.0A,R<sub>DS(ON)</sub>=55mΩ@V<sub>GS</sub>=4.5V
- ◆ 40V/4.5A,R<sub>DS(ON)</sub>=80mΩ@V<sub>GS</sub>=2.5V
- ◆ Super high density cell design for extremely low RDS (ON)
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ SOP-8 package design

### APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

### PIN CONFIGURATION(SOP-8)



### PART MARKING



A : Lot Code  
B : Date Code



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### PIN DESCRIPTION

Pin	Symbol	Description
1	S	Source
2	S	Source
3	S	Source
4	G	Gate
5	D	Drain
6	D	Drain
7	D	Drain
8	D	Drain

### ORDERING INFORMATION

Part Number	Package	Part Marking
SPN4546S8RGB	SOP-8	SPN4546

※ SPN4546S8RGB 13" Tape Reel ; Pb – Free ; Halogen – Free

### ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V <sub>DSS</sub>	40	V
Gate –Source Voltage	V <sub>GSS</sub>	±12	V
Continuous Drain Current(T <sub>J</sub> =150°C)	TA=25°C	6.0	A
	TA=70°C		
Pulsed Drain Current	I <sub>DM</sub>	30	A
Continuous Source Current(Diode Conduction)	I <sub>S</sub>	2.3	A
Power Dissipation	TA=25°C	2.5	W
	TA=70°C		
Operating Junction Temperature	T <sub>J</sub>	-55/150	°C
Storage Temperature Range	T <sub>STG</sub>	-55/150	°C
Thermal Resistance-Junction to Ambient	R <sub>θJA</sub>	80	°C/W



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### ELECTRICAL CHARACTERISTICS

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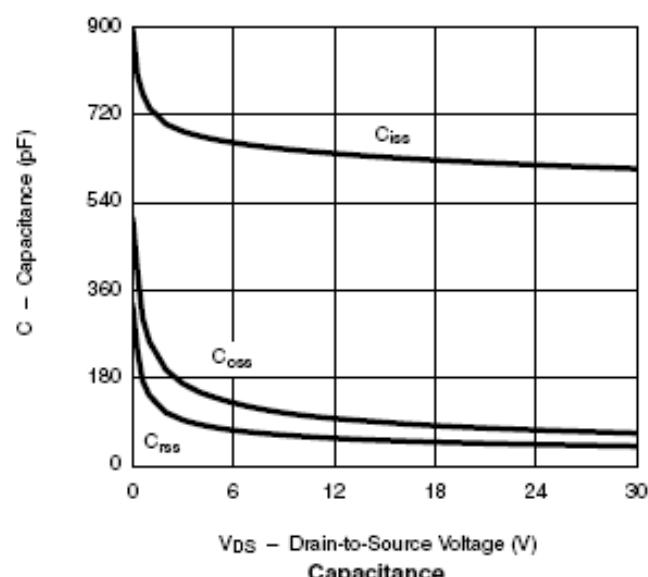
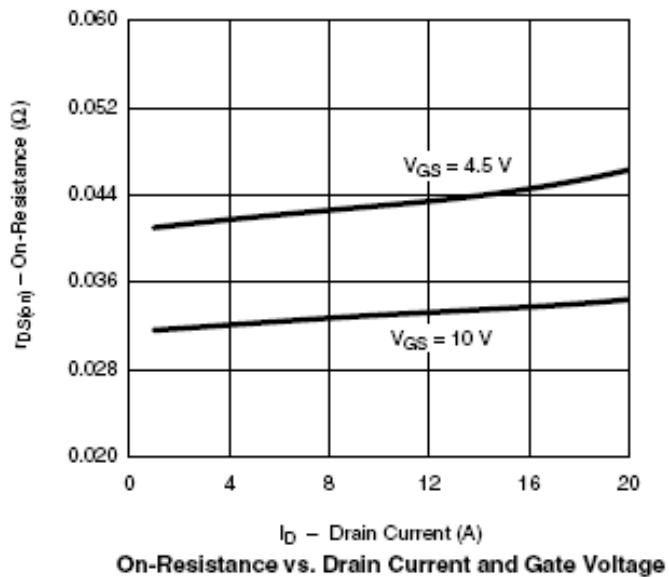
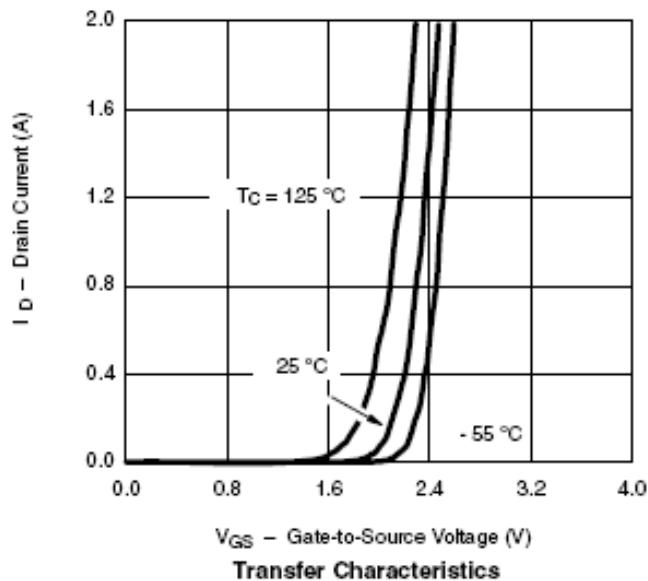
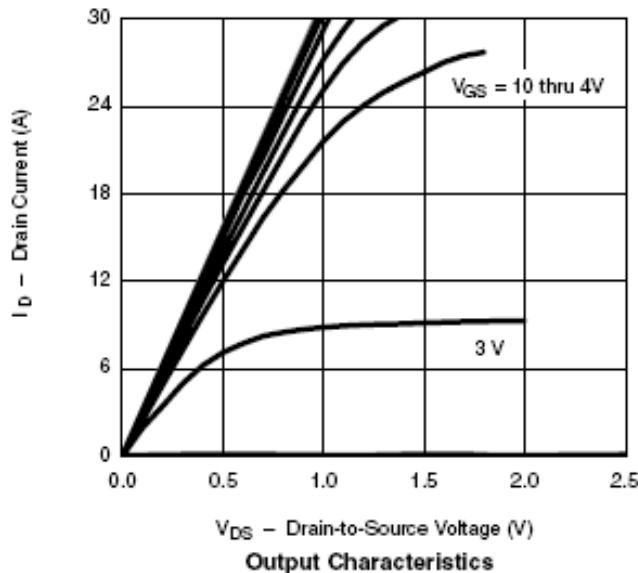
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, ID=250uA	40			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , ID=250uA	0.5		1.2	
Gate Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±12V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V			1	uA
		V <sub>DS</sub> =40V, V <sub>GS</sub> =0V T <sub>J</sub> =85°C			5	
On-State Drain Current	I <sub>D(on)</sub>	V <sub>DS</sub> =5V, V <sub>GS</sub> =4.5V	10			A
Drain-Source On-Resistance	R <sub>DSS(on)</sub>	V <sub>GS</sub> =10V, ID=6.0A		0.040	0.048	Ω
		V <sub>GS</sub> =4.5V, ID=5.0A		0.047	0.055	
		V <sub>GS</sub> =2.5V, ID=4.5A		0.072	0.080	
Forward Transconductance	g <sub>f</sub> s	V <sub>DS</sub> =15V, ID=6.2A		13		S
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =2.3A, V <sub>GS</sub> =0V		0.8	1.2	V
<b>Dynamic</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =10V ID=2A		10	15	nC
Gate-Source Charge	Q <sub>gs</sub>			1.6		
Gate-Drain Charge	Q <sub>gd</sub>			2.0		
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V f=1MHz		500		pF
Output Capacitance	C <sub>oss</sub>			80		
Reverse Transfer Capacitance	C <sub>rss</sub>			45		
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> =15V, R <sub>L</sub> =15Ω ID=1.0A, V <sub>GEN</sub> =10V R <sub>G</sub> =6Ω		15	20	nS
	t <sub>r</sub>			6	12	
Turn-Off Time	t <sub>d(off)</sub>			10	20	
	t <sub>f</sub>			40	80	



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### TYPICAL CHARACTERISTICS

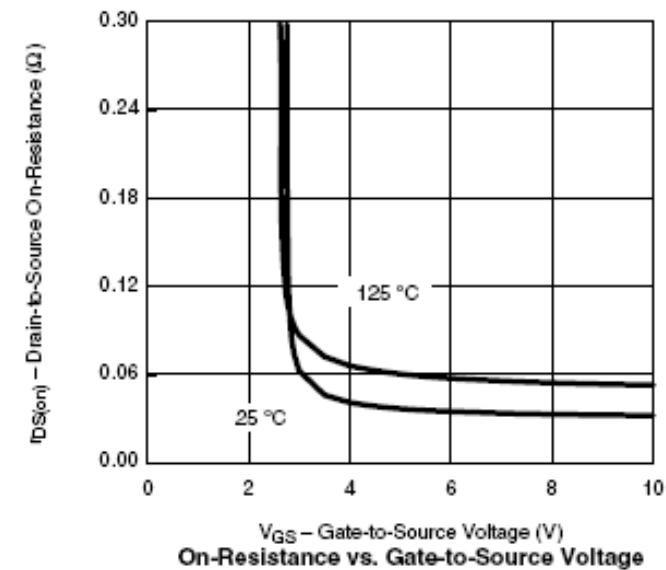
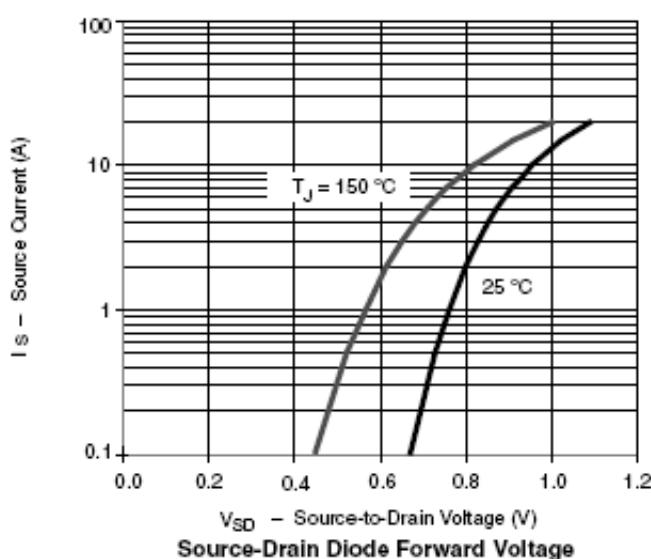
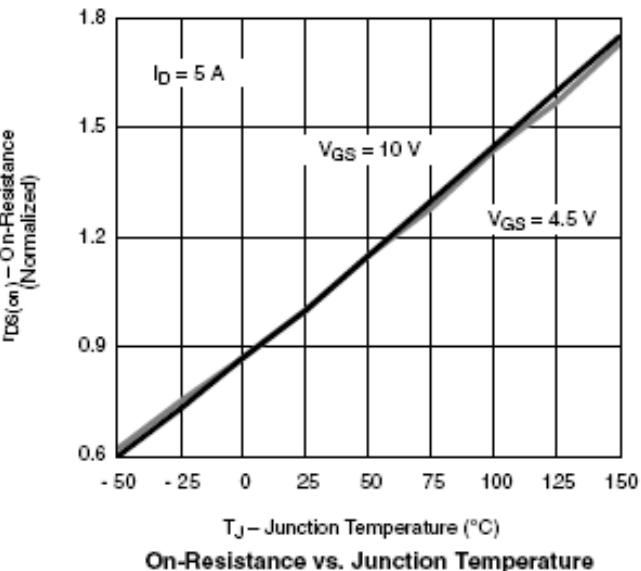
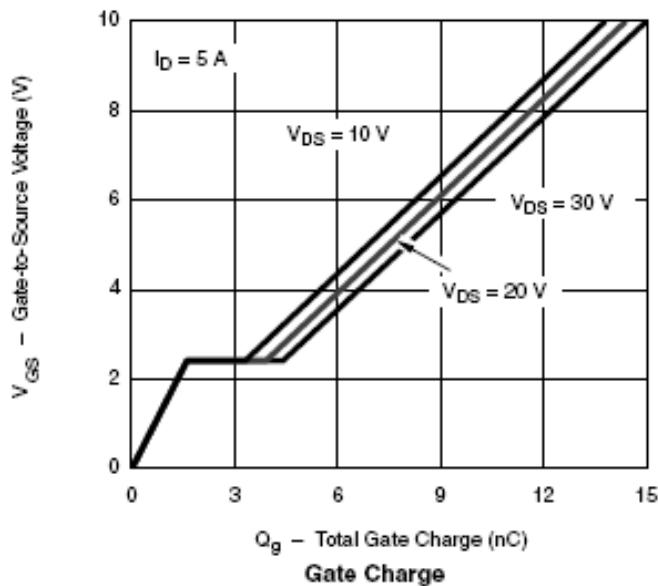




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### TYPICAL CHARACTERISTICS

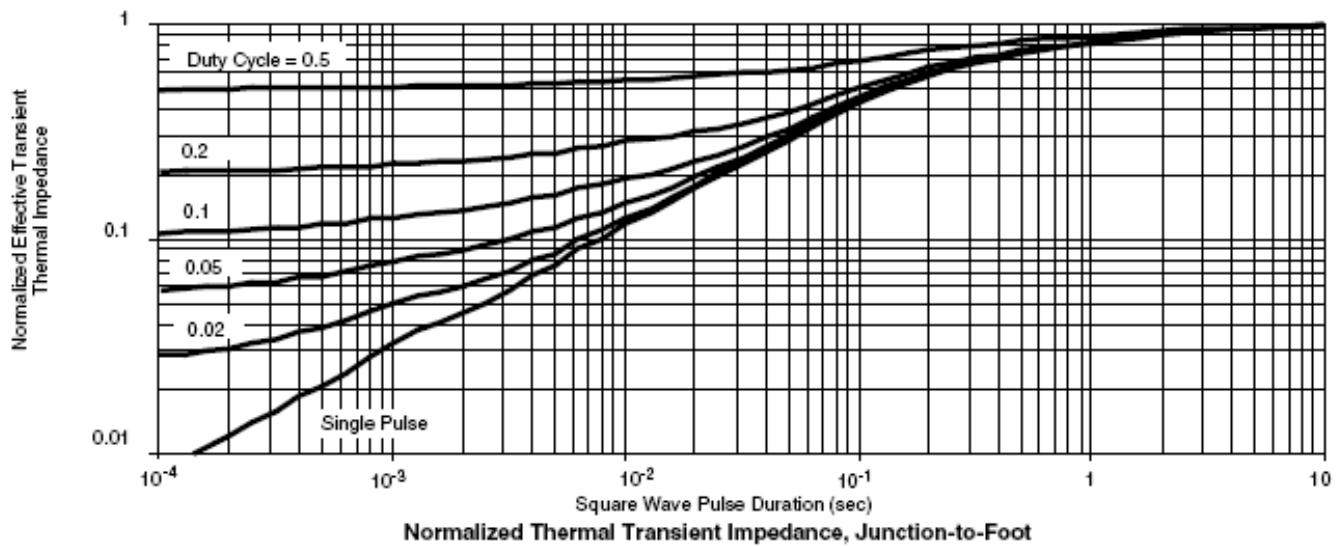
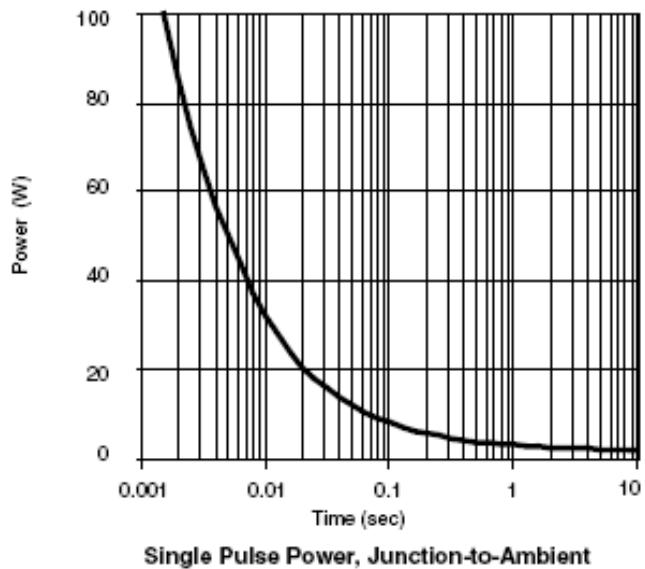
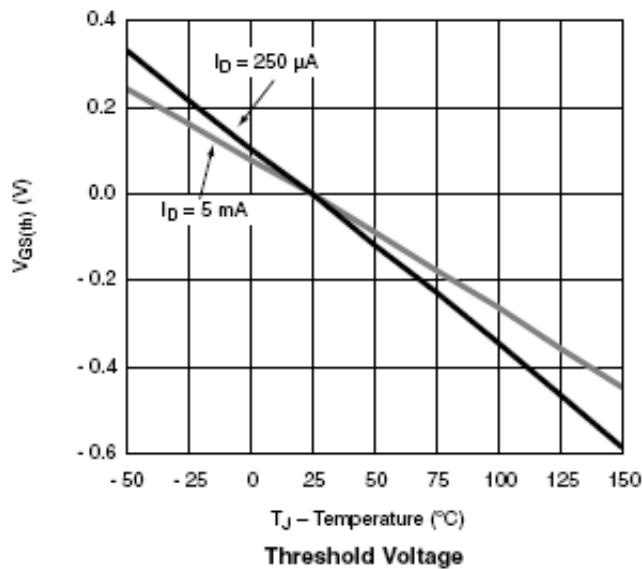




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### TYPICAL CHARACTERISTICS





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