

DESCRIPTION

The SPN6335 is the Dual N-Channel enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology. This high density process is especially tailored to minimize on-state resistance and provide superior switching performance. These devices are particularly suited for low voltage applications such as notebook computer power management and other battery powered circuits where high-side switching, low in-line power loss, and resistance to transients are needed.

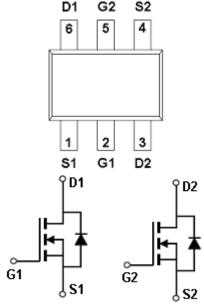
APPLICATIONS

- Power Management in Note book
- Portable Equipment
- Battery Powered System
- DC/DC Converter
- Load Switch
- DSC
- LCD Display inverter

FEATURES

- N-Channel 20V/0.95A,RDs(ON)=380mΩ@VGs=4.5V 20V/0.75A,RDs(ON)=450mΩ@VGs=2.5V 20V/0.65A,RDs(ON)=800mΩ@VGs=1.8V
- Super high density cell design for extremely low RDS (ON)
- Exceptional on-resistance and maximum DC current capability
- SOT-363 (SC-70-6L) package design

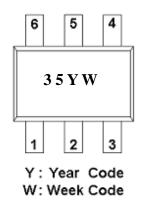
PIN CONFIGURATION(SOT-363/SC-70-6L)



n-channel

n-channel

PART MARKING





PIN DESCRIPTION						
Pin	Symbol	Description				
1	S1	Source 1				
2	G1	Gate 1				
3	D2	Drain 2				
4	S2	Source 2				
5	G2	Gate 2				
6	D1	Drain1				

ORDERING INFORMATION

Part Number	Package	Part Marking		
SPN6335S36RGB	SOT-363	35		

Week Code : A ~ Z(1 ~ 26); a ~ z(27 ~ 52)

X SPN6335S36RGB : Tape Reel ; Pb – Free ; Halogen – Free

ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter		Symbol	Typical	Unit	
Drain-Source Voltage		VDSS	20	V	
Gate –Source Voltage		VGSS	±12	V	
	TA=25°C	ID	1.2	٨	
Continuous Drain Current(TJ=150°C)	Ta=80°C	ID	0.9	- A	
Pulsed Drain Current		Idm	4	Α	
Continuous Source Current(Diode Conduction)		Is	0.6	А	
	TA=25°C	– PD	0.35	W	
Power Dissipation	TA=70°C	PD	0.19	vv	
Operating Junction Temperature		TJ	-55/150	°C	
Storage Temperature Range		Tstg	-55/150	°C	

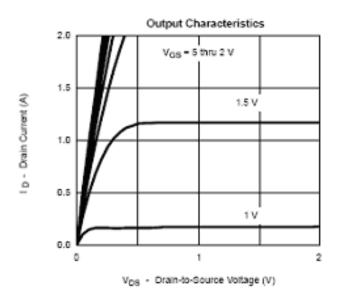


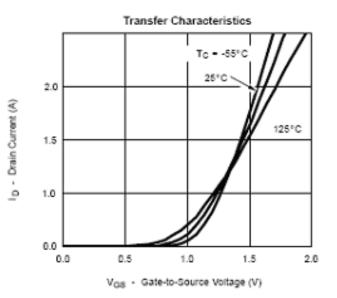
ELECTRICAL CHARACTERISTICS

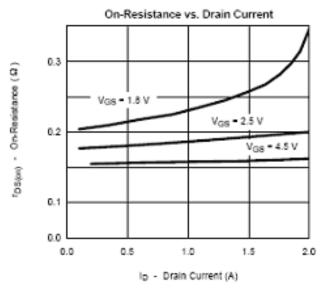
(TA=25°C Unless otherwise noted)

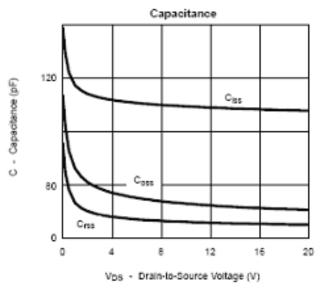
Parameter	Symbol	Conditions	Min.	Тур	Max.	Unit	
Static		·					
Drain-Source Breakdown Voltage	V(BR)DSS	VGS=0V,ID= 250uA	20			V	
Gate Threshold Voltage	VGS(th)	VGS(th) VDS=VGS,ID=250uA			1.0	V	
Gate Leakage Current	IGSS	VDS=0V,VGS=±12V			100	nA	
Zero Gate Voltage Drain Current		VDS= 20V, VGS=0V			1	uA	
	Idss	Vds= 20V,Vgs=0V Tj=55°C			5		
On-State Drain Current	ID(on)	$VDS \ge 4.5V, VGS = 5V$	0.7			А	
Drain-Source On-Resistance	RDS(on)	VGS=4.5V,ID=0.95A		0.26	0.38		
		VGS=2.5V,ID=0.75A		0.32	0.45	Ω	
		Vgs=1.8V,Id=0.65A		0.42	0.80		
Forward Transconductance	gfs	VDS=10V,ID=0.4A		1.0		S	
Diode Forward Voltage	Vsd	Is=0.15A,Vgs=0V		0.8	1.2	V	
Dynamic							
Total Gate Charge	Qg	Vds=10V,Vgs=4.5V,		1.2	1.5	nC	
Gate-Source Charge	Qgs	ID=0.6A		0.2			
Gate-Drain Charge	Qgd			0.3			
Turn-On Time	td(on)	$V_{DD}=10V,RL=10\Omega$,		5	10	nS	
	tr	ID=0.5A		8	15		
Turn-Off Time	td(off)	VGEN=4.5V, RG=6 Ω		10	18		
	tf]		1.2	2.8		

TYPICAL CHARACTERISTICS

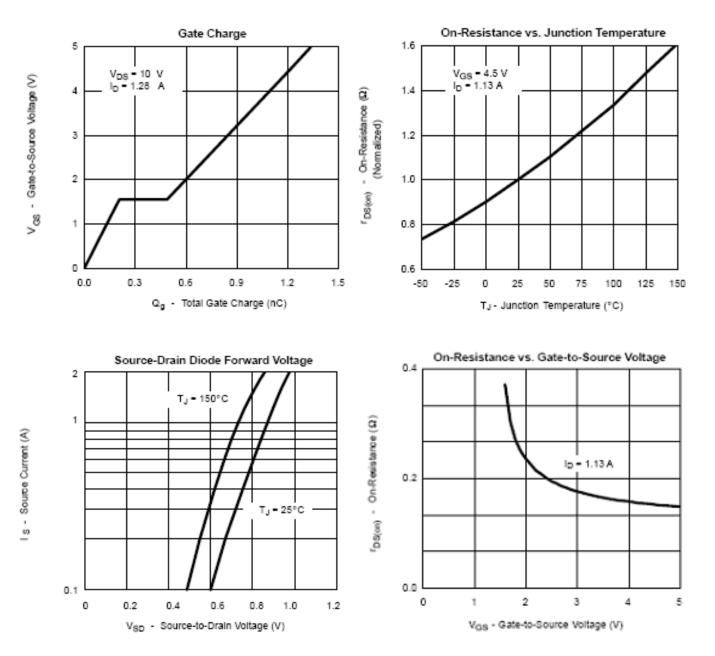




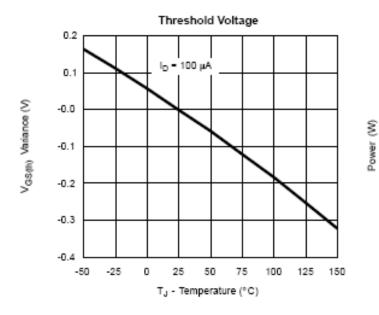


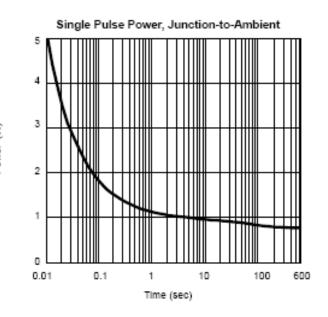


TYPICAL CHARACTERISTICS

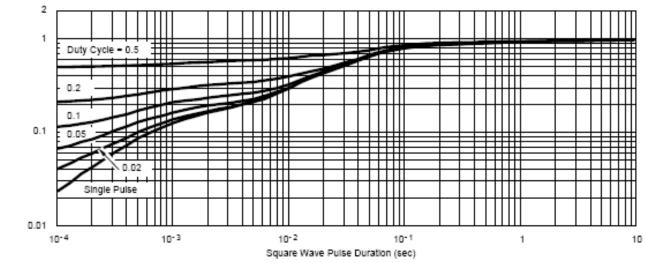


TYPICAL CHARACTERISTICS





Normalized Thermal Transient Impedance, Junction-to-Foot



Normalized Effective Transient Thermal Impedance



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