



SPN65T25

N-Channel Enhancement Mode MOSFET

DESCRIPTION

The SPN65T25 is the N-Channel enhancement mode power field effect transistor which is produced using super high cell density DMOS trench technology. This high density process is especially tailored to minimize on-state resistance. These devices are particularly suitable for synchronous rectifier application, Motor control power management and other Power Tool circuits. It has been optimized for low gate charge, low $R_{DS(ON)}$ and fast switching speed.

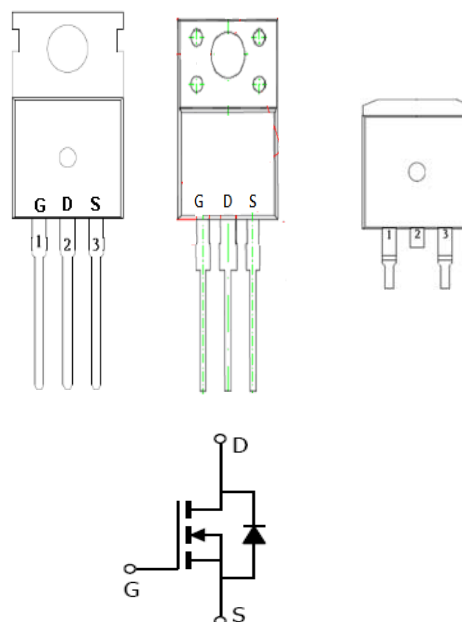
FEATURES

- ◆ 250V/90A, $R_{DS(ON)}=22m\Omega@V_{GS}=10V$
- ◆ High density cell design for extremely low $R_{DS(ON)}$
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ TO-220-3L/TO-220F-3L/TO-263-2L package design

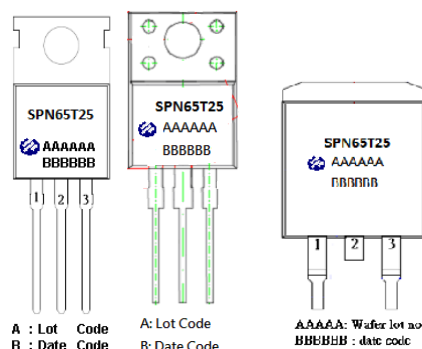
APPLICATIONS

- AC/DC Synchronous Rectifier
- Load Switch
- UPS
- Power Tool
- Motor Control

PIN CONFIGURATION



PART MARKING





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PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	D	Drain
3	S	Source

ORDERING INFORMATION

Part Number	Package	Part Marking
SPN65T25T220TGB	TO-220-3L	SPN65T25
SPN65T25T220FTGB	TO-220F-3L	SPN65T25
SPN65T25T262RGB	TO-263-2L	SPN65T25

- ※ SPN65T25T220TGB : Tube ; Pb – Free ; Halogen – Free
- ※ SPN65T25T220FTGB : Tube ; Pb – Free ; Halogen – Free
- ※ SPN65T25T262RGB : Tape Reel ; Pb – Free ; Halogen – Free

ABSOLUTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V _{DSS}	250	V
Gate –Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (Silicon Limited)	I _D	90	A
		64	
Pulsed Drain Current	I _{DM}	281	A
Avalanche Energy, Single Pulse @ L=0.4mH, Tc=25°C	E _{AS}	180	mJ
Power Dissipation @ Tc=25°C	P _D	358	W
Operating Junction Temperature	T _J	-55/150	°C
Storage Temperature Range	T _{STG}	-55/150	°C
Thermal Resistance-Junction to Case	R _{θJC}	0.35	°C/W



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ELECTRICAL CHARACTERISTICS

(T_A=25°C Unless otherwise noted)

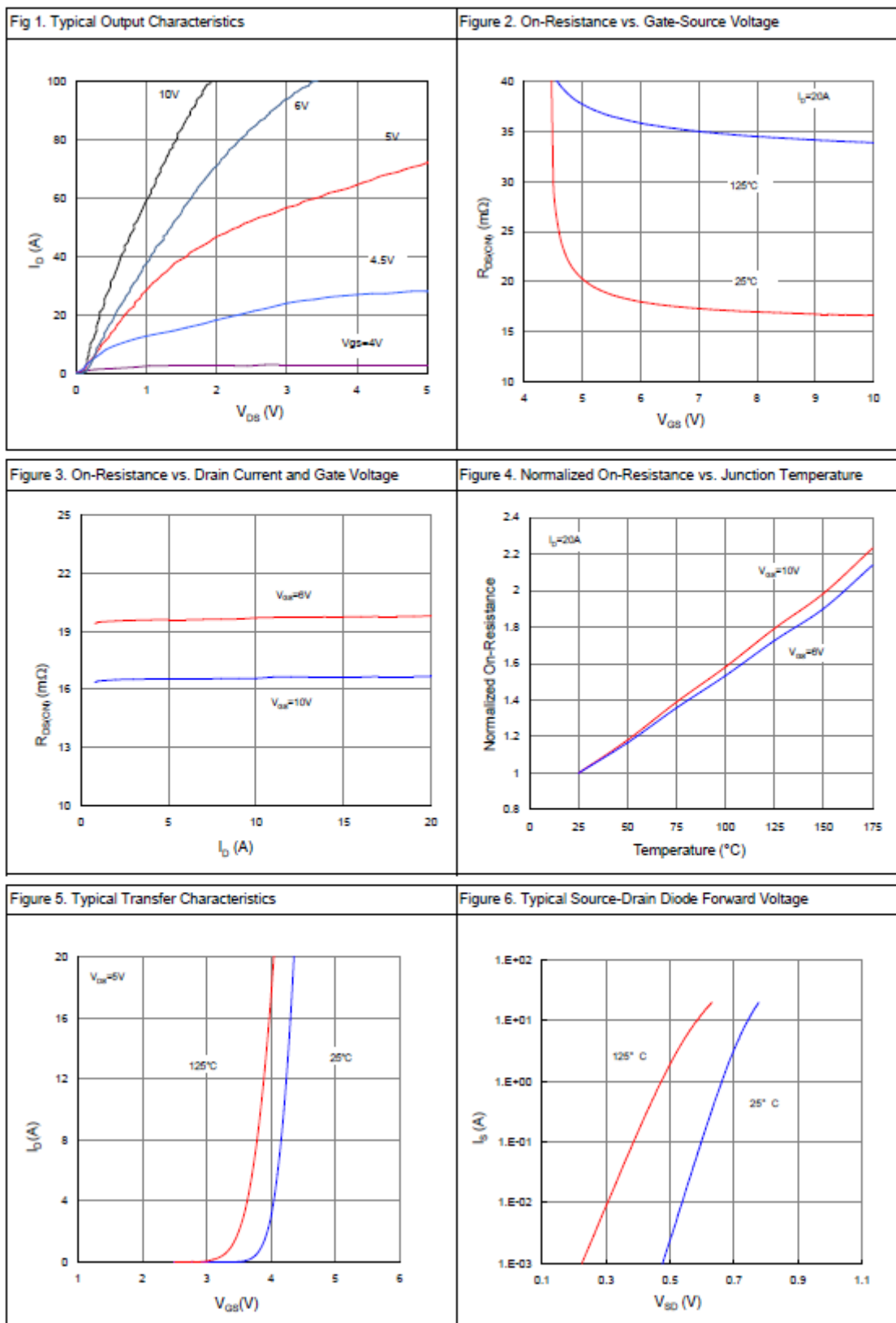
Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =250μA	250			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250μA	2.0	3.0	4.0	
Gate Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =200V, V _{GS} =0V T _J =25°C			1	μA
		V _{DS} =200V, V _{GS} =0V T _J =100°C			100	
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =10V, I _D =20A		16.5	22	mΩ
Forward Transconductance	g _{fs}	V _{DS} =5V, I _D =20A		70		S
Gate Resistance	R _G	V _{GS} =0V, V _{DS} =Open, f=1MHz		3.7		Ω
Diode Forward Voltage	V _{SD}	I _F =20A, V _{GS} =0V		0.9	1.2	V
Dynamic						
Total Gate Charge	Q _g	V _{DS} =125V, V _{GS} =10V I _D =20A		58		nC
Gate-Source Charge	Q _{gs}			18		
Gate-Drain Charge	Q _{gd}			6		
Input Capacitance	C _{iss}	V _{DS} =100V, V _{GS} =0V f=1MHz		4970		pF
Output Capacitance	C _{oss}			350		
Reverse Transfer Capacitance	C _{rss}			7.5		
Turn-On Time	t _{d(on)}	V _{DD} =125V, V _{GS} =10V I _D =20A, R _G =10Ω		16		nS
	t _r			22		
Turn-Off Time	t _{d(off)}			38		
	t _f			10		
Reverse Recovery Time	t _{rr}	V _R =125V, I _F =20A, dI _F /dt=100A/μS		165		nS
Reverse Recovery Charge	Q _{rr}			830		nC



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TYPICAL CHARACTERISTICS





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TYPICAL CHARACTERISTICS

Figure 7. Typical Gate-Charge vs. Gate-to-Source Voltage

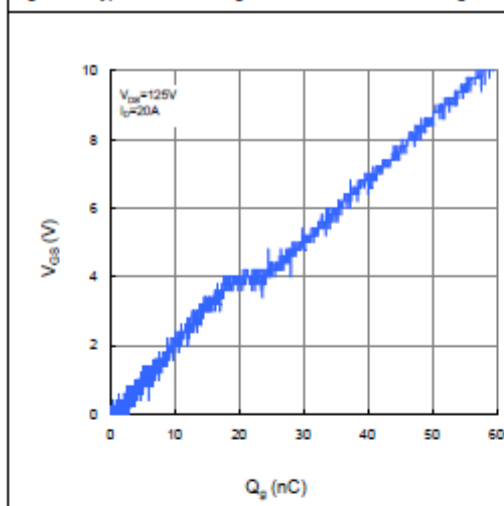


Figure 8. Typical Capacitance vs. Drain-to-Source Voltage

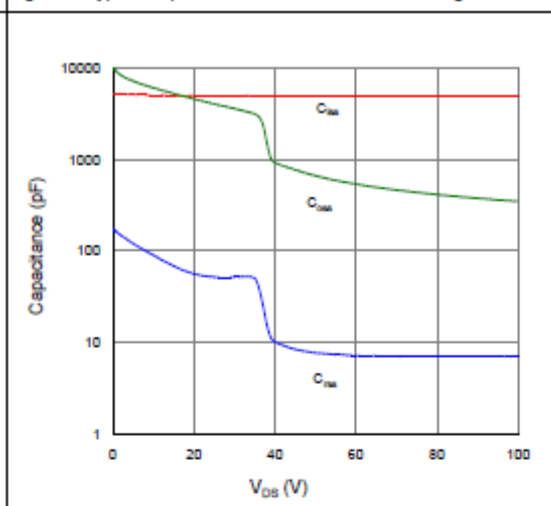


Figure 9. Maximum Safe Operating Area

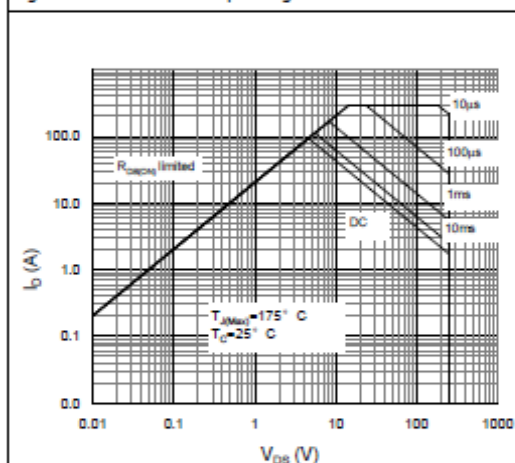


Figure 10. Maximum Drain Current vs. Case Temperature

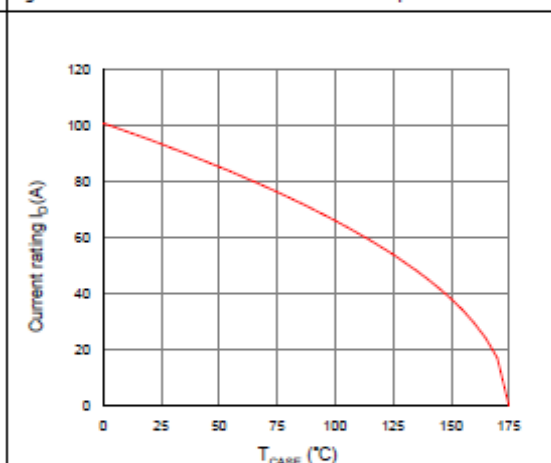
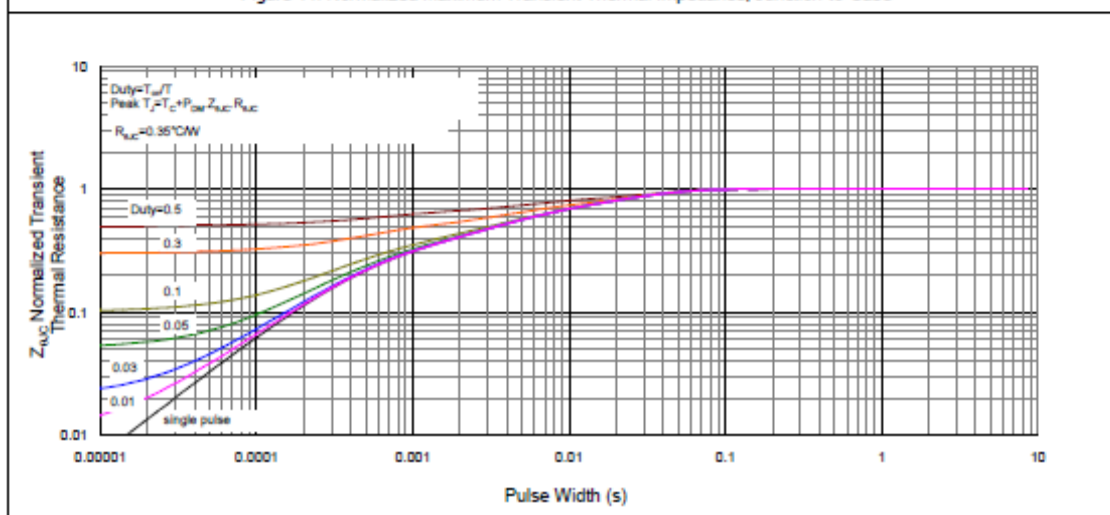


Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Case

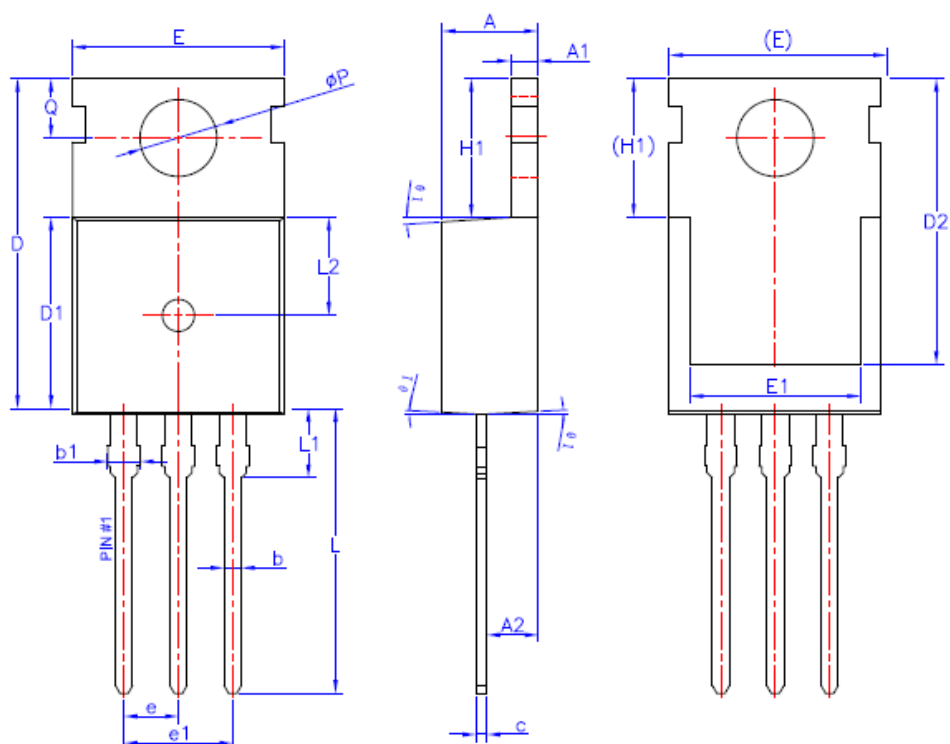




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TO-220-3L PACKAGE OUTLINE



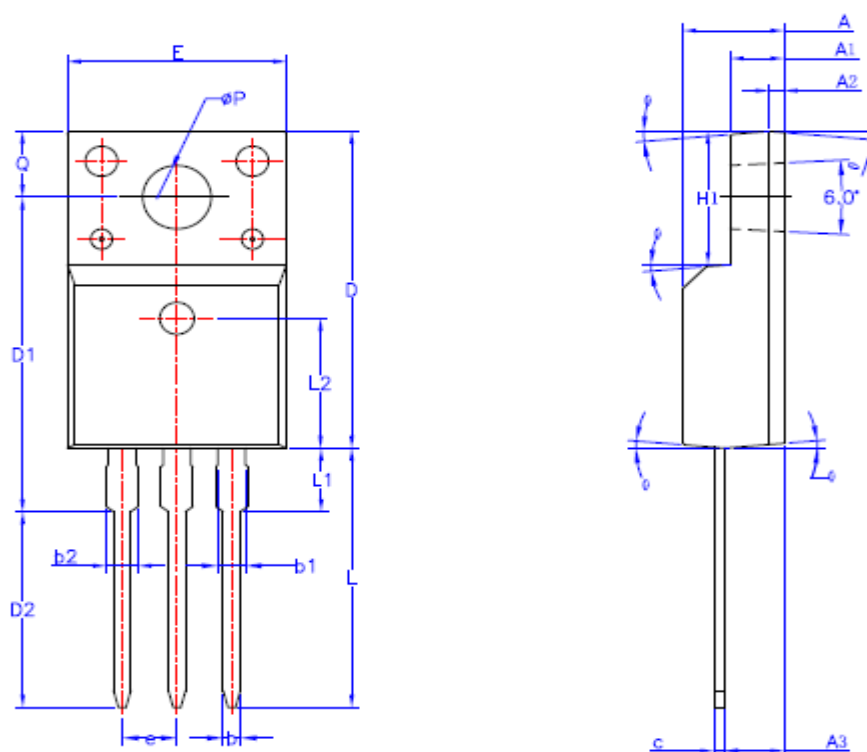
SYMBOL	MIN	NOM	MAX
A	4.40	4.50	4.60
A1	1.27	1.30	1.33
A2	2.30	2.40	2.50
b	0.70	0.60	0.90
b1	-	-	1.40
c	0.45	0.50	0.60
D	15.30	15.70	16.10
D1	9.10	9.20	9.30
D2	13.10	-	13.70
E	9.70	9.90	10.20
E1	7.80	8.00	8.20
e	2.54BSC		
e1	5.08BSC		
H1	6.30	6.50	6.70
L	12.78	13.08	13.38
L1	-	-	3.50
L2	4.6REF		
ϕP	3.55	3.60	3.65
Q	2.73	-	2.87
$\theta 1$	1°	3°	5°



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TO-220F-3L PACKAGE OUTLINE



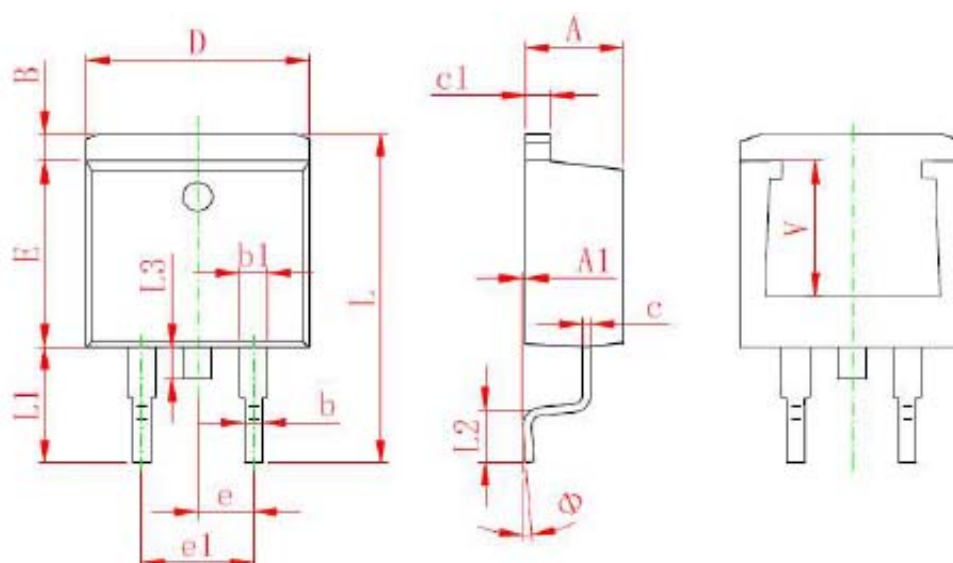
SYMBOL	MIN	NOM	MAX
A	4.50	4.70	4.83
A1	2.34	2.54	2.74
A2	0.7REF		
A3	2.56	2.76	2.93
b	0.70	--	0.90
b1	1.18	--	1.40
b2	--	--	1.47
c	0.45	0.50	0.60
D	15.67	15.87	16.07
D1	15.55	15.75	15.95
D2	9.60	9.80	10.00
E	9.96	10.16	10.36
e	2.54BSC		
H1	6.48	6.68	6.88
L	12.68	12.98	13.28
L1	-	-	3.50
L2	6.50REF		
φP	3.08	3.18	3.28
Q	3.20	-	3.40
$\theta 1$	1°	3°	5°



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TO-263-2L PACKAGE OUTLINE



Unit : mm

SYMBOL	MIN	NOM	MAX
A	4.30	4.50	4.70
A1	0.00	0.10	0.25
B	1.12	-	1.42
b	0.70	-	0.91
b1	1.17	--	1.37
c	0.31	--	0.55
c1	1.17	--	1.37
D	10.01	--	10.31
E	8.50	8.70	8.90
e	2.54TYP.		
e1	4.93	--	5.23
L	14.90	--	15.50
L1	4.95	5.20	5.45
L2	2.34	2.54	2.74
L3	1.30	1.50	1.70
Ø	0°	--	8°
V	5.400REF		



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