



# SPN7575

## N-Channel Enhancement Mode MOSFET

### DESCRIPTION

The SPN7575 is the N-Channel enhancement mode power field effect transistors are produced using high cell density, DMOS trench technology.

This high density process is especially tailored to minimize on-state resistance.

This device is particularly suited for E Bike application.

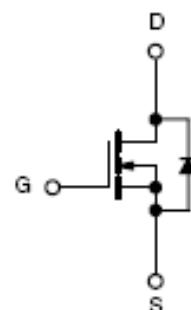
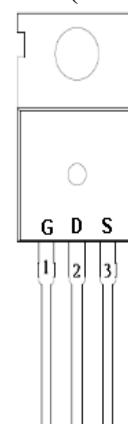
### APPLICATIONS

- DC/DC Converter
- Load Switch
- Power Tool

### FEATURES

- ◆ 75V/80A, R<sub>DS(ON)</sub>=12mΩ@V<sub>GS</sub>=10V
- ◆ Super high density cell design for extremely low R<sub>DS (ON)</sub>
- ◆ Exceptional on-resistance and maximum DC current capability
- ◆ TO-220-3L package design

### PIN CONFIGURATION (TO-220-3L)



### PART MARKING





# SPN7575

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### PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	D	Drain
3	S	Source

### ORDERING INFORMATION

Part Number	Package	Part Marking
SPN7575T220TGB	TO-220-3L	SPN7575

※ SPN7575T220TGB : Tube ; Pb – Free ; Halogen - Free

### ABSOULTE MAXIMUM RATINGS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Typical	Unit
Drain-Source Voltage	V <sub>DSS</sub>	75	V
Gate –Source Voltage	V <sub>GSS</sub>	±20	V
Continuous Drain Current(T <sub>J</sub> =150°C)	TA=25°C	90	A
	TA=70°C		
Pulsed Drain Current	I <sub>DM</sub>	370	A
Avalanche Current	I <sub>AS</sub>	52	A
Power Dissipation	TA=25°C	200	W
	TA=70°C		
Avalanche Energy with Single Pulse ( T <sub>j</sub> =25°C , L = 500uH , I <sub>AS</sub> = 20A , V <sub>DD</sub> = 60V. )	E <sub>AS</sub>	165	mJ
Operating Junction Temperature	T <sub>J</sub>	-55/150	°C
Storage Temperature Range	T <sub>STG</sub>	-55/150	°C
Thermal Resistance-Junction to Ambient	R <sub>θJC</sub>	0.75	°C/W



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### ELECTRICAL CHARACTERISTICS

(TA=25°C Unless otherwise noted)

Parameter	Symbol	Conditions	Min.	Typ	Max.	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	75			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	2.0		4.0	
Gate Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V			1	uA
		V <sub>DS</sub> =60V, V <sub>GS</sub> =0V T <sub>J</sub> = 55 °C			5	
On-State Drain Current	I <sub>D(on)</sub>	V <sub>DS</sub> ≥5V, V <sub>GS</sub> =10V	70			A
Drain-Source On-Resistance	R <sub>D(S)on</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =40A			12	mΩ
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =20A		52		S
Single Pulse Avalanche Energy	E <sub>AS</sub>	V <sub>DS</sub> =60V, L=500uH, I <sub>AS</sub> =20A	58			mJ
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =30A, V <sub>GS</sub> =0V			1.2	V
<b>Dynamic</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =10V I <sub>D</sub> =15A		105		nC
Gate-Source Charge	Q <sub>gs</sub>			20		
Gate-Drain Charge	Q <sub>gd</sub>			17		
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V f=1MHz		7760		pF
Output Capacitance	C <sub>oss</sub>			320		
Reverse Transfer Capacitance	C <sub>rss</sub>			210		
Turn-On Time	t <sub>d(on)</sub>	V <sub>DD</sub> =15V, I <sub>D</sub> =1A, V <sub>GEN</sub> =10V, R <sub>G</sub> =3.3Ω		19.5		nS
	t <sub>r</sub>			11.5		
Turn-Off Time	t <sub>d(off)</sub>			118.5		
	t <sub>f</sub>			11		



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## TYPICAL CHARACTERISTICS

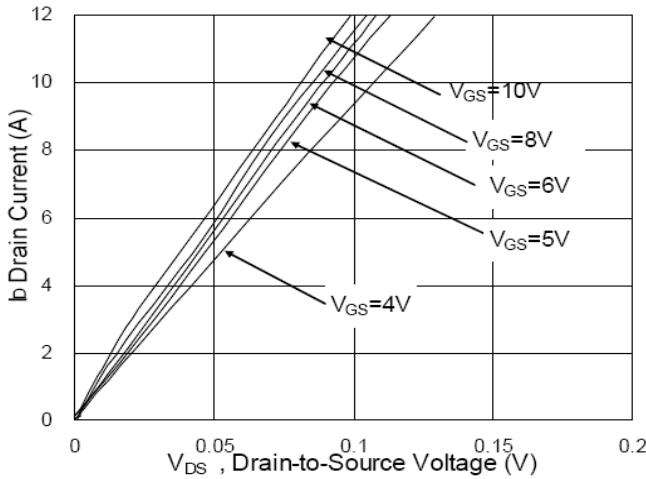


Fig. 1 Typical Output Characteristics

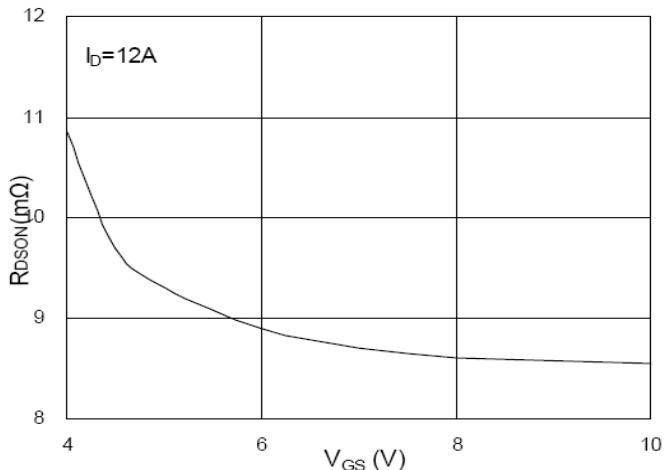


Fig. 2 On-Resistance vs. Gate Voltage

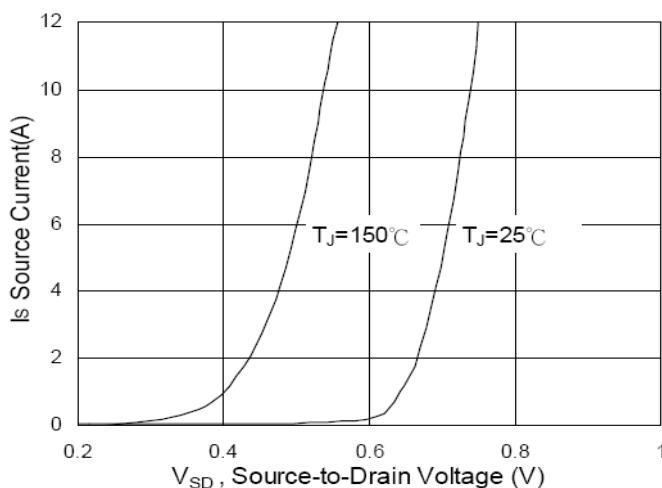


Fig. 3 Forward Characteristics of Reverse Diode

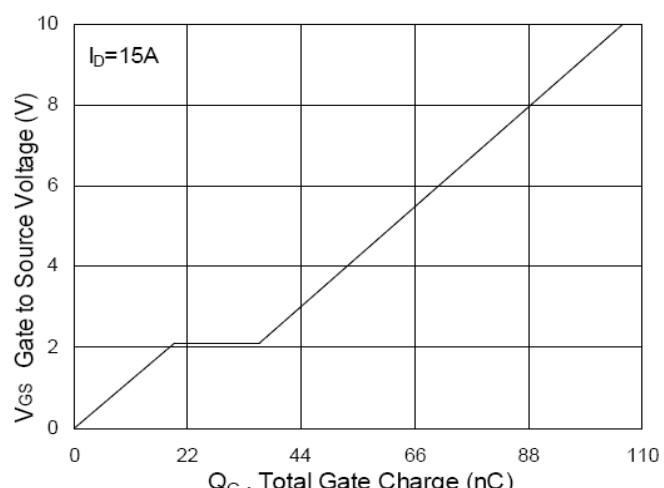


Fig. 4 Gate Charge Characteristics

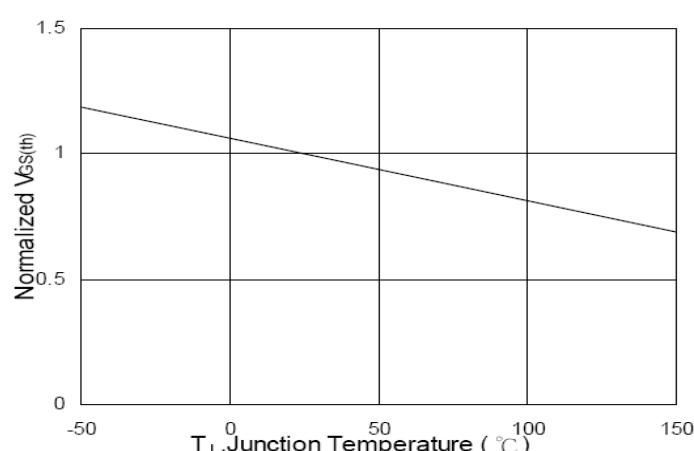


Fig. 5  $V_{GS}$  vs. Junction Temperature

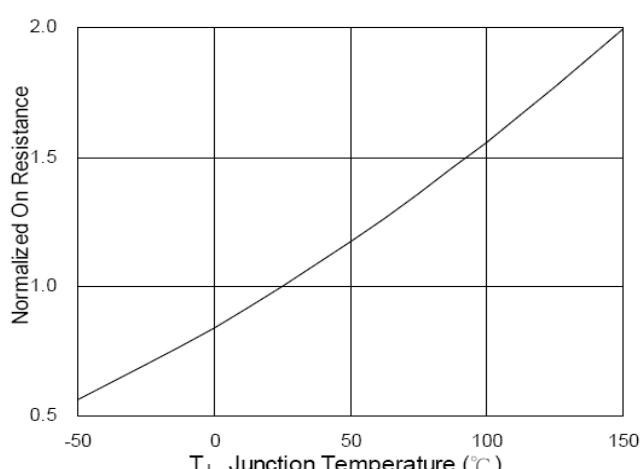


Fig. 6 On Resistance vs. Junction Temperature



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### TYPICAL CHARACTERISTICS

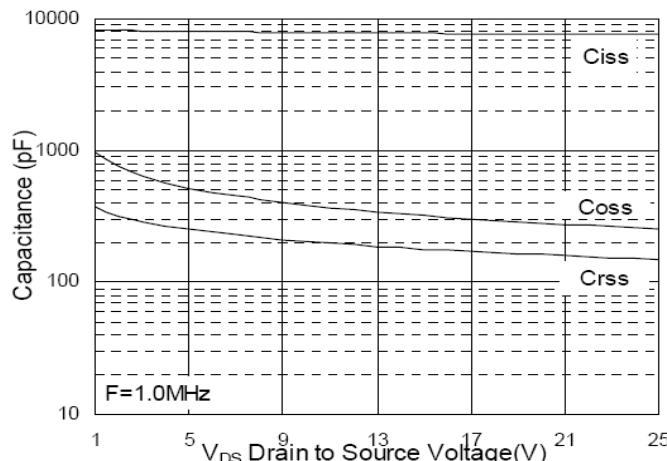


Fig. 7 Typical Capacitance Characteristics

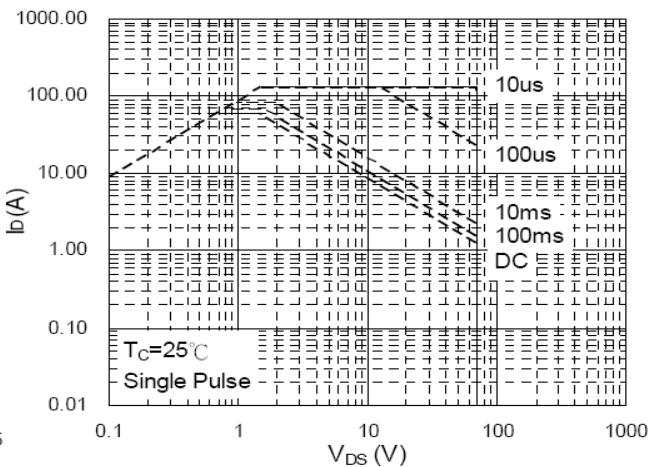


Fig. 8 Maximum Safe Operation Area

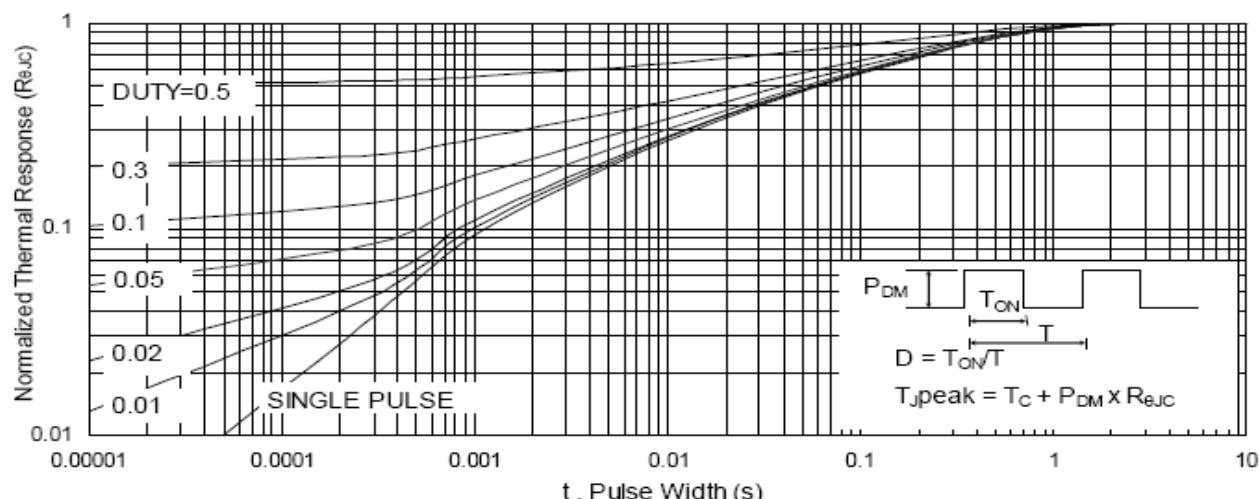


Fig. 9 Effective Transient Thermal Impedance

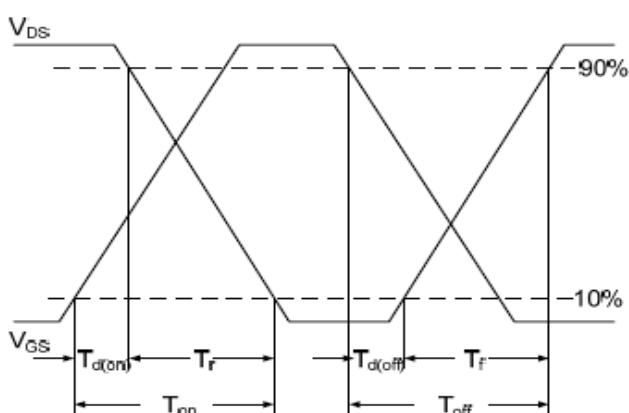


Fig. 10 Switching Time Waveform

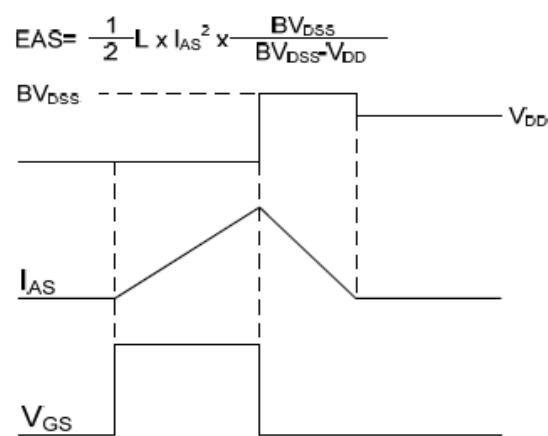


Fig. 11 Unclamped Inductive Waveform



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