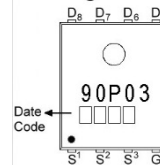


P-Channel Enhancement Mode Power MOSFET
PRIMARY CHARACTERISTICS

BV_{DS}	-30V
$R_{DS(ON)}$	4.5m Ω
I_D	-90A
$T_{J,Max}$	150°C

FEATURES

- Advanced DMOS Trench technology
- Suit for -4.5V Gate Drive Application
- Green Device Available
- Fast switching
- 100% EAS Guaranteed

PR-PAK
PACKAGE
Marking :

Date Code

*ABEDWVZ3H0 4=2014, 5=2015

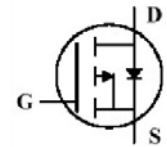
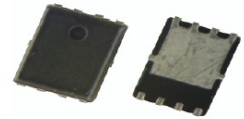
The second code : Month=A,B,C,D.....

 A B C D E F G H I J K L M
1 2 3 4 5 6 7 8 X 9 10 11 12

The third code : Production Type No. 01-99

The fourth code : Production Type No. 01-99

Ex : 4A01=2014101 Produce


MECHANICAL DATA

- Case : Molded plastic,PR-PAK
- Polarity : Shown above
- Terminals :Plated terminals, solderable per MIL-STD-750,Method 2026
- Epoxy : UL94-V0 rated flame retardant

DESCRIPTION

- The SPR90P03 is using trench DMOS technology. This advanced technology has been especially tailored to minimize RDS(ON), provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.
- The SPR90P03 meet the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹	$I_D @ T_C=25^\circ C$	-90	A
Continuous Drain Current ¹	$I_D @ T_C=100^\circ C$	-56	A
Pulsed Drain Current ^{1,2}	I_{DM}	-360	A
Total Power Dissipation ⁴	$P_D @ T_C=25^\circ C$	136	W
	$P_D @ T_A=25^\circ C$	2	W
Single Pulse Avalanche Energy, L=0.1mH ³	E_{AS}	296	mJ
Single Pulse Avalanche Current, L=0.1mH ³	I_{AS}	-77	A
Operating Junction and Storage Temperature Range	T_j, T_{stg}	-55 ~ +150	°C

Thermal Data

Parameter	Symbol	Conditions	Max. Value	Unit
Thermal Resistance Junction-ambient ¹	$R_{\theta JA}$	Steady State	62.5	°C/W
Thermal Resistance Junction-case ¹	$R_{\theta JC}$	Steady State	0.92	°C/W



P-Channel Enhancement Mode Power MOSFET

Electrical Characteristics (T_j = 25°C unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV _{DSS}	-30	-	-	V	V _{GS} =0, I _D =-250uA
Gate Threshold Voltage	V _{GS(th)}	-1.2	-1.6	-2.2	V	V _{DS} =V _{GS} , I _D =-250uA
Forward Transconductance	g _{fs}	-	25	-	S	V _{DS} =-10V, I _D =-5A
Gate-Source Leakage Current	I _{GSS}	-	-	±100	nA	V _{GS} = ±20V
Drain-Source Leakage Current(T _j =25°C)	I _{DSS}	-	-	-1	uA	V _{DS} =-30V, V _{GS} =0
Drain-Source Leakage Current(T _j =125°C)		-	-	-10	uA	V _{DS} =-24V, V _{GS} =0
Static Drain-Source On-Resistance ²	R _{DS(ON)}	-	3.6	4.5	mΩ	V _{GS} =-10V, I _D =-30A
		-	5.2	7.0		V _{GS} =-4.5V, I _D =-20A
Total Gate Charge ²	Q _g	-	108	-	nC	I _D =-10A V _{DS} =-15V V _{GS} =-10V
Gate-Source Charge	Q _{gs}	-	15	-		
Gate-Drain ("Miller") Charge	Q _{gd}	-	17.4	-		
Turn-on Delay Time ²	T _{d(on)}	-	28	-	ns	V _{DD} =-15V I _D =-1A V _{GS} =-10V R _G =6Ω
Rise Time	T _r	-	16	-		
Turn-off Delay Time	T _{d(off)}	-	178	-		
Fall Time	T _f	-	72	-		
Input Capacitance	C _{iss}	-	6220	-	pF	V _{GS} =0V V _{DS} =-25V f=1.0MHz
Output Capacitance	C _{oss}	-	782	-		
Reverse Transfer Capacitance	C _{rss}	-	412	-		

Guaranteed Avalanche Characteristics

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Single Pulse Avalanche Energy ⁵	EAS	45	-	-	mJ	V _{DD} =-25V, L=0.1mH, I _{AS} =-30A

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage ²	V _{SD}	-	-	-1.2	V	V _{GS} =0V, I _S =-30A, T _j =25°C
Continuous Source Current ^{1,6}	I _S	-	-	-85	A	V _G =V _D =0V, Force Current
Pulsed Source Current ^{2,6}	I _{SM}	-	-	-170	A	

Notes: 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

2. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.

3. The EAS data shows Max. rating. The test condition is V_{DD}=-25V, V_{GS}=-10V, L=0.1mH, I_{AS}=- 77A.

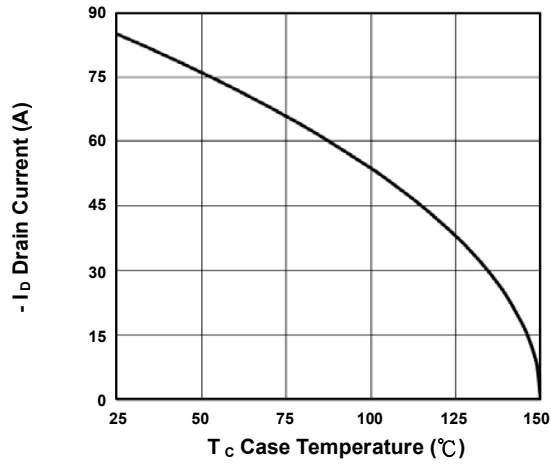
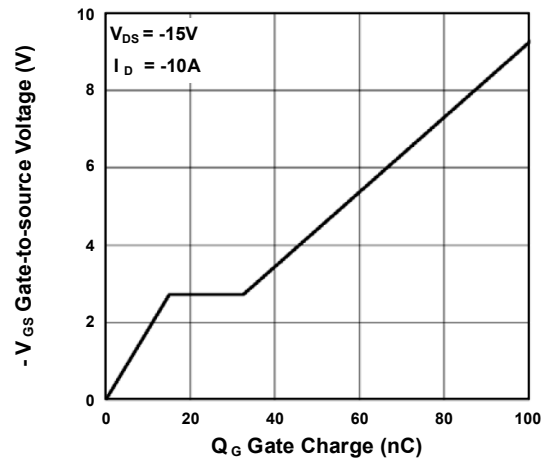
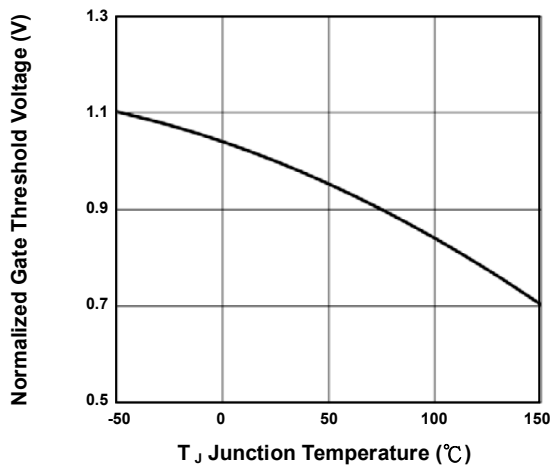
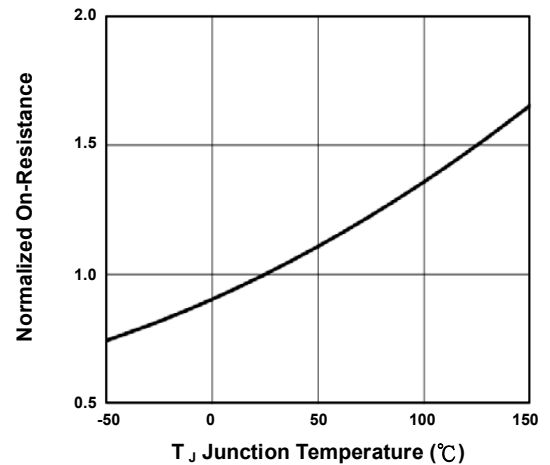
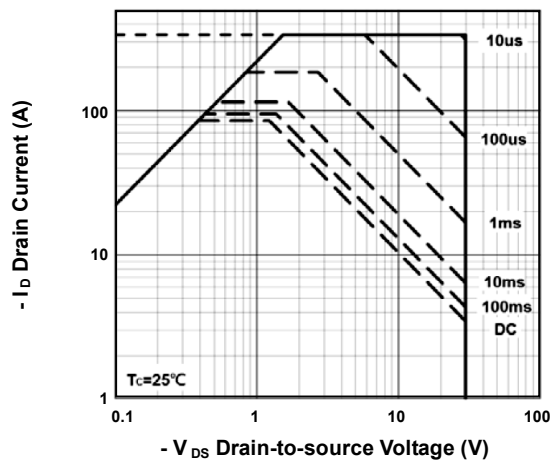
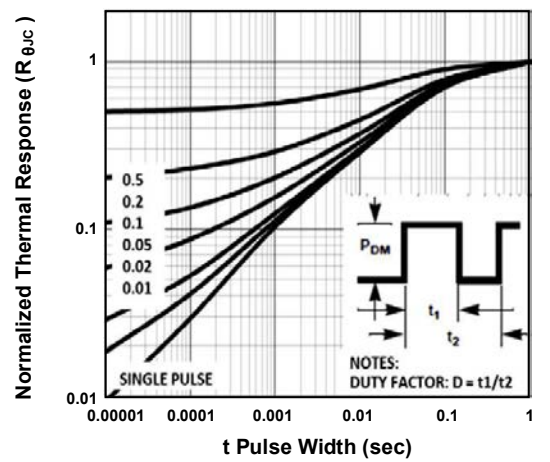
4. The power dissipation is limited by 150°C junction temperature.

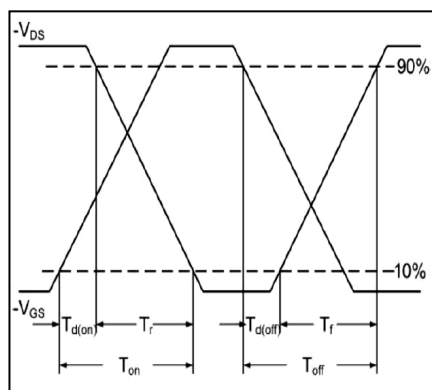
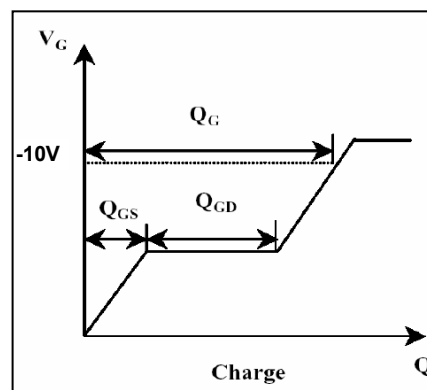
5. The Min. value is 100% EAS tested guarantee.

6. The data is theoretically the same as I_D and I_{DM}, in real applications, should be limited by total power dissipation.



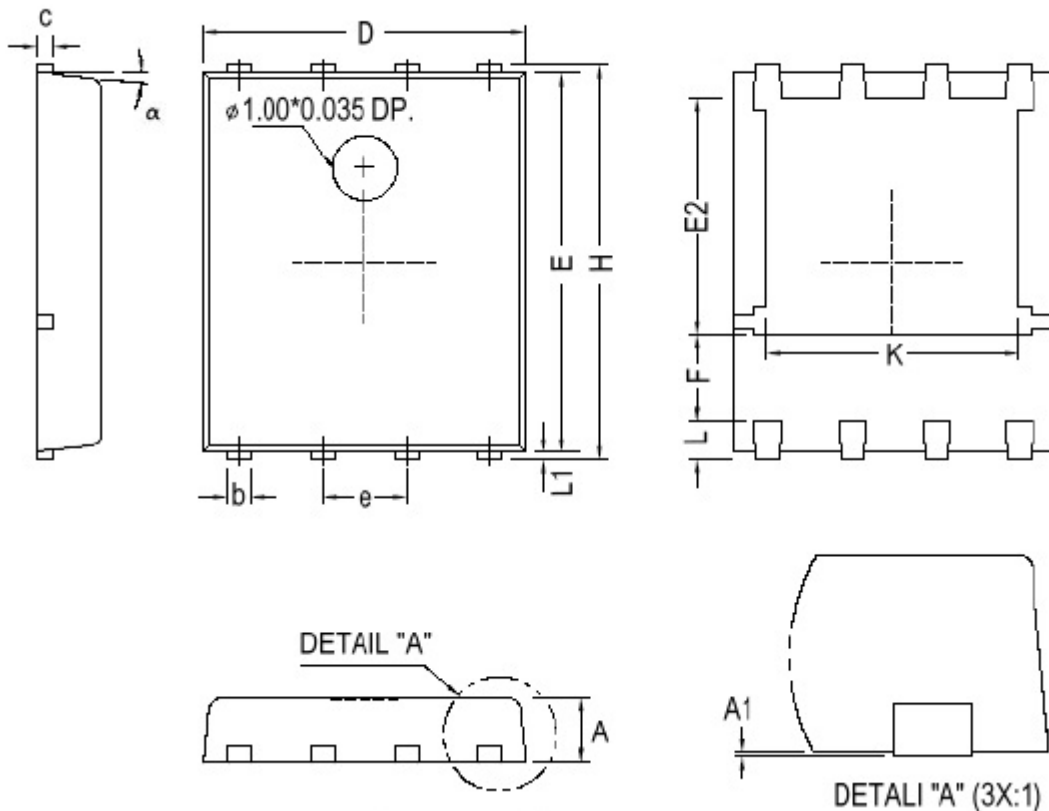
Typical Characteristics

**Fig.1 Drain Current vs. T_C** **Fig.2 Gate Charge Characteristics****Fig.3 Normalized $V_{GS(th)}$ vs. T_J** **Fig.4 Normalized $R_{DS(on)}$ vs. T_J** **Fig.5 Safe Operating Area****Fig.6 Transient Thermal Impedance**


Fig.7 Switching Time Waveform

Fig.8 Gate Charge Waveform

Outline Drawing

PR-PAK



REF.	Millimeter			REF.	Millimeter		
	Min.	Nom.	Max.		Min.	Nom.	Max.
A	0.85	1.00	1.15	E	5.70	-	5.90
A1	0.00	-	0.10	e	-	1.27	-
b	0.30	-	0.51	H	5.90	-	6.20
c	0.20	-	0.30	L	-	0.60	-
D	4.80	-	5.00	L1	0.06	-	0.20
F	1.10REF.			α	0°	-	12°
E2	3.50REF.			K	3.70	3.90	4.10

Rev.C

Ordering Information:

Device PN	Packing
SPR90P03-T ⁽¹⁾ H ⁽²⁾ -WS	Tape&Reel: 3 Kpcs/Reel

Note: (1) Packing code, Tape & Reel Packing

(2) Haloge free product for packing code suffix "H"

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