

SOLID STATE DEVICES, INC. 14830 Valley View Blvd * La Mirada, Ca 90638 Phone: (562) 404-7855 * Fax: (562) 404-1773

DESIGNER'S DATA SHEET

FEATURES:

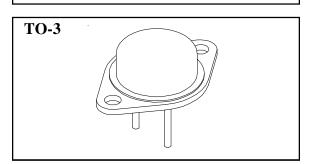
- High Voltage Rating 350V Sustaining
- Fast Switching Capabilities / Fast Turn-off Time
- Thermally Stable Structure for Reliability in Power Cycling
- **Overload Short Circuit Rating**

APPLICATIONS:

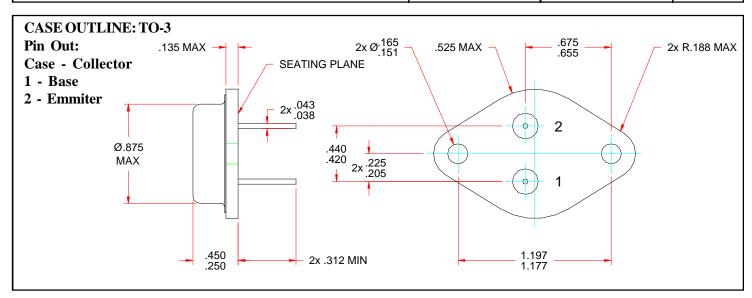
- High Voltage Switching Power Supplies
- Inverter / Regulators
- **Deflection Circuits**
- Pulse-Width-Modulated (PWM) System Control Circuits

SPT6060/3

20 AMP 350 VOLTS NPN DARLINGTON **TRANSISTOR**



MAXIMUM RATINGS		SYMBOL	VALUE	UNITS
Collector-Emitter Voltage		V _{CEO}	350	Volts
Collector-Base Voltage		V _{CBO}	350	Volts
Collector Current	Continuous Peak	I _C I _{CM}	20 40	Amps
Base Current	Continuous Peak	I _B I _{BM}	4 6	Amps
Total Device Dissipation Derate above 25°C	@ $T_C = 25^{\circ}C$	P_{D}	125 1	W W/°C
Operating and Storage Temperature		T_{J}, T_{STG}	-65 to +150	°C
Thermal Resistance, Junction to Case		$\mathbf{R}_{\mathbf{qJC}}$	1	°C/W



NOTE: All specifications are subject to change without notification. SCD's for these devices should be reviewed by SSDI prior to release.

DATA SHEET #: TR0017B

SPT6060/3



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ELECTRICAL CHARACTERISTICS		SYMBOL	MIN	MAX	UNITS	
		V _{CEO(sus)}	300	-	V _{DC}	
Collector Cutoff Current $(V_{CE} = Rated\ Value,\ T_C = 25^{\circ}C)$		I _{CEO}	-	1.0	mA _{DC}	
		I _{EBO}	-	300	mA _{DC}	
Overload Short Circuit Rating (V _{CEO(sus)} = 300V, I _C = 30A, I _B = 300mA, Time = 10:s)			90	-	mJ	
Forward Bias Safe Op	erating Area	$V_{CE} = 6.25 \text{ V}, t = 1 \text{sec}$ $V_{CE} = 12.5 \text{ V}, t = 1 \text{sec}$ $V_{CE} = 300 \text{ V}, t = 1 \text{sec}$	FBSOA	20 10 0.125	- - -	A
DC Current Gain* (V _{CE} = 5V _{DC})		$I_{C} = 10A_{DC}$ $I_{C} = 15A_{DC}$ $I_{C} = 20A_{DC}$	${ m H_{FE}}$	30 20 15	120 80 60	
$\label{eq:collector-Emitter Saturation Voltage*} I_{C} = 10A_{DC}, \ I_{B} = 2A_{DC} \\ I_{C} = 10A_{DC}, \ I_{B} = 1A_{DC} \\ I_{C} = 20A_{DC}, \ I_{B} = 2A_{DC} \\$		V _{CE(SAT)}		1.4 1.5 2.0	V _{DC}	
Base-Emitter Saturation	on Voltage*	$I_{C} = 10A_{DC}, I_{B} = 2A_{DC}$ $I_{C} = 20A_{DC}, I_{B} = 2A_{DC}$	V _{BE} (SAT)	-	2.5 3.0	V _{DC}
Small Signal Current Gain $(I_C = 1A_{DC}, V_{CE} = 10V_{DC}, f = 1MHz)$		h _{FE}	10			
Rise Time	$V_{CC} = 150 V_{DC}$, $I_{C} = 10 A_{DC}$, $I_{B1} = I_{B2} = 1.0 A_{DC}$, $t_{P} = 4 \mu s min$.		$t_{\rm r}$	-	0.4	ms
Storage Time			t_s	-	2.5	ms
Fall Time			t _f	-	1.0	ms

*Pulse Test: Pulse Width = 300ms, Duty Cycle = 2%