

**isc N-Channel MOSFET Transistor**
**SPW21N50C3**
**ISPW21N50C3**
**• FEATURES**

- Static drain-source on-resistance:  
 $R_{ds(on)} \leq 190m\Omega$
- Enhancement mode:
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

**• DESCRIPTION**

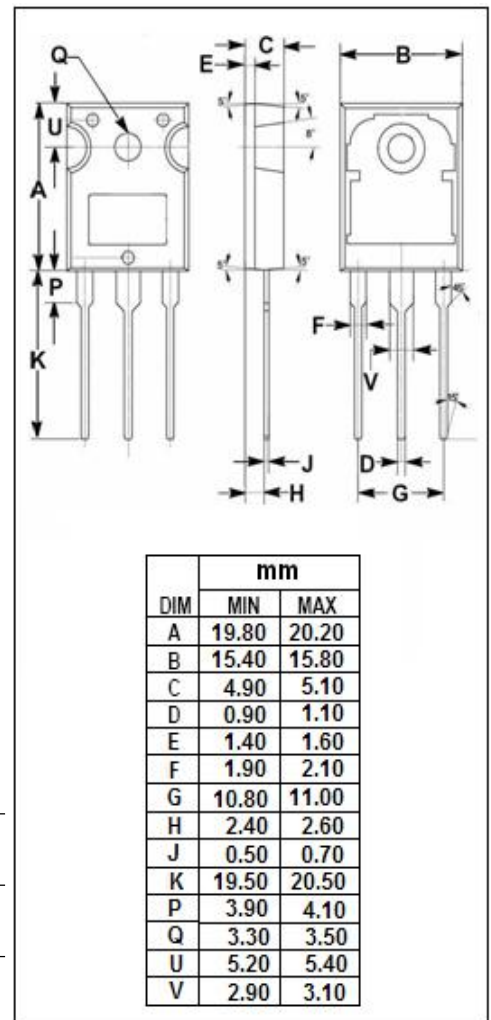
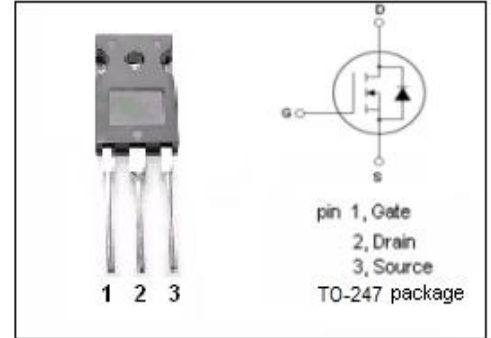
- Improved Transconductance

**• ABSOLUTE MAXIMUM RATINGS( $T_a=25^\circ\text{C}$ )**

SYMBOL	PARAMETER	VALUE	UNIT
$V_{DSS}$	Drain-Source Voltage	500	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current-Continuous	21	A
$I_{DM}$	Drain Current-Single Pulsed	63	A
$P_D$	Total Dissipation @ $T_c=25^\circ\text{C}$	208	W
$T_j$	Max. Operating Junction Temperature	150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature	-55~150	$^\circ\text{C}$

**• THERMAL CHARACTERISTICS**

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(j-c)}$	Channel-to-case thermal resistance	0.6	$^\circ\text{C/W}$
$R_{th(j-a)}$	Channel-to-ambient thermal resistance	62	$^\circ\text{C/W}$



## isc N-Channel MOSFET Transistor

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## ELECTRICAL CHARACTERISTICS

T<sub>c</sub>=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
B <sub>V</sub> DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V; I <sub>D</sub> =0.25mA	500			V
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> ; I <sub>D</sub> =1000 μ A	2.1		3.9	V
R <sub>DS(on)</sub>	Drain-Source On-Resistance	V <sub>GS</sub> =10V; I <sub>D</sub> =13.1A			190	mΩ
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> = 20V; V <sub>DS</sub> = 0V			0.1	μ A
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =500V; V <sub>GS</sub> = 0V			1	μ A
V <sub>SD</sub>	Diode forward voltage	I <sub>F</sub> =I <sub>S</sub> , V <sub>GS</sub> = 0V			1.2	V

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