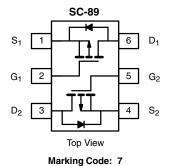


Vishay Siliconix

Automotive Dual P-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	- 60			
$R_{DS(on)}(\Omega)$ at $V_{GS} = -10 \text{ V}$	4			
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 \text{ V}$	8			
I _D (A)	- 0.19			
Configuration	Dual			



FEATURES

• TrenchFET® Power MOSFET

• Typical ESD Protection: 2000 V

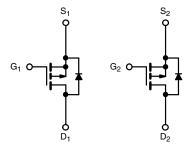
• AEC-Q101 Qualified^c

• 100 % R_g Tested

Material categorization:
 For definitions of compliance please see www.vishay.com/doc?99912



ROHS COMPLIANT HALOGEN FREE



P-Channel MOSFET P-Channel MOSFET

ORDERING INFORMATION				
Package	SC-89			
Lead (Pb)-free and Halogen-free	SQ1025X-T1-GE3			

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V_{DS}	- 60	.,	
Gate-Source Voltage		V _{GS} ± 20		V	
Continuous Drain Current	T _A = 25 °C	1	- 0.19		
Continuous Drain Current	T _A = 125 °C	- I _D	- 0.11		
Continuous Source Current (Diode Conduction)		I _S	- 0.38	Α	
Pulsed Drain Current ^a		I _{DM}	- 0.75		
Maximum Power Dissipation ^a	T _A = 25 °C	D	0.300	W	
	T _A = 125 °C	P_{D}	0.100		
Operating Junction and Storage Temperature F	Range	T _J , T _{stg}	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient	PCB Mountb	R _{thJA}	500	°C/W	

Notes

- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. When mounted on 1" square PCB (FR-4 material).
- c. Parametric verification ongoing.



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT	
Static							ı	
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$		- 60	-	-		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_{D} = -250 \mu\text{A}$		- 2	- 3	· V	
Gate-Source Leakage	,	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$		-	-	± 1	μΑ	
Gate-Source Leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 1	mA	
		V _{GS} = 0 V	V _{DS} = - 60 V	-	-	- 1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = - 60 V, T _J = 125 °C	-	-	- 50	μΑ	
		$V_{GS} = 0 V$	V _{DS} = - 60 V, T _J = 175 °C	-	-	- 150	1	
On-State Drain Current ^a	I _{D(on)}	V _{GS} = - 10 V	V _{DS} = ≤ - 5 V	- 0.5	-	-	Α	
		V _{GS} = - 10 V	I _D = - 500 mA	-	2.81	4		
Drain-Source On-State Resistance ^a	Ь	V _{GS} = - 10 V	$I_D = -500 \text{ mA}, T_J = 125 \text{ °C}$	-	-	6.88	Ω	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} = - 10 V	$I_D = -500 \text{ mA}, T_J = 175 \text{ °C}$	-	-	8.52		
		V _{GS} = - 4.5 V	I _D = - 250 mA	-	3.8	8		
Forward Transconductance ^b	9 _{fs}	V _{DS} = -	10 V, I _D = - 100 mA	-	0.100	-	S	
Dynamic ^b								
Input Capacitance	C _{iss}		V _{GS} = 0 V V _{DS} = - 25 V, f = 1 MHz	-	26	33	pF	
Output Capacitance	C _{oss}	$V_{GS} = 0 V$		-	6	8		
Reverse Transfer Capacitance	C _{rss}			-	3	4		
Total Gate Charge ^c	Qg			-	1.05	1.6		
Gate-Source Charge ^c	Q_{gs}	V _{GS} = - 10 V	$V_{DS} = -30 \text{ V}, I_{D} = -500 \text{ mA}$	-	0.22	-	- nC	
Gate-Drain Charge ^c	Q_{gd}			-	0.17	1		
Gate Resistance	R_{g}		f = 1 MHz	100	222	350	Ω	
Turn-On Delay Time ^c	t _{d(on)}			-	6	9		
Rise Time	t _r	$V_{DD} = 30 \text{ V}, \text{ R}_{L} = \text{182 } \Omega$ $I_{D} \cong 165 \text{ mA}, \text{ V}_{GEN} = 10 \text{ V}, \text{ R}_{g} = \text{1 } \Omega$		-	8	12	- ns	
Turn-Off Delay Time ^c	t _{d(off)}			-	17	26		
Fall Time	t _f			-	13	20		
Diode Ratings and Characteristics ^b								
Pulsed Current ^a	I _{SM}			-	-	- 0.75	Α	
Forward Voltage	V_{SD}	I _F = - 200 mA, V _{GS} = 0 V		-	- 0.88	- 1.2	V	

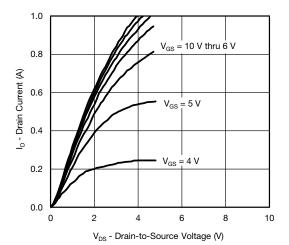
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

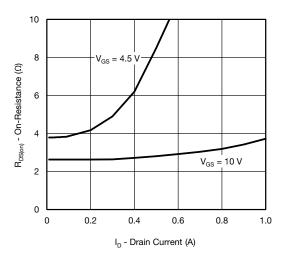
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



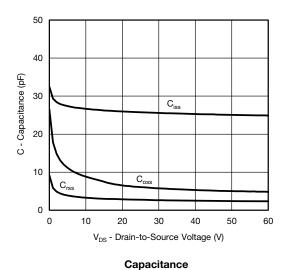
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

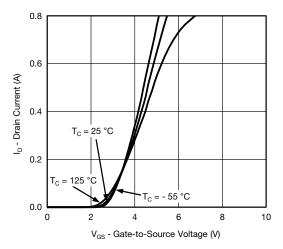


Output Characteristics

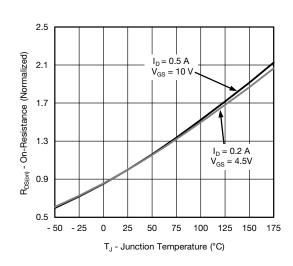


On-Resistance vs. Drain Current

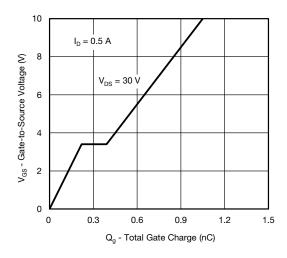




Transfer Characteristics



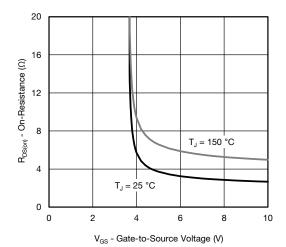
On-Resistance vs. Junction Temperature



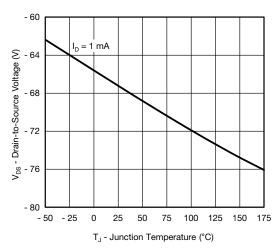
Gate Charge



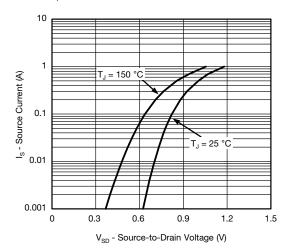
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



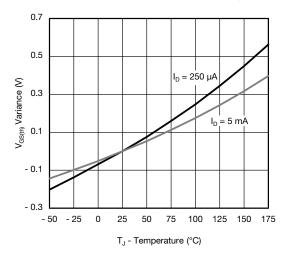
On-Resistance vs. Gate-to-Source Voltage



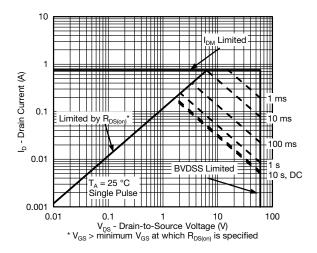
Drain Source Breakdown vs. Junction Temperature



Source-Drain Diode Forward Voltage



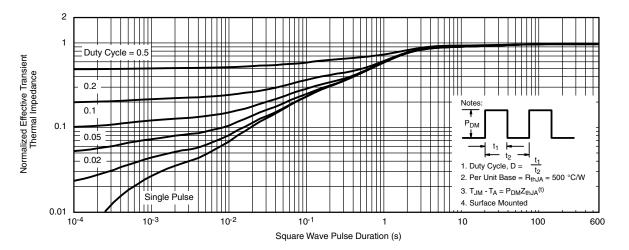
Threshold Voltage



Safe Operating Area



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient

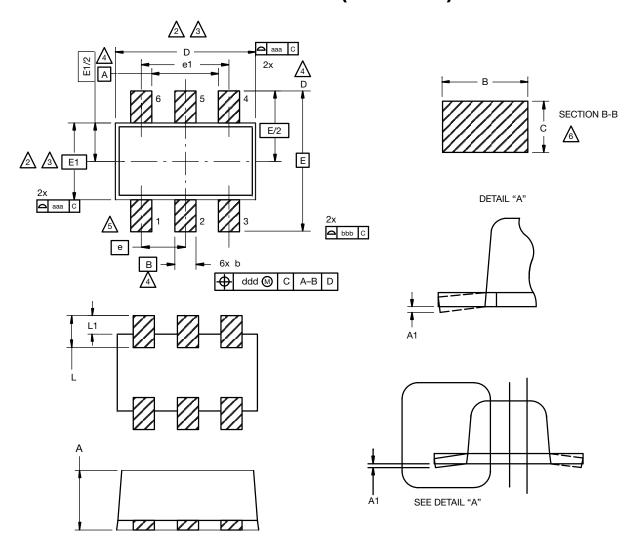
Note

• The characteristics shown in the the graph Normalized Transient Thermal Impedance Junction to Ambient (25 °C) is given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg267060.



SC-89 6-Leads (SOT-563F)



Notes

1. Dimensions in millimeters.

Dimension D does not include mold flash, protrusions or gate burrs. Mold flush, protrusions or gate burrs shall not exceed 0.15 mm per dimension E1 does not include interlead flash or protrusion, interlead flash or protrusion shall not exceed 0.15 mm per side.

Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, the bar burrs, gate burrs and interlead flash, but including any mismatch between the top and the bottom of the plastic body.

ADatums A, B and D to be determined 0.10 mm from the lead tip.

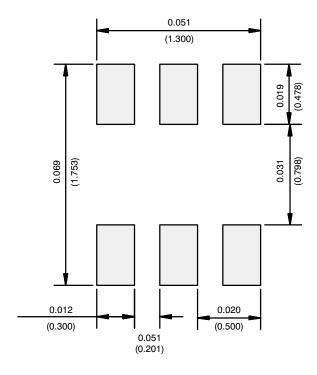
A Terminal numbers are shown for reference only.

These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

DIM.		MILLIMETERS		
	MIN.	NOM.	MAX.	
Α	0.56	0.58	0.60	
A1	0	0.02	0.10	
b	0.15	0.22	0.30	
С	0.10	0.14	0.18	
D	1.50	1.60	1.70	
E	1.50	1.60	1.70	
E1	1.15	1.20	1.25	
е	0.45	0.50	0.55	
e1	0.95	1.00	1.05	
L	0.25	0.35	0.50	
L1	0.10	0.20	0.30	
C14-0439-Rev. C, 11-Aug-14 DWG: 5880				



RECOMMENDED MINIMUM PADS FOR SC-89: 6-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index

APPLICATION NOTE



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