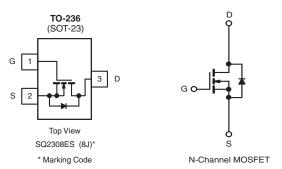


Vishay Siliconix

# Automotive N-Channel 60 V (D-S) 175 °C MOSFET

PRODUCT SUMMARY			
V <sub>DS</sub> (V)	60		
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.155		
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.205		
I <sub>D</sub> (A)	2.3		
Configuration	Single		



#### **FEATURES**

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Compliant to RoHS Directive 2002/95/EC
- AEC-Q101 Qualified<sup>c</sup>
- Find out more about Vishay's Automotive Grade Product Requirements at: www.vishay.com/applications



ROHS COMPLIANT HALOGEN FREE

ORDERING INFORMATION			
Package	SOT-23		
Lead (Pb)-free and Halogen-free	SQ2308ES-T1-GE3		

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		$V_{DS}$	60		
Gate-Source Voltage		$V_{GS}$	± 20	V	
Continuous Drain Current	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	2.3		
	T <sub>C</sub> = 125 °C		1.4		
Continuous Source Current (Diode Conduction)		I <sub>S</sub>	2.5	Α	
Pulsed Drain Current <sup>a</sup>		I <sub>DM</sub>	9		
Single Pulse Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	11		
Single Pulse Avalanche Energy	L = 0.1 min	E <sub>AS</sub>	6	mJ	
Maximum Power Dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C	P <sub>D</sub>	2	W	
	T <sub>C</sub> = 125 °C		0.6	] <b>*</b> V	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to + 175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-Ambient PC	CB Mount <sup>b</sup>	$R_{thJA}$	175	°C/W	
Junction-to-Foot (Drain)		$R_{thJF}$	75		

#### Notes

- a. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- b. When mounted on 1" square PCB (FR-4 material).
- c. Parametric verification ongoing.

# **SQ2308ES**

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PARAMETER	SYMBOL	TES	TEST CONDITIONS		TYP.	MAX.	UNIT
Static				L	L	L	<u> </u>
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		60	-	-	V
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$		2.0	2.5	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA
Zero Gate Voltage Drain Current		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 60 V	-	-	1	μΑ
	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 60 V, T <sub>J</sub> = 125 °C	-	-	50	
		V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 60 V, T <sub>J</sub> = 175 °C	-	-	150	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 10 V	$V_{DS} \ge 5 V$	8	-	-	Α
Drain-Source On-State Resistance <sup>a</sup>		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 6 A	-	0.131	0.155	Ω
		V <sub>GS</sub> = 10 V	I <sub>D</sub> = 6 A, T <sub>J</sub> = 125 °C	-	-	0.279	
	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V	I <sub>D</sub> = 6 A, T <sub>J</sub> = 175 °C	-	-	0.357	
		V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 4 A	-	0.170	0.205	
Forward Transconductance <sup>b</sup>	9 <sub>fs</sub>	V <sub>DS</sub> :	= 15 V, I <sub>D</sub> = 1.9 A	-	4	-	S
Dynamic <sup>b</sup>							
Input Capacitance	C <sub>iss</sub>		V V <sub>DS</sub> = 25 V, f = 1 MHz	-	244	305	pF
Output Capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$		-	46	60	
Reverse Transfer Capacitance	C <sub>rss</sub>	1		-	17	25	
Total Gate Charge <sup>c</sup>	Qg	V <sub>GS</sub> = 10 V	V <sub>GS</sub> = 10 V V <sub>DS</sub> = 30 V, I <sub>D</sub> = 2 A	-	5.5	8.5	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>			-	0.9	-	
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>	1		-	1.2	-	
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	$V_{DD}$ = 30 V, $R_L$ = 15 $\Omega$ $I_D \cong$ 2 A, $V_{GEN}$ = 10 V, $R_g$ = 1 $\Omega$		-	5	8	
Rise Time <sup>c</sup>	t <sub>r</sub>			-	9	14	ns
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			-	12	18	
Fall Time <sup>c</sup>	t <sub>f</sub>			-	9	14	
Source-Drain Diode Ratings and Char	acteristics <sup>b</sup>	•					
Pulsed Current <sup>a</sup>	I <sub>SM</sub>			-	-	9	Α
Forward Voltage	$V_{SD}$	I <sub>F</sub> = 1.5 A, V <sub>GS</sub> = 0 V		-	0.8	1.2	V

#### Notes

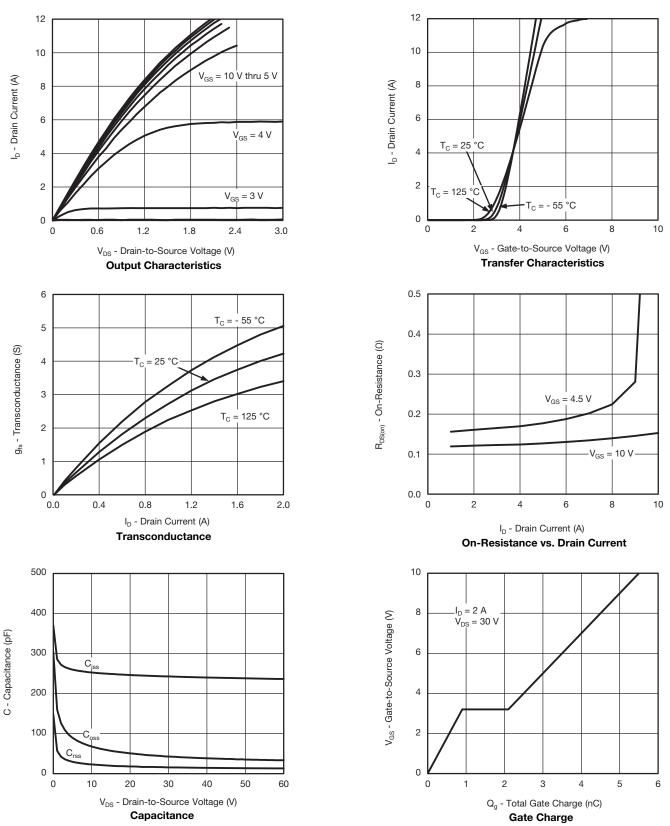
- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





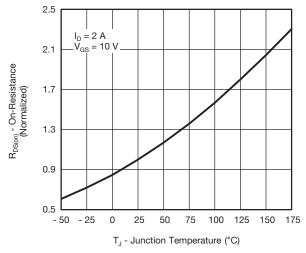
# TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)



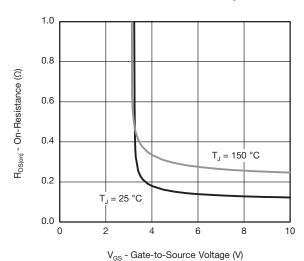
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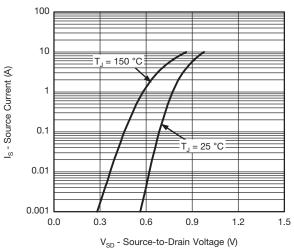
# TYPICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C, unless otherwise noted)

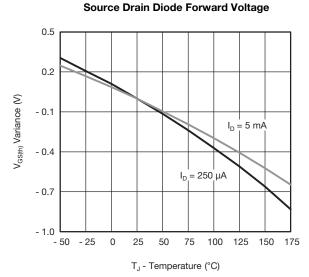


#### On-Resistance vs. Junction Temperature

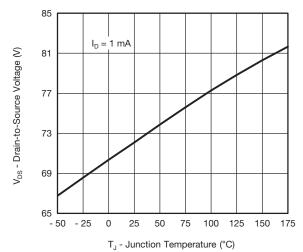


On-Resistance vs. Gate-to-Source Voltage





Threshold Voltage

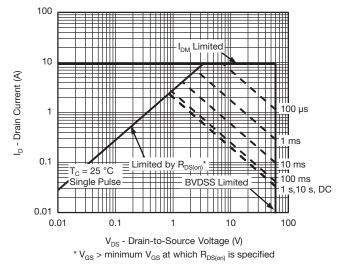


**Drain Source Breakdown vs. Junction Temperature** 

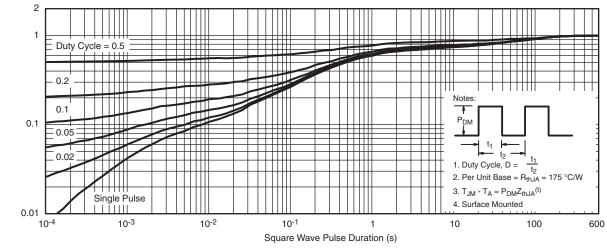


Normalized Effective Transient Thermal Impedance

# THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



#### Safe Operating Area

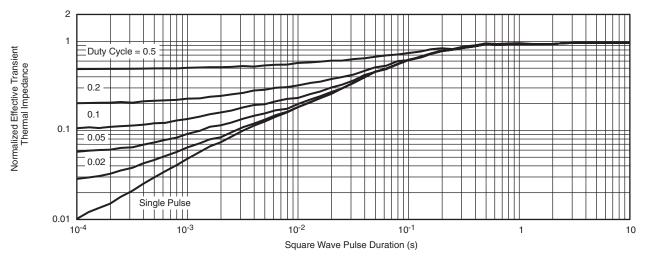


Normalized Thermal Transient Impedance, Junction-to-Ambient

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#### **THERMAL RATINGS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



#### Normalized Thermal Transient Impedance, Junction-to-Foot

#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <a href="https://www.vishay.com/ppg?65353">www.vishay.com/ppg?65353</a>.



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