



LOW FORWARD VOLTAGE SCHOTTKY BARRIER RECTIFIER

SRBL1030 THRU SRBL1060

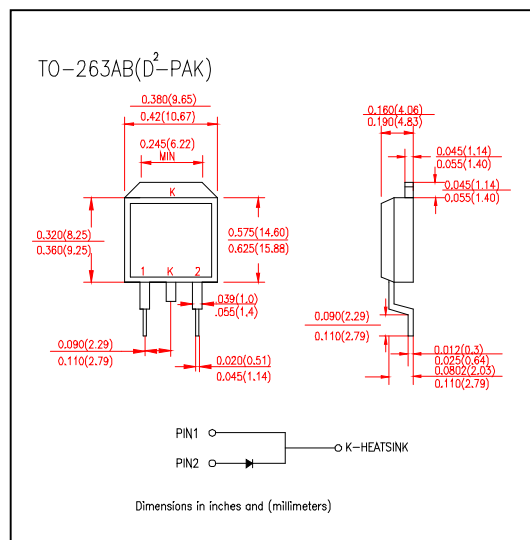
VOLTAGE RANGE 30 t o 60 Volts
CURRENT 10.0 Ampere

FEATURES

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High efficiency
- High Surge Capability
- High Current capacity and Low Forward Voltage Drop
- For use in low voltage high frequency inverters, Free wheeling, and polarity protection applications
- Plastic Material has UL Flammability Classification 94V-0

MECHANICAL DATA

- Case: D²-PAK molded plastic
- Terminals: Plated Lead solderable per MIL-STD-202 Method 208
- Polarity: See Diagram
- Weight: 1.7 grams (approx)
- Mounting Position: Any
- Marking: Type Number



MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

- Single Phase, half wave, 60Hz, resistive or inductive load
- For capacitive load derate current by 20%

	SYMBOLS	SRBL 1030	SRBL 1035	SRBL 1040	SRBL 1045	SRBL 1050	SRBL 1060	UNIT
Peak Repetitive Reverse Voltage	V_{RRM}	30	35	40	45	50	60	V
Working Peak Reverse Voltage	V_{RWM}							
DC Blocking Voltage	V_R							
RMS Reverse Voltage	$V_{R(RMS)}$	21	25	28	32	35	42	V
Average Rectified Output Current (Note 1)@T _c =95°C	I_O	10.0						A
Non-Repetitive Peak Forward Surge Current 8.3ms single half sine wave superimposed on rated load (JEDEC method)	I_{FSM}	250						A
Forward Voltage Drop @ I _F =10A, T _c =25°C	V_{FM}	0.60				0.75		V
Peak Reverse Current at Rated DC Blocking Voltage	T _c = 25°C	1.0						mA
	T _c = 100°C	50						
Typical Junction Capacitance(Note2)	C_j	700						pF
Typical Resistance Junction to case(Note1)	$R_{\theta JC}$	3.5						°C/W
Operating and Storage Temperature Range	T _J T _{STG}	(-55 to +150)						°C

Notes:

1. Thermal Resistance Junction to case mounted on heatsink
2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC



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RATING AND CHARACTERISTIC CURVES SRBL1030 THRU SRBL1060

FIG.1-FORWARD CURRENT DERATING CURVE

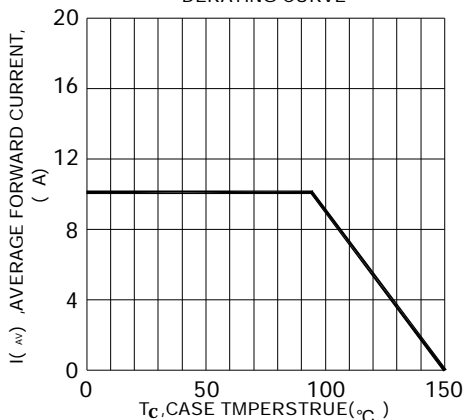


FIG.2-TYPICAL FORWARD CHARACTERISTICS

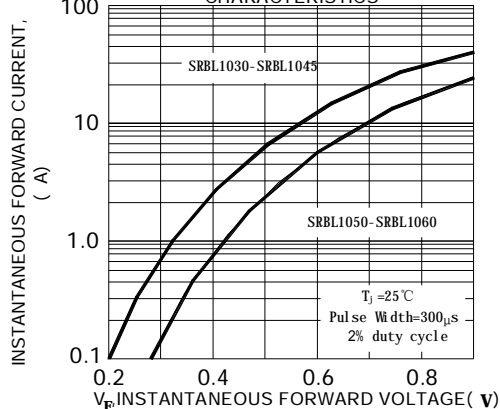


FIG.3 MAX NON-REPETITIVE SURGE CURRENT

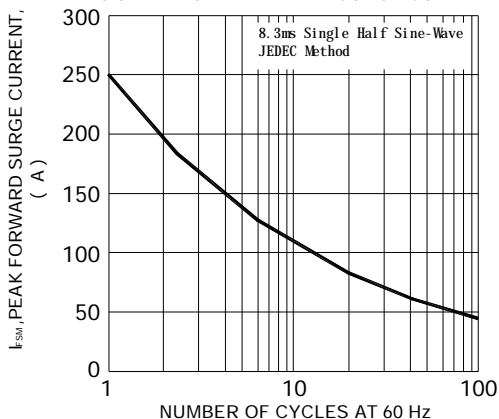


FIG.4 TYPICAL JUNCTION CAPACITANCE PER ELEMENT

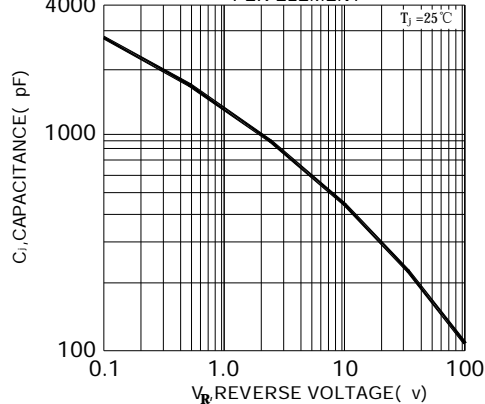


FIG.5-TYPICAL REVERSE CHARACTERISTICS

